**Single Wafer Wet Etch Processing**

**Backside and Bevel Edge Cleaning**

Chemical stripping of films on the wafer backside and bevel edge is increasingly important for integrated circuit manufacturing for 90 nm, 65 nm, and 45 nm. Contamination from new materials, such as dielectrics, metals, and protective backside coatings will need to be removed. Veeco’s single wafer technology meets these challenges, while attaining the lowest cost of ownership (COO).

**Backside Cleaning Technology**

Veeco’s patent pending gas seal spindle tools protect the active area of the wafer with a non-contacting cushion. The wafer floats on a 100 μm air cushion and is held in place by centrifugal forces with precision retaining rings. An available bevel ring utilizes a capillary action with the etch fluid to etch the bevel area with clean, crisp, sharp lines from an 0.8 mm bevel area. Veeco’s approach enables very low flow rate chemistry to be utilized that allows the use of single-pass chemistry.

**Chemical Separation and Recirculation**

For applications requiring chemical separation and recirculation, Veeco systems achieve efficiencies > 95% with simple gravity drainage only. There are no moving parts in the plumbing system, as chemistries are transported by pressure and vacuum for clean, low maintenance performance.

**Single Wafer Chemistry Technology**

Veeco systems implement closed loop processing with on-the-fly control for chemistry mixture and temperature, whether for one wafer or a batch of 25, by recipe. Chemistry can be mixed by recipe to the ppb level and temperature-controlled on the run, utilizing true single wafer technology. Recipes are controlled by SECS GEM management, and include hot lot capability. Individual data collection provides complete management information.

PC programmable temperature controllers provide on-the-fly control of the fluid dispensed. These units are specifically sized for the required flow rate and temperature rise. They are true “single wafer technology temperature controllers,” avoiding the cost and performance degradation of batch chemistry heaters.

*Veeco’s patent pending gas seal chuck includes an onboard final point-of-use filter for gas.*

*Chemistry transport is vacuum- and pressure-driven, featuring integrated chemical monitoring with adaptive management.*

*In-situ Collection Ring with Open and Closed Position"
In-situ Adaptive Process Control

Veeco’s WaferChek™ system provides further means to control the single wafer processing with process status feedback for film strip cleaning control. Wet etching rates can be controlled by traditional techniques of managing chemistry temperature, flow rate, and concentration. Control of the etch process can be determined automatically by the optical properties of the surface. The result is uniform film conditions, wafer to wafer, despite process variables. This is advantageous for both single pass and recirculated chemistry applications. Several examples that follow show the effectiveness of this technique.

8000 Å SiO2 Backside Strip

The color change went through approximately 1 of the 5 color cycles. About 20% of the SiO2 was removed (1500 Å) in the 10 seconds of etch time.

Process parameters: HF 49%, < 100 ml/minute flow rate; etch rate 15,000 Å/minute @ 20 °C

1,500 Å Si3N4 Backside Strip

Nitride film wafers were completely stripped in 200 seconds by processing with controlled chemistry temperature and flow rate.

Process parameters: HF 49%, < 400 ml/minute flow rate; etch rate 600 Å/minute @ 50 °C

1,500 Å Poly Silicon

Process parameters: HNA (HF:HNO3:CH3COOH:H2O, 3:123:6:63), < 350 ml/minute flow rate; etch rate of 8,000 Å/minute @ 20 °C

Copper Removal

We see from the chart that the first 40 seconds are utilized to strip the copper layer and then the insulating layer appears in the seconds that follow.

Process parameters: H2SO4:H2O2:DI, 10:10:4,000, 100 ml/minute flow rate; etch rate 2,000 Å/minute @ 25 °C

Veeco Systems for Effective, Backside Wafer Cleaning Performance

Veeco systems are available in sizes and configurations suitable for your production requirements. All systems are SEMI® S2-0703e Safety and SEMI S8 Ergonomics Compliant, CE Marked, and ETL Listed.

Learn more about Veeco’s single wafer process capabilities at www.veeco.com/PSP

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