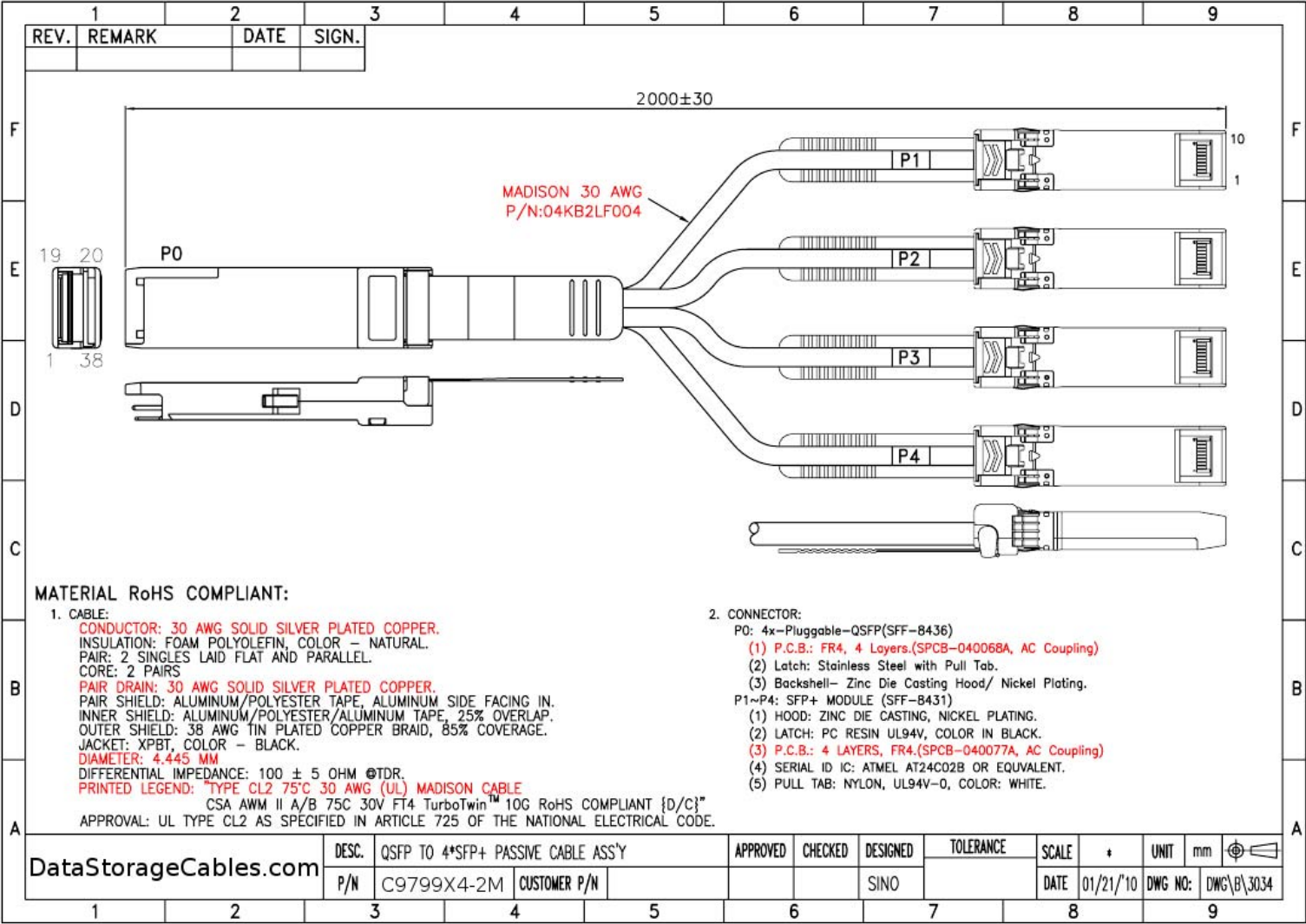




	1	2	3	4	5	6	7	8	9
	REV.	REMARK	DATE	SIGN.					
F									F
E									E
D									D
C									C
B									B
A									A

Wiring Diagram

High Speed Signal

P0(4x-Pluggable-QSFP)		SFP+		
Pad	Signal	Pad	Signal	
36	TX1p	13	RX+	P1
37	TX1n	12	RX-	
18	RX1n	19	TX-	P2
17	RX1p	18	TX+	
GND Group GND		GND Group GND		
03	TX2p	13	RX+	P3
02	TX2n	12	RX-	
21	RX2n	19	TX-	P4
22	RX2p	18	TX+	
GND Group GND		GND Group GND		
33	TX3p	13	RX+	P1
34	TX3n	12	RX-	
15	RX3n	19	TX-	P2
14	RX3p	18	TX+	
GND Group GND		GND Group GND		
06	TX4p	13	RX+	P3
05	TX4n	12	RX-	
24	RX4n	19	TX-	P4
25	RX4p	18	TX+	
GND Group GND		GND Group GND		
GND Group: 01,04,07,13,16,19 20,23,26,32,35,38		P1~P4 GND Group: 01,02,06,08,10, 11,14,17,20		
Connector Shell		P1~P4 Connector Shell		

Low Speed Signal & Power

P0(4x-Pluggable-QSFP)

Pad	SIGNAL	
08	ModSelL	
09	ResetL	
10	VccRx	
11	SCL	
12	SDA	
27	ModPrsL	
28	IntL	
29	VccTx	
30	Vcc1	
31	LPMode	

Low Speed Signal & Power

P1~P4(SFP+)

Pad	Signal	
03	TX_Disable	
04	SDA	
05	SCL	
07	RS0	
09	RS1	
15	VCCR	
16	VCCT	

*DC Blocking Caps on P0~P4 Rx Side.

DataStorageCables.com

DESC. QSFP TO 4*SFP+ PASSIVE CABLE ASS'Y
P/N C9799X4-xM CUSTOMER P/N

APPROVED
CHECKED
DESIGNED Sino

TOLERANCE
SCALE

DATE 02/01/'12

UNIT mm
DWG NO: DWG\B\3837

1 2 3 4 5 6 7 8 9