

UaDAP-a Modular Approach to Software Design

Michael Krenz

United Technologies Corporation

Aerospace Systems

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“Verification is not simply testing. Testing, in general, cannot show the absence of errors.”

- Section 6.0

“The structural coverage analysis may be performed on the Source Code, unless the software level is A and the compiler generates object code that is not directly traceable to Source Code statements. Then, additional verification should be performed on the object code to establish the correctness of such generated code sequences.” –
Section 6.4.4.2

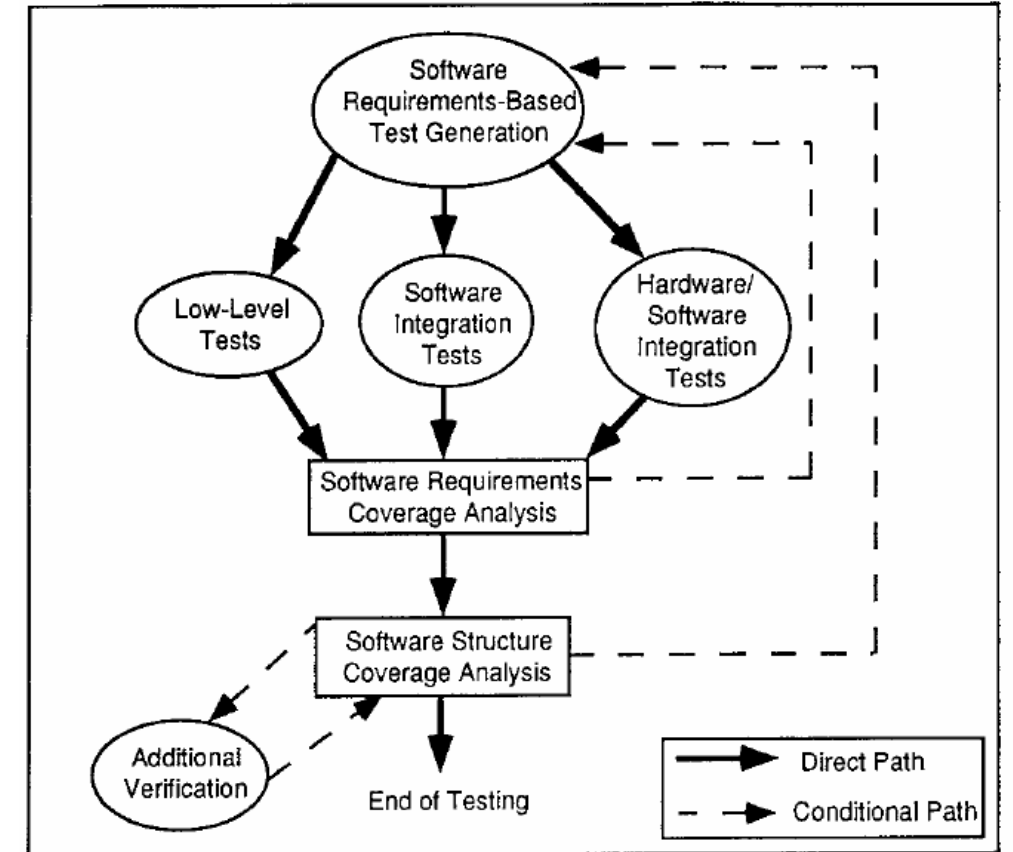


FIGURE 6-1
SOFTWARE TESTING PROCESS

UaDAPT Patent Overview

Software Block Diagram

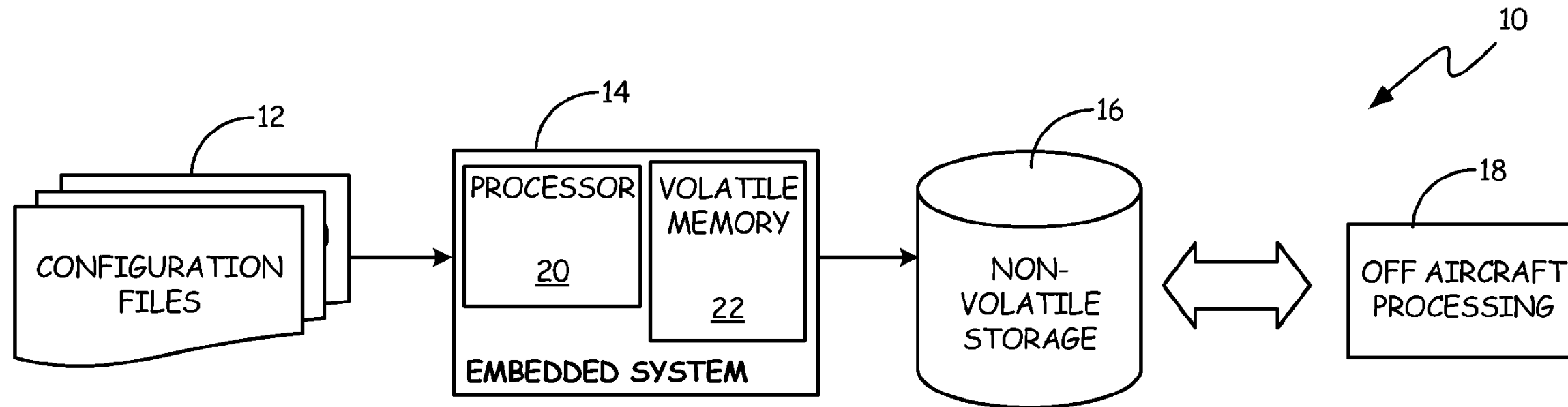


FIG. 1A

UaDAPT Patent Overview

- Modifiable Logic without white box regression testing
- US Patent 9,213,626
 - Specified data stored in ring buffers
 - Ring buffer transferred to non-volatile when trigger condition is met
- US Patent application
 - Extends 9,213,626
 - Modifiable trigger logic and I/O storage definition in flight
 - Transmission of stored data in flight

