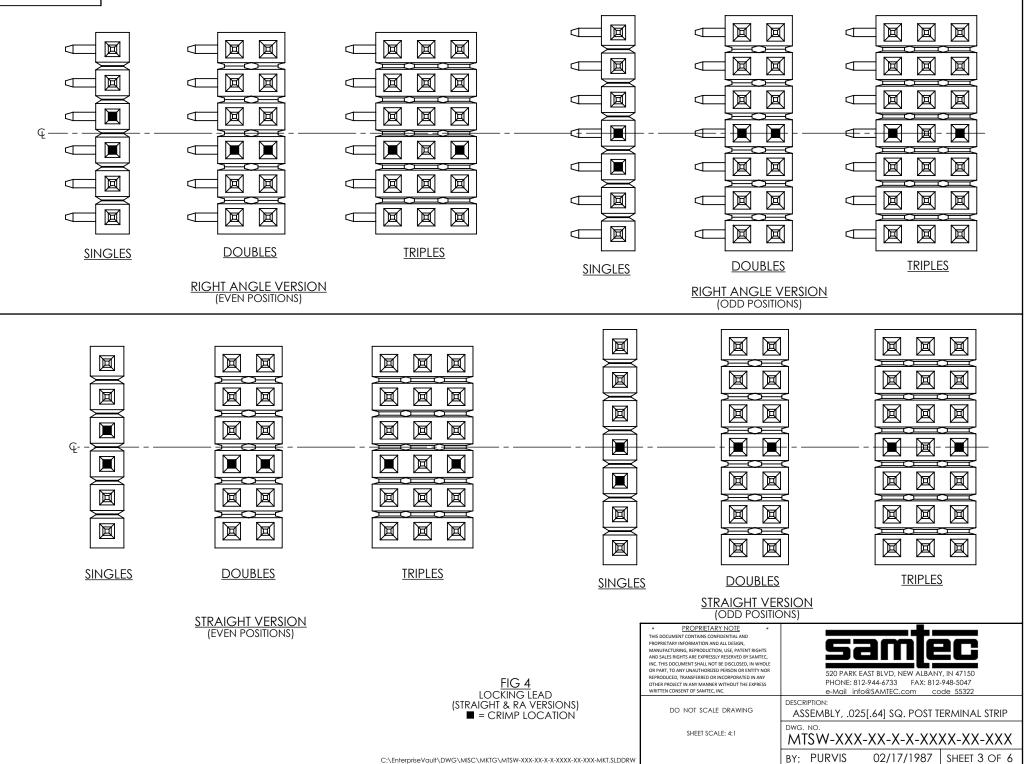
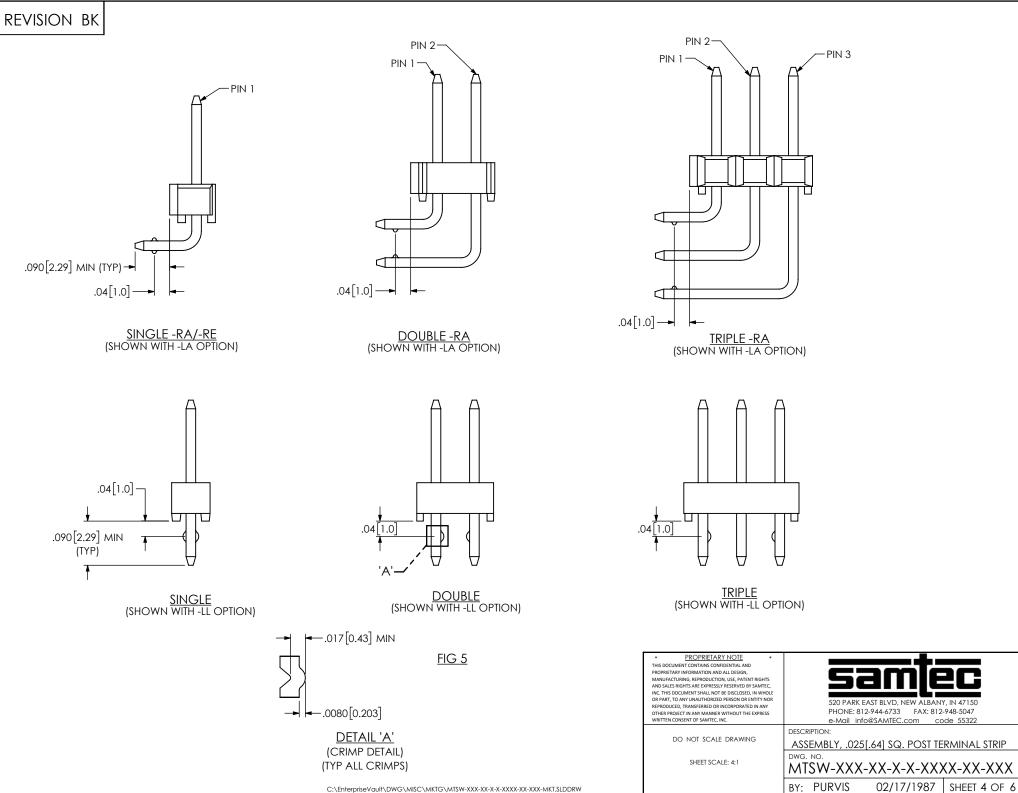


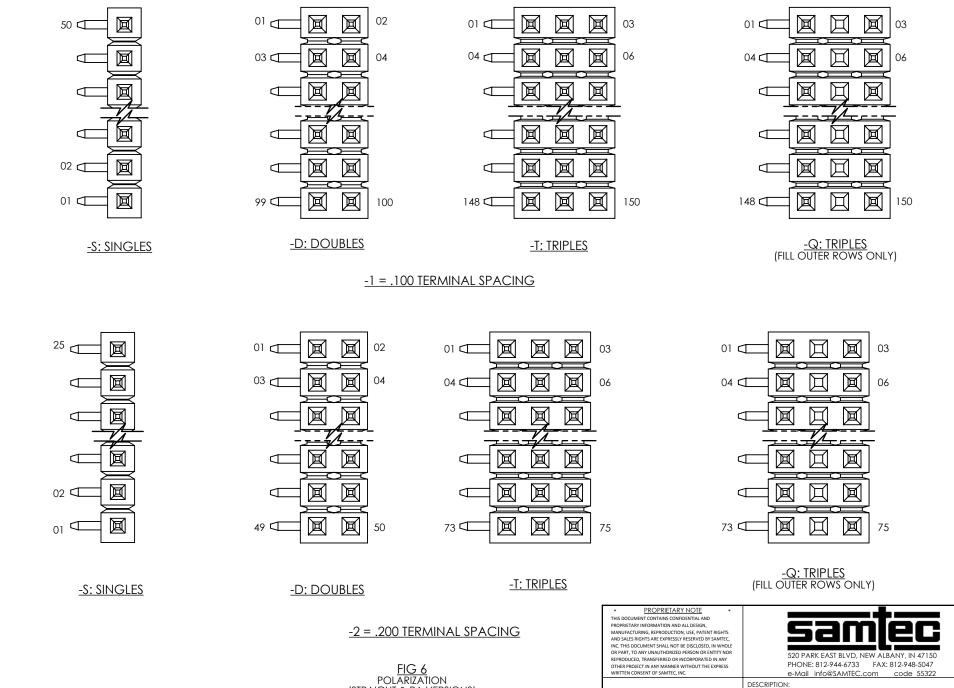
C:\EnterpriseVault\DWG\MISC\MKTG\MTSW-XXX-XX-XXX-XXX-XXX-MKT.SLDDRW

**REVISION BK** 





## **REVISION BK**



(STRAIGHT & RA VERSIONS)

C:\EnterpriseVault\DWG\MISC\MKTG\MTSW-XXX-XX-XXXX-XXX-XXX-MKT.SLDDRW

DO NOT SCALE DRAWING

SHEET SCALE: 4:1

DWG. NO.

BY: PURVIS

ASSEMBLY, .025[.64] SQ. POST TERMINAL STRIP

MTSW-XXX-XX-X-X-XXXX-XXX-XXX

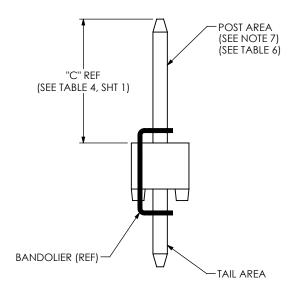
02/17/1987

SHEET 5 OF 6

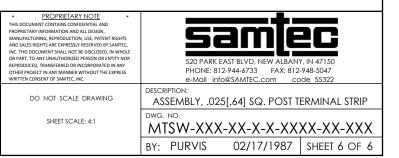
## **REVISION BK**

	1	BLE 6	
LEAD STYLE	NUMBER OF BAND MARKS ALLOWED II POST AREA		
	PIN #1	PIN #2	PIN #3
*-05	1	N/A	N/A
-06	1	N/A	N/A
-07	1	1	1
-07-RA	0	0	0
-08	2	2	2
-08-RA	0	0	0
-08-RE	0	0	0
*-09	2	2	2
*-09-RA	1	0	0
*-09-RE	0	0	0
*-10	2	2	2
*-10-RA	1	0	0
*-10-RE	0	0	0
*-11	2	2	2
*-11-RA	1	0	0
*-11-RE	0	0	0
*-12	2	2	2
*-12-RA	1	0	0
*-12-RE	0	0	0
*-13	2	2	N/A
*-13-RA	1	0	N/A
*-13-RE	0	0	N/A
*-21	2	N/A	N/A
*-21-RA	1	N/A	N/A
*-21-RE	0	N/A	N/A
-22	2	2	2
*-22-RA	1	0	0
*-22-RE	0	0	0
-23	2	N/A	N/A
-23-RA	0	N/A	N/A
-24	2	N/A	N/A
-24-RA	0	N/A	N/A
*-27	2	N/A	N/A
*-27-RA	1	N/A	N/A
*-27-RE	0	N/A	N/A
*-28	2	2	N/A
*-28-RA	1	0	N/A
*-28-RE	0	0	N/A

\* PINS TO BE FLIPPED OPPOSITE OF SELECTIVE PLATING BEFORE BEING INSERTED



IN PROCESS 1 -T PLATING ONLY



C:\EnterpriseVault\DWG\MISC\MKTG\MTSW-XXX-XX-XXX-XXX-XXX-MKT.SLDDRW