

COVER

Schematics Index

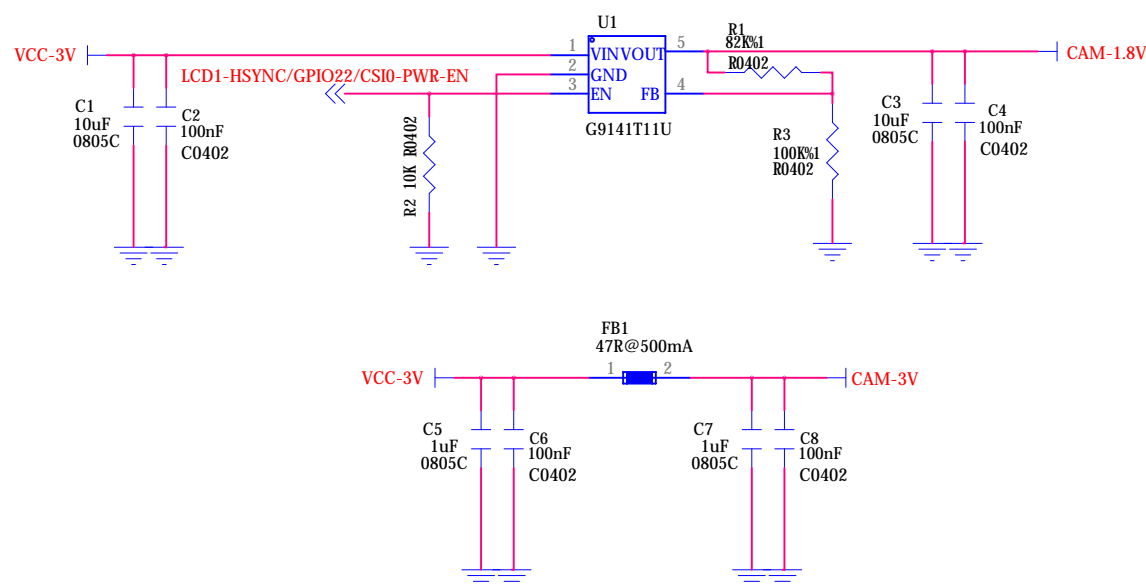
- 01 COVER
- 02 BLOCK
- 03 GPIO ASSIGNMENT
- 04 POWER
- 05 A20_CORE
- 06 HDM-SATA-SD
- 07 USB
- 08 EMAC
- 09 ARDUINO_SOCKET
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REVISION HISTORY

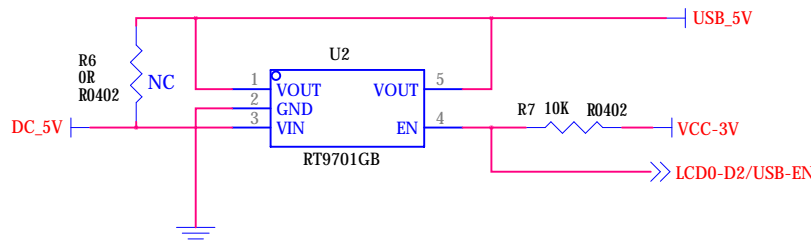
Rev	Description	Date	Drawn	Checked	Approved
A20_pcduino_V1.0		2013-04-05	CWL		

LINKSPRITE		
A20_pcduino_V1.0		
DSN	Maple	CHKD
STAD	APPD	
DATE: 2014-04-05	SHEET	1 OF 16

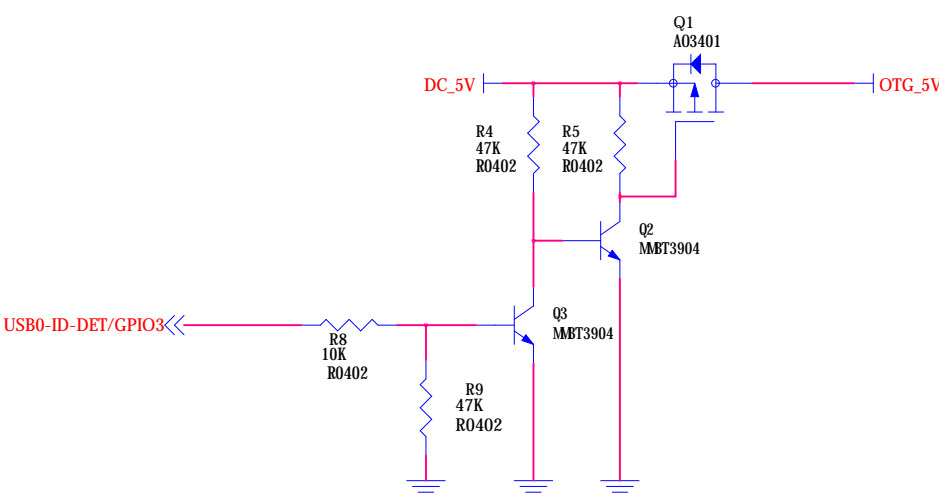
Camera Power



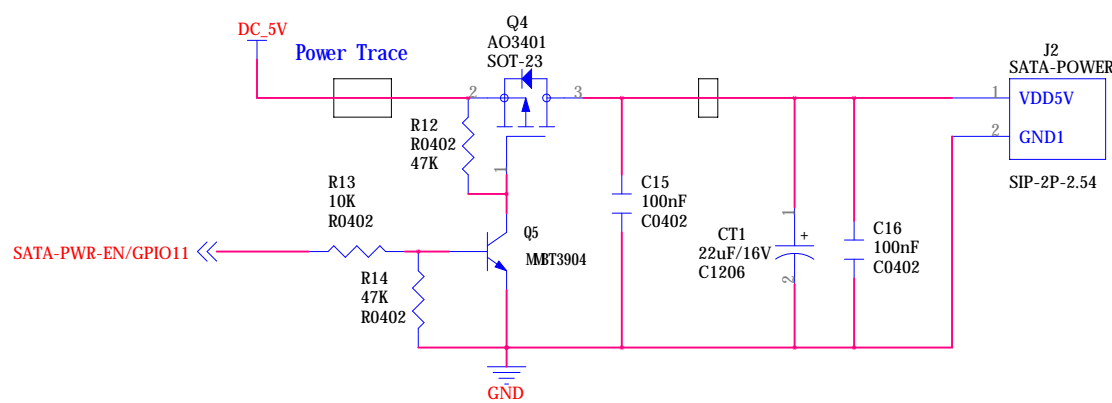
USB Power



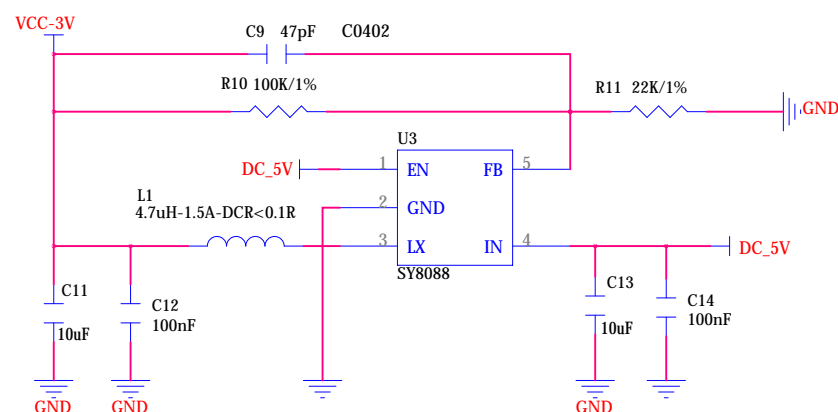
USB OTG Power



SATA Power

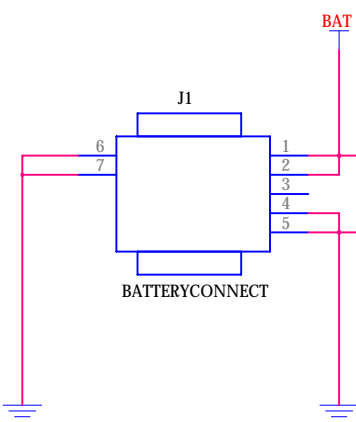


DC5V-DC3. 3V

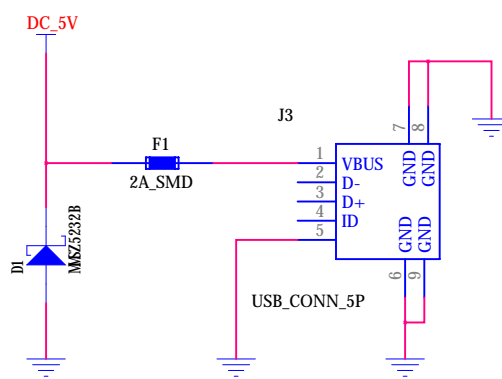


$$V_{out} = 0.6 * (1 + R1/R2)$$

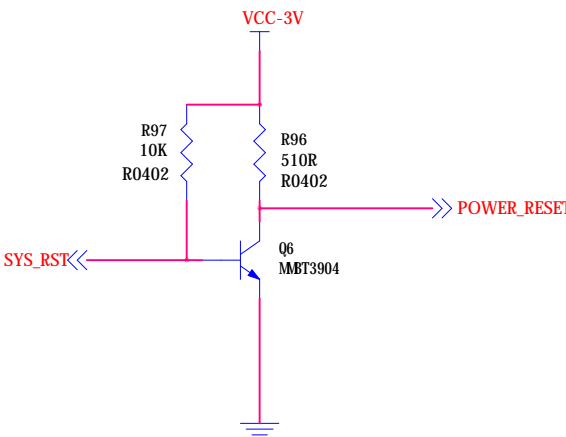
Battery Socket



Power IN Socket



RESET



LINKSPRITE

A20_pcdduino_V1.0

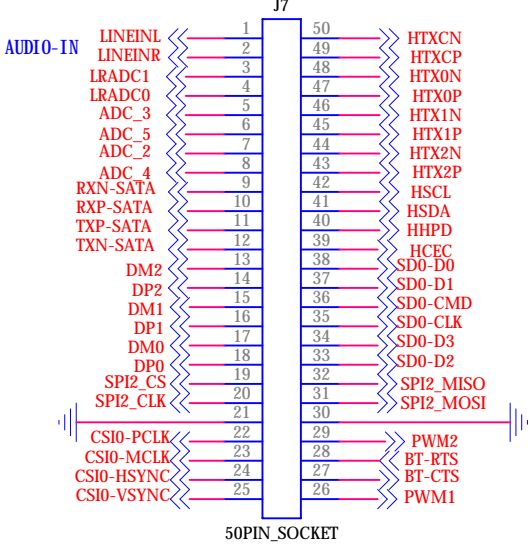
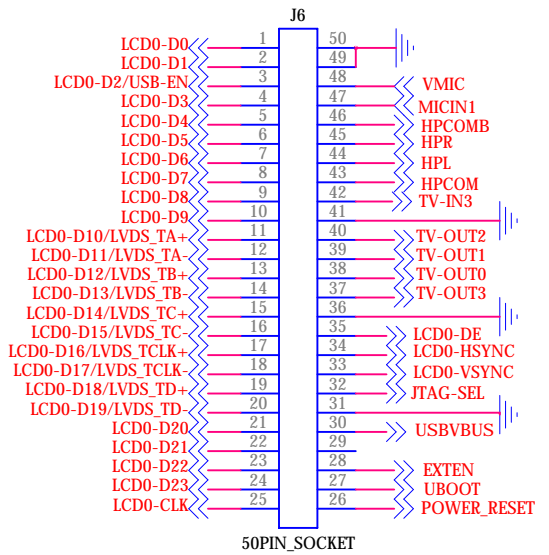
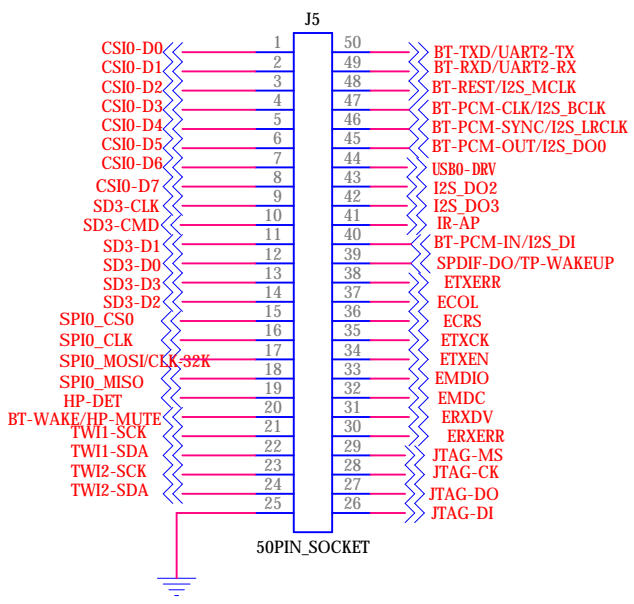
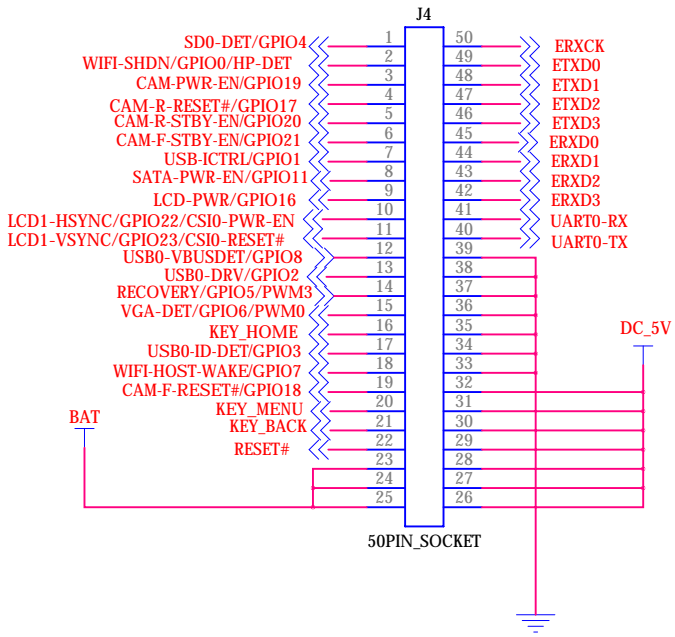
DSN Maple CHKD

STAD APPD

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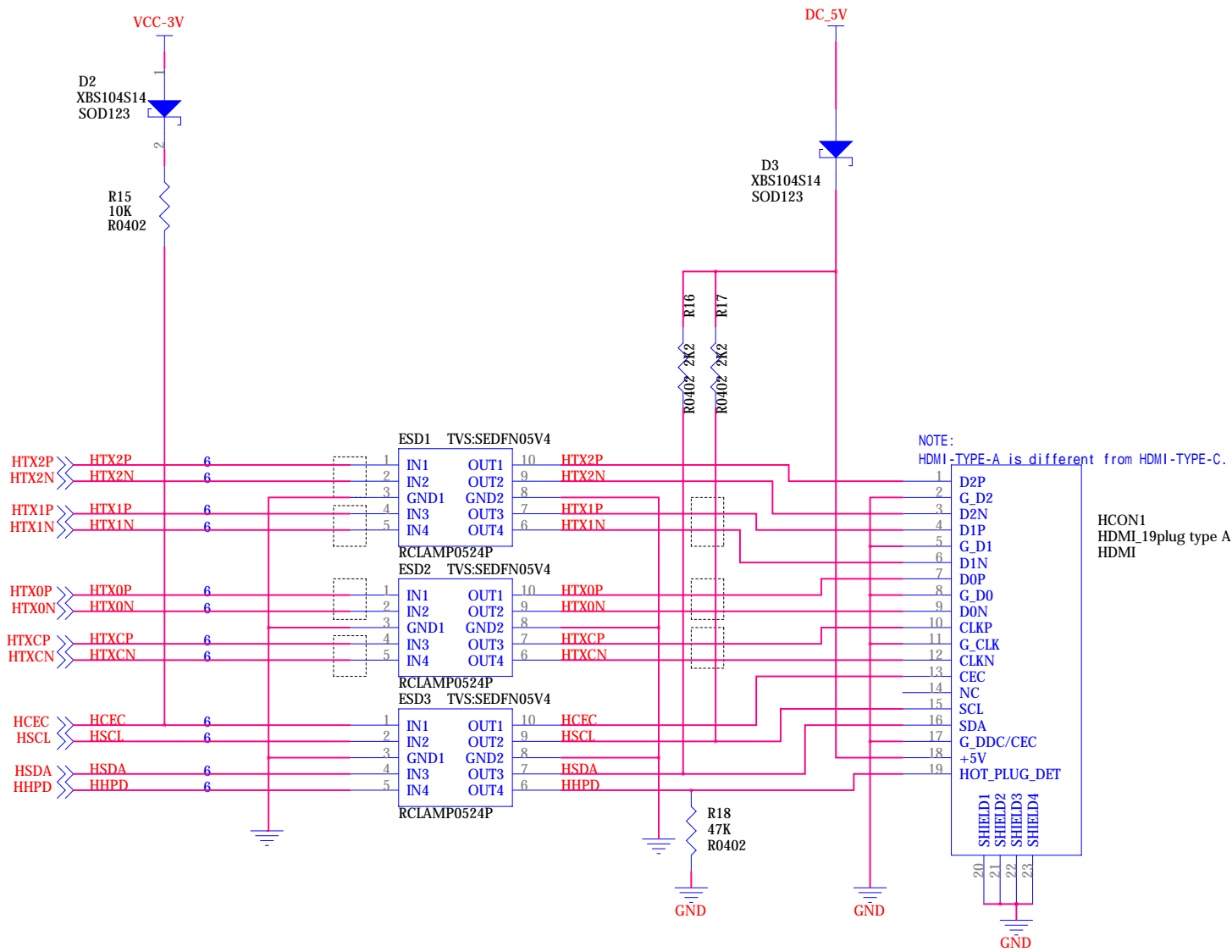
SOCKET



LINKSPRITE		
A20_pcdduino_V1.0		
DSN	Maple	CHKD
STAD		APPD
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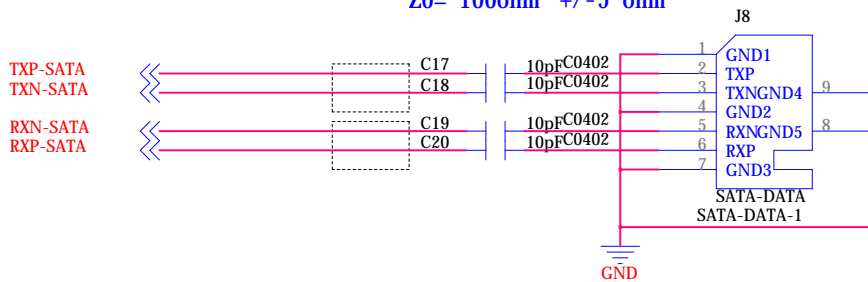
HDMI

Differential pairs
Z0= 100 ohm +/-5 ohm



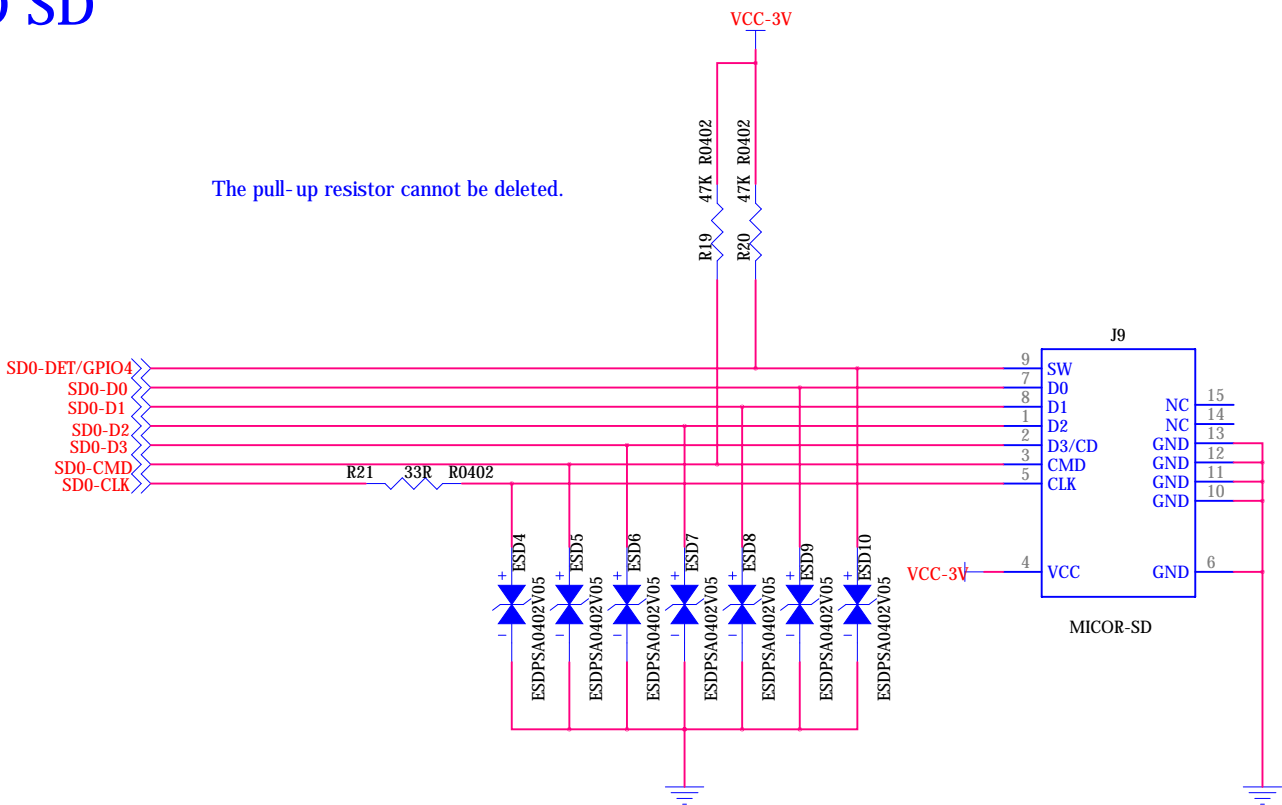
SATA

Differential pairs
Z0= 100ohm +/-5 ohm



MICRO SD

The pull-up resistor cannot be deleted.



LINKSPRITE

A20_pcduino_V1.0

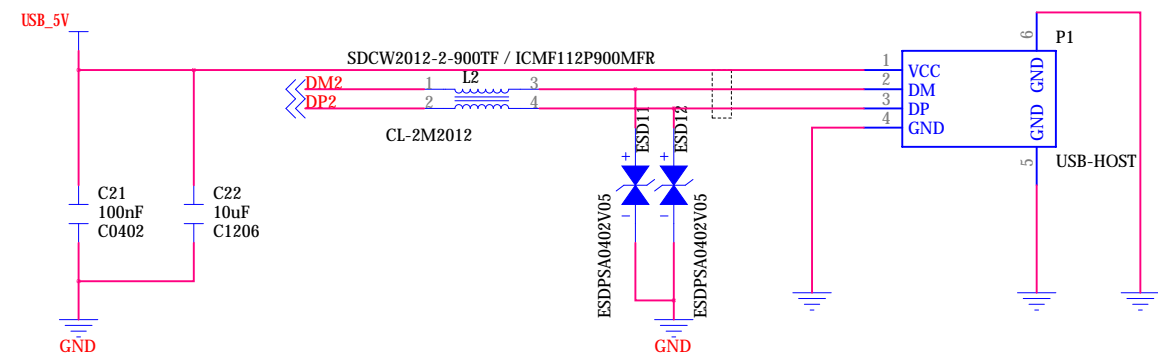
DSN Maple CHKD

STAD APPD

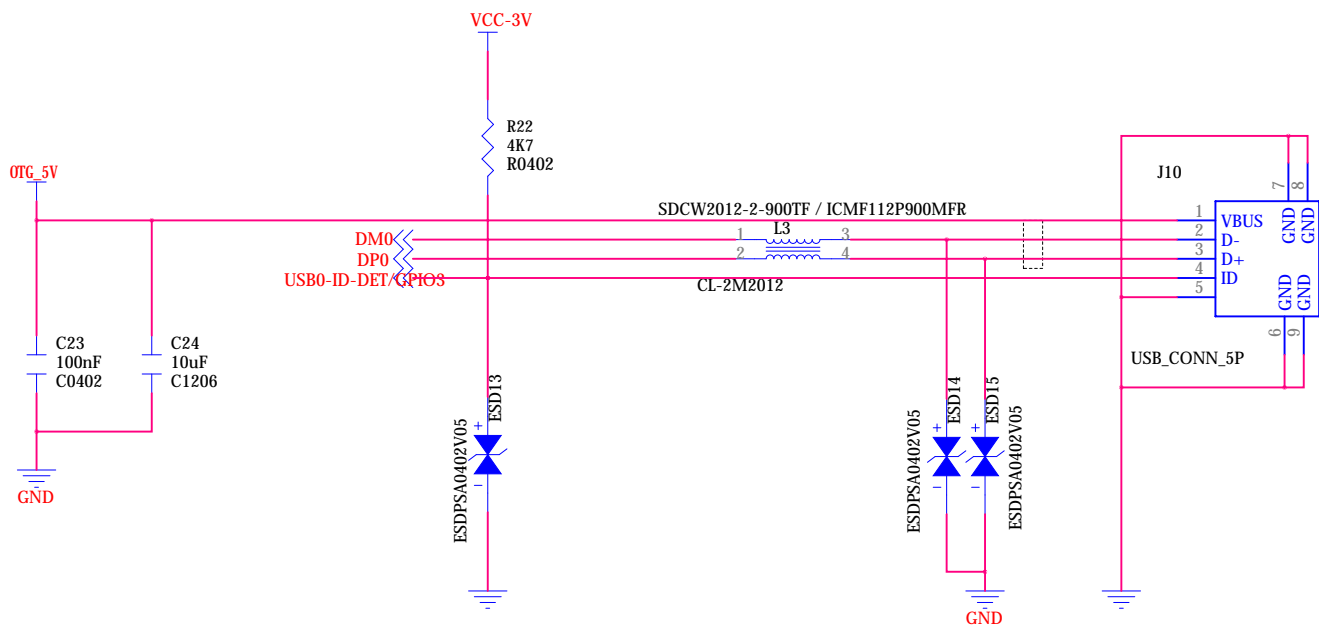
DATE: 2014-04-05

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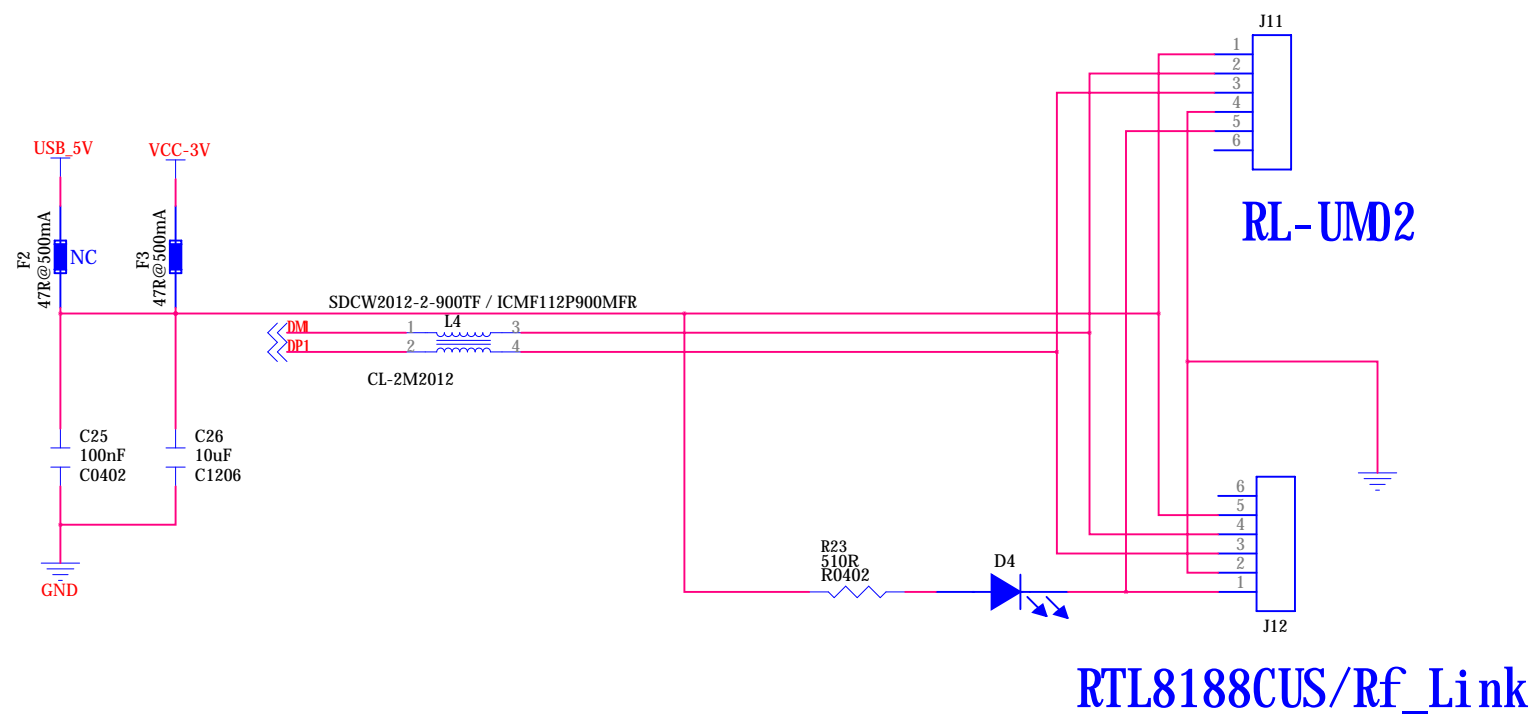
USB_HOST



USB_OTG



USB_WIFI



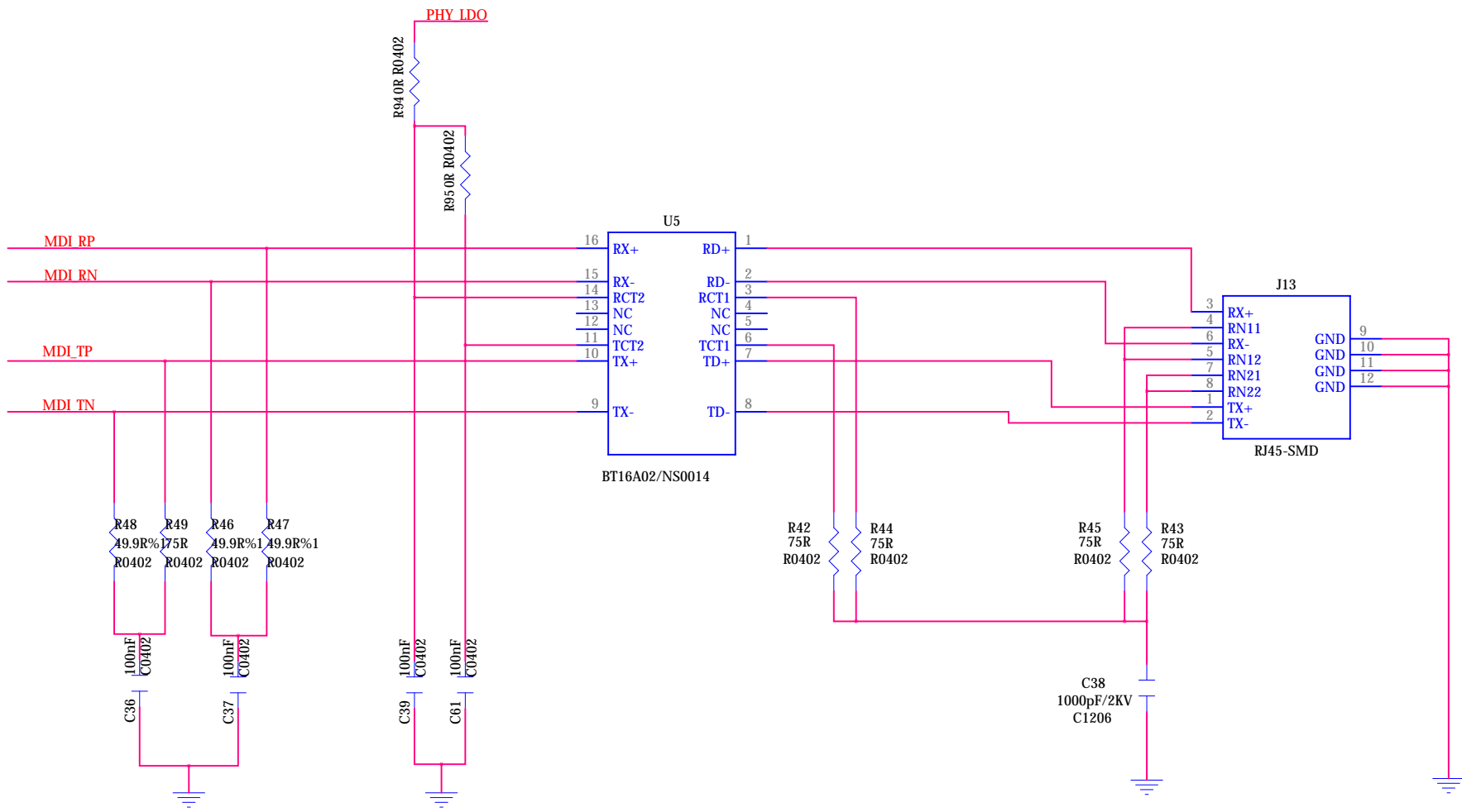
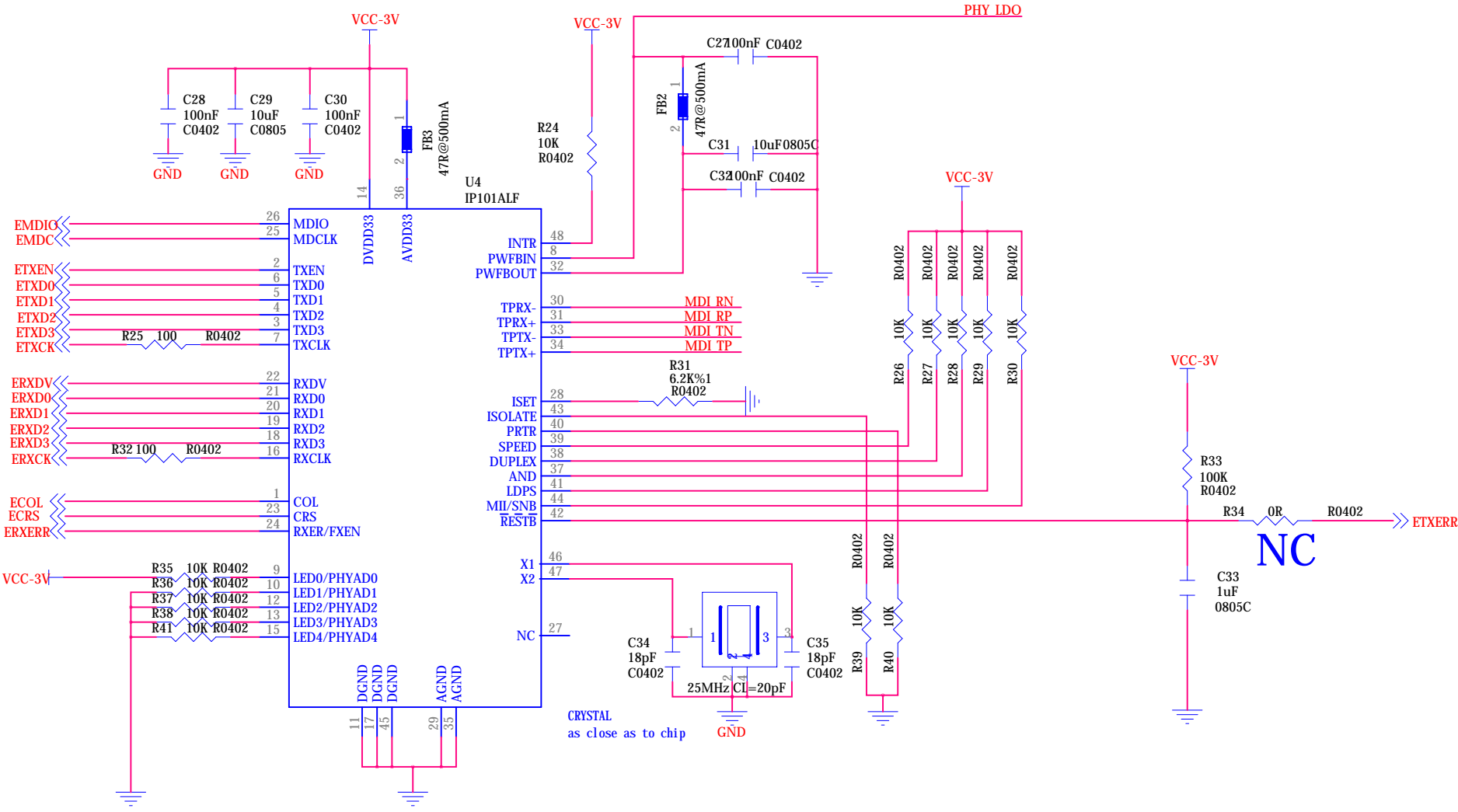
LINKSPRITE

A20_pcduino_V1.0

DSN Maple CHKD

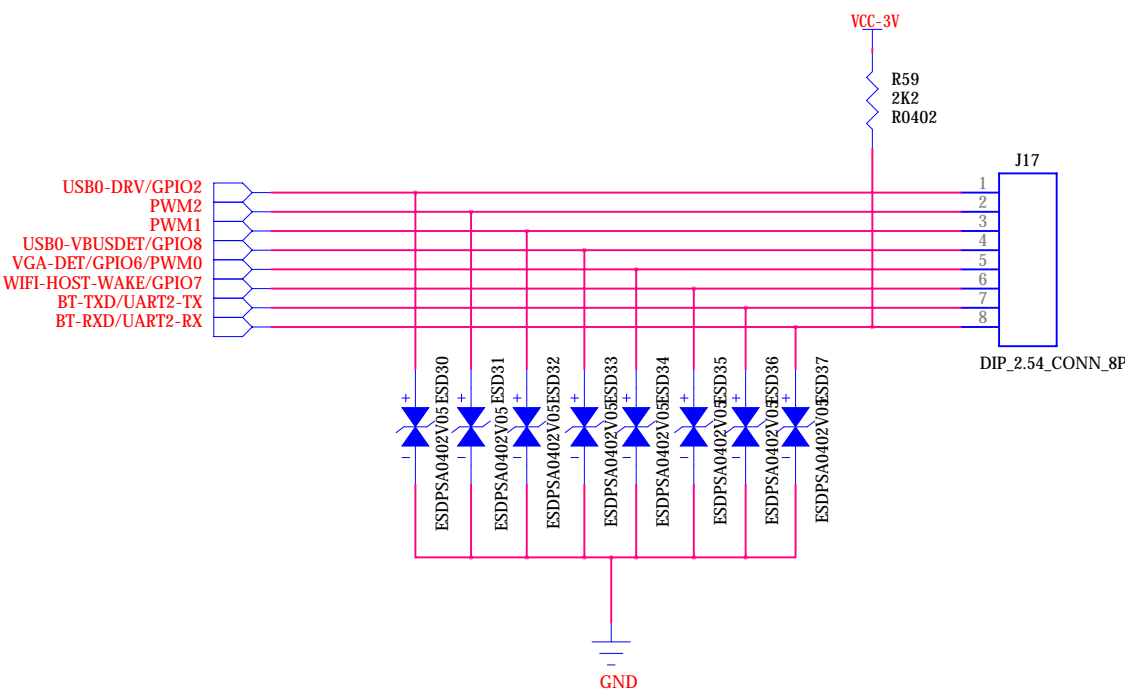
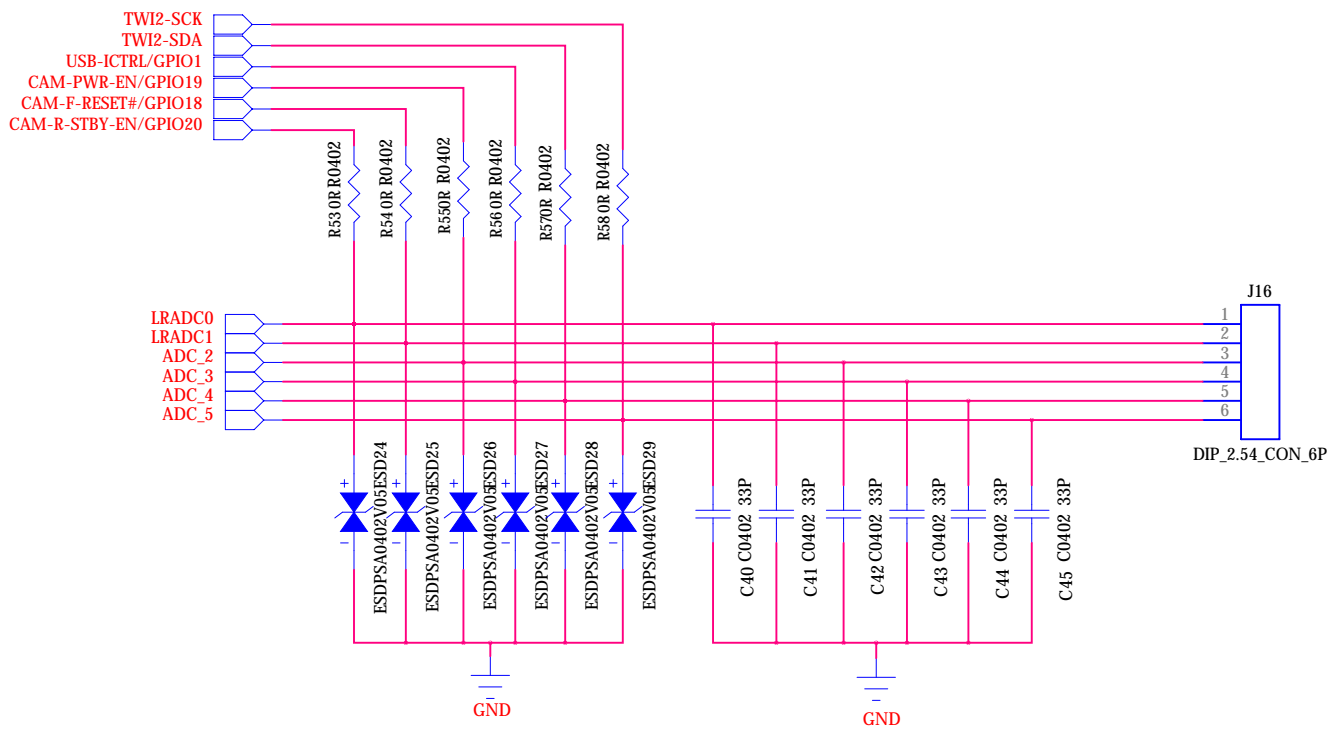
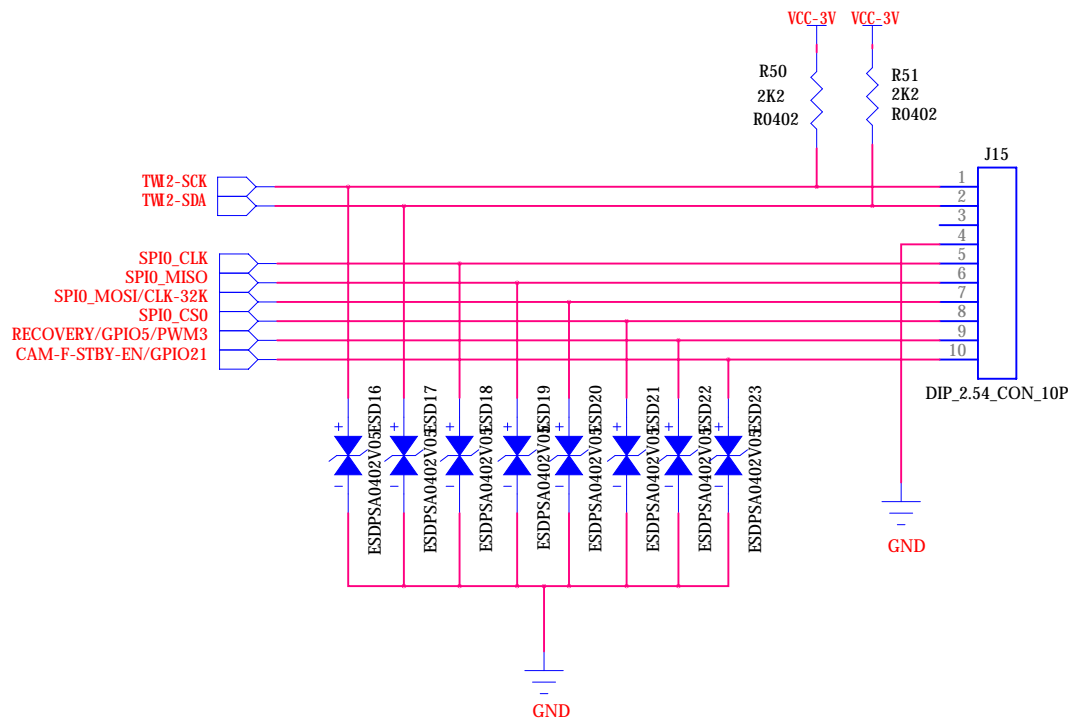
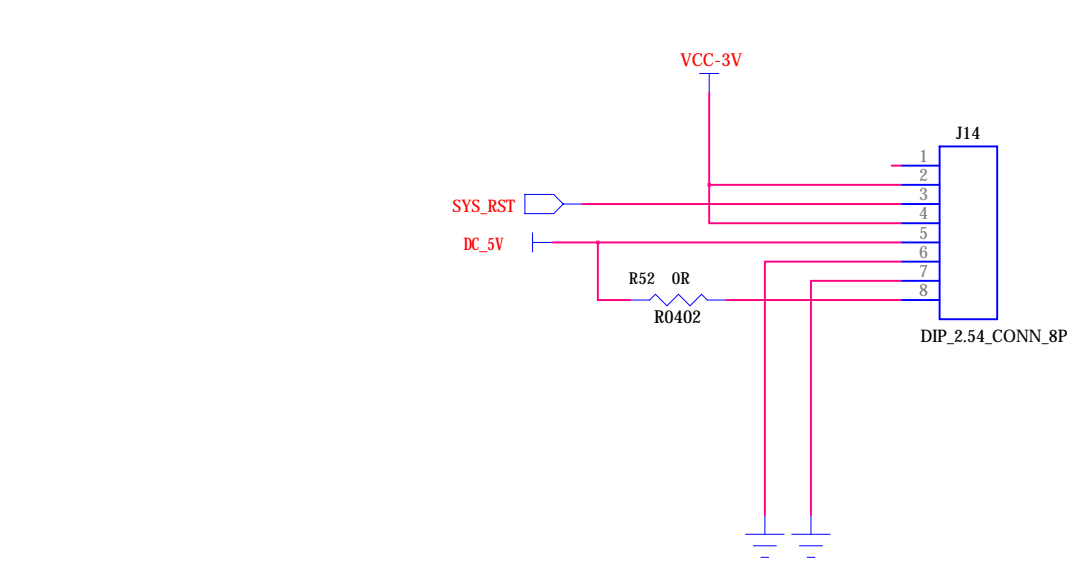
STAD APPD

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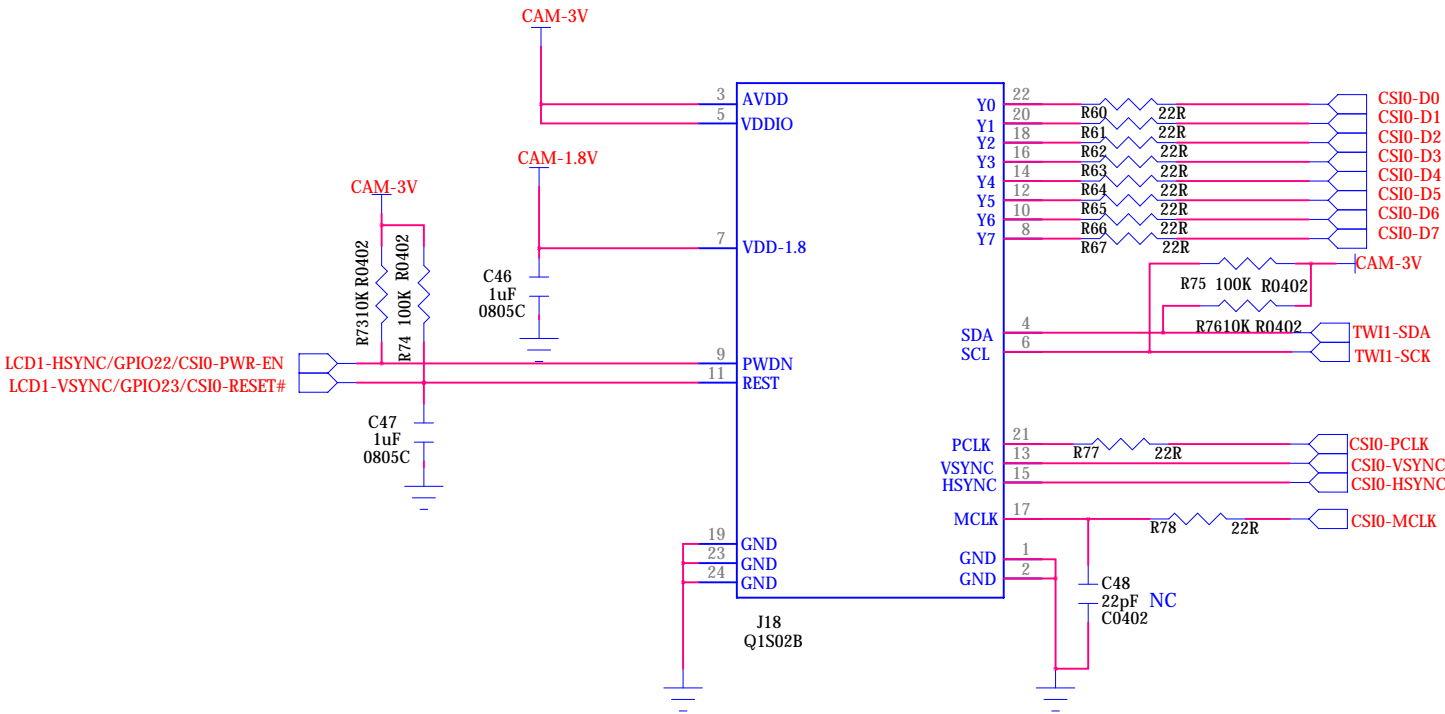


	Pull high	Pull down
CRS	TEST mode	Normal mode
TEST	RMII mode	MII mode
COL/RMII	LED mode1	LED mode0
RX_DV		

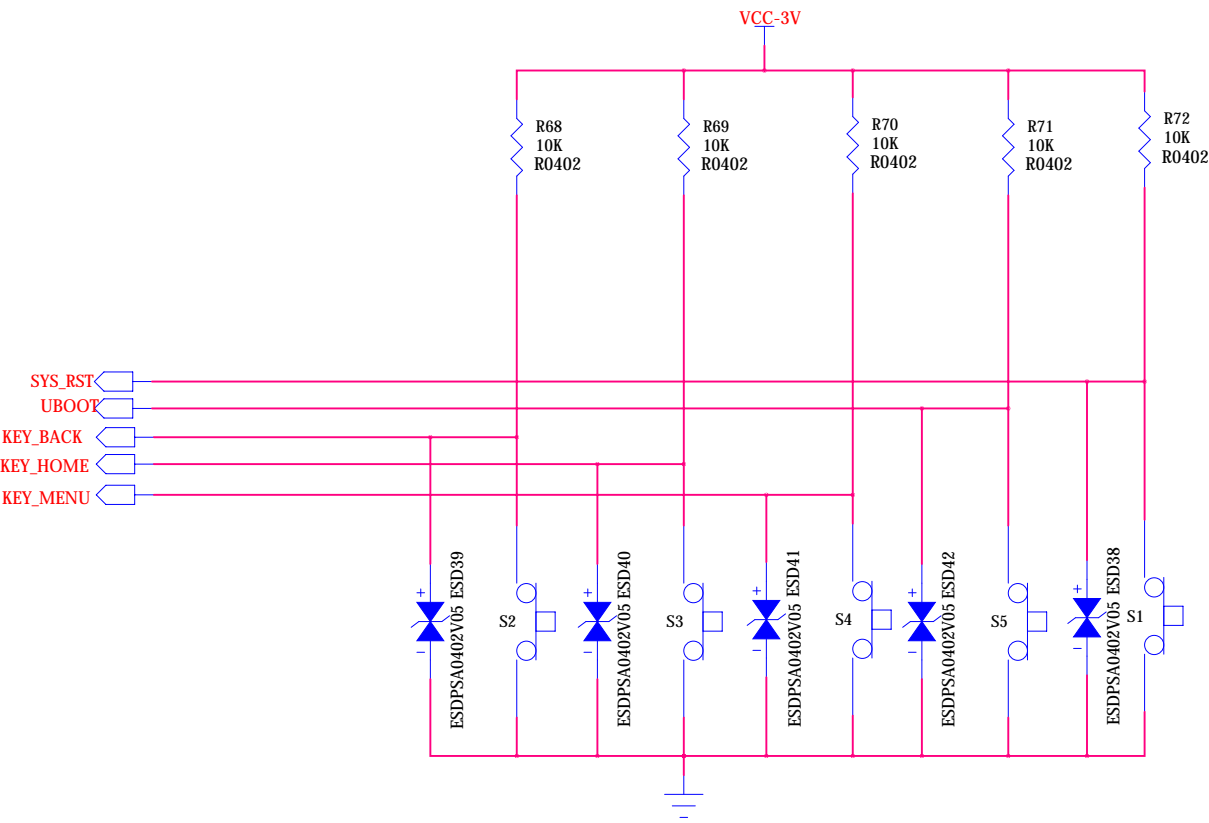
LINKSPRITE		
A20_pcdduino_V1.0		
DSN	Maple	CHKD
STAD		APPD
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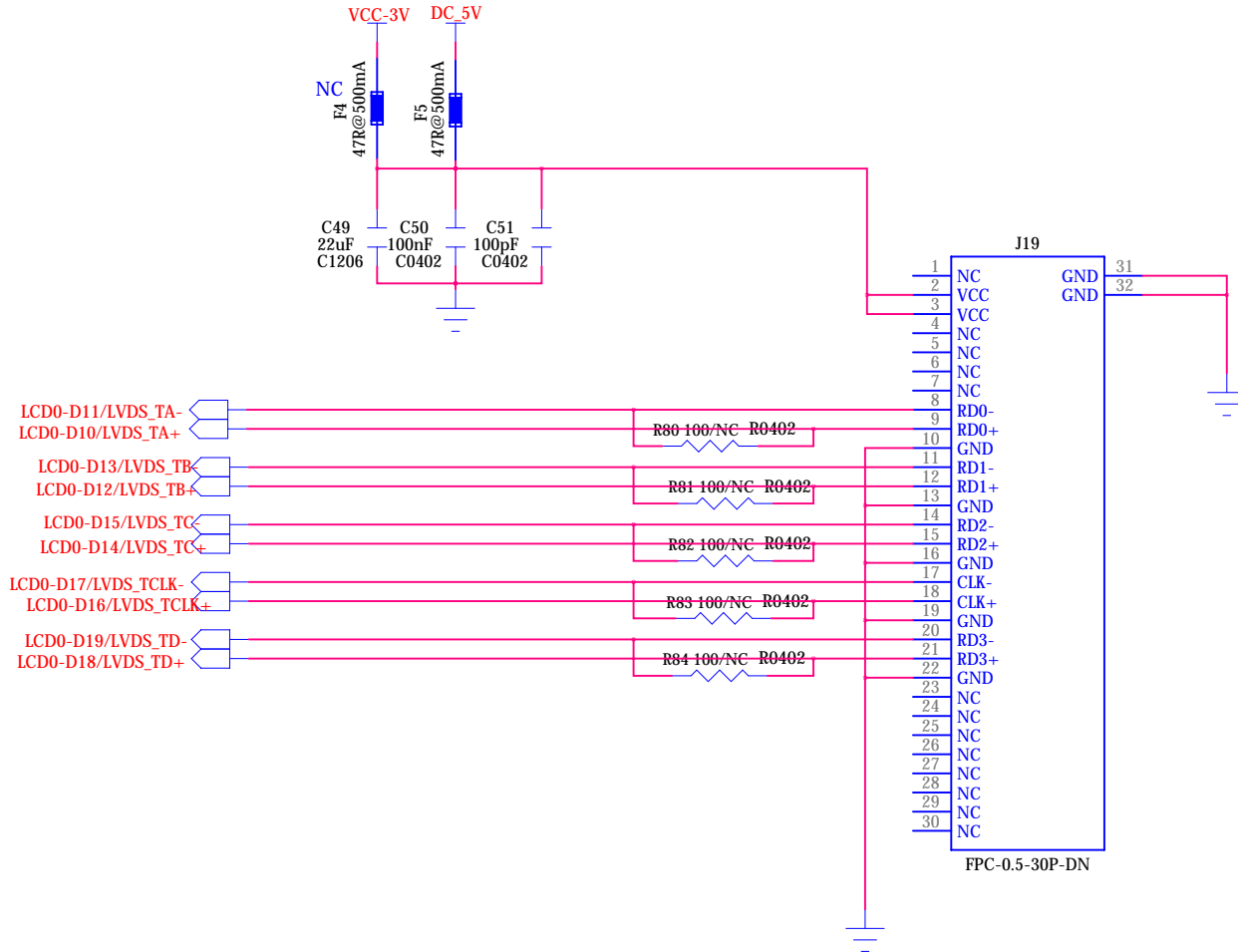
LINKSPRITE		
A20_pcduino_V1.0		
DSN	Maple	CHKD
STAD		APPD
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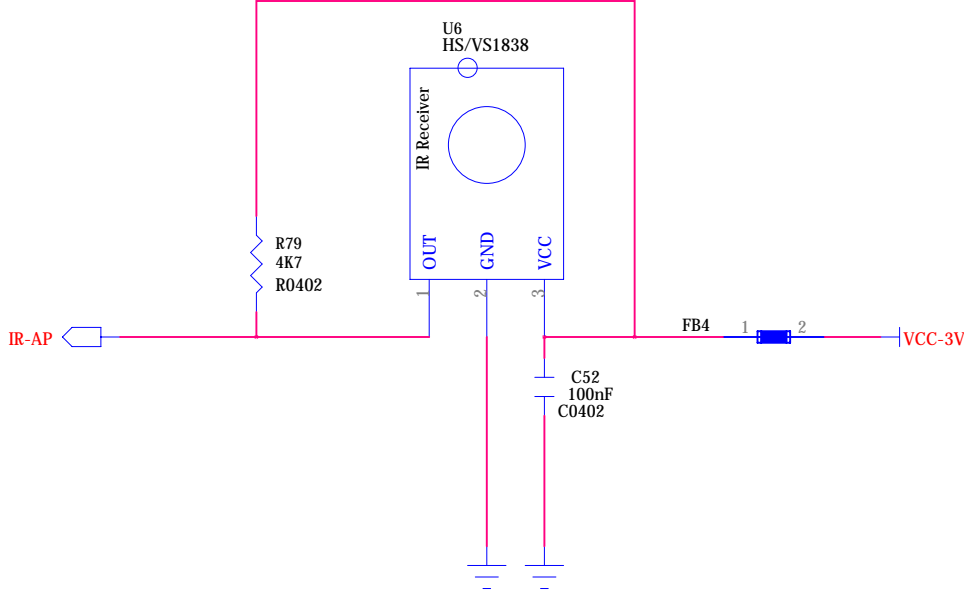
Camera



KEY



LVDS LCD



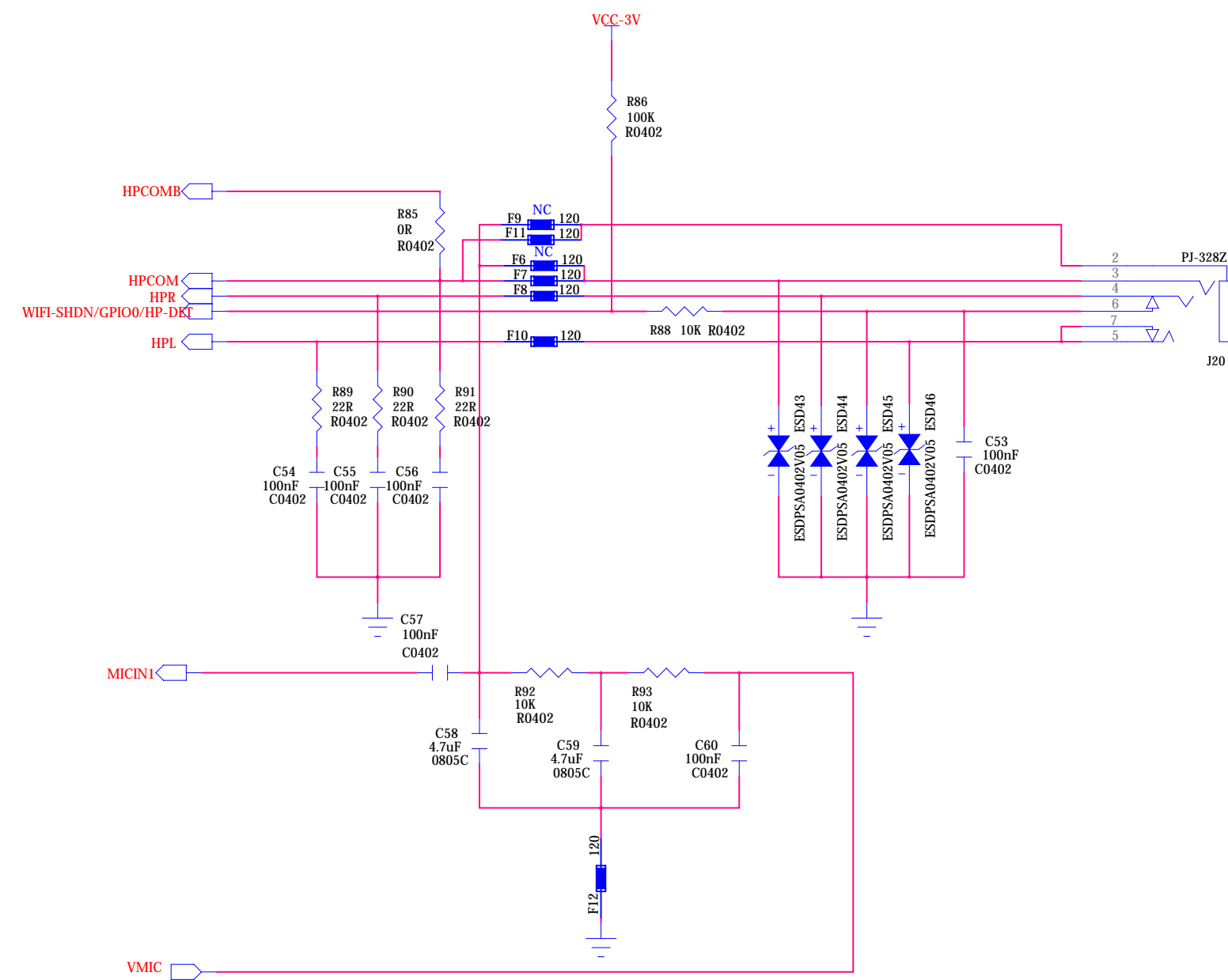
LINKSPRITE

A20_pcduino_V1.0

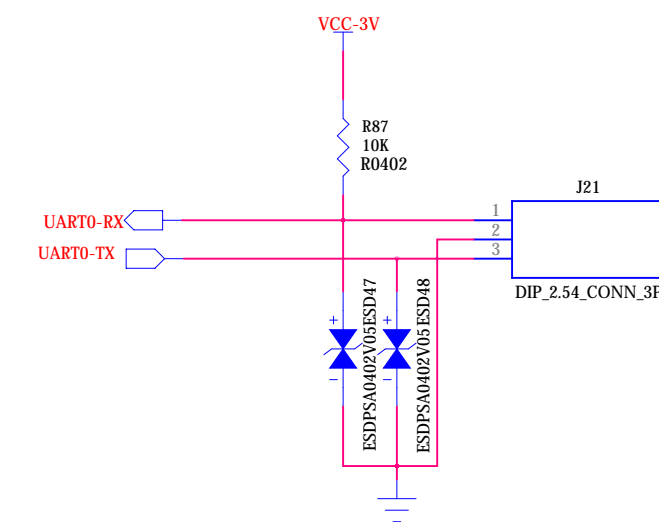
DSN Maple CHKD

STAD APPD

Audio/Mic



Debug



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A20_pcduino_V1.0	
DSN	Maple
STAD	CHKD
	APPD
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