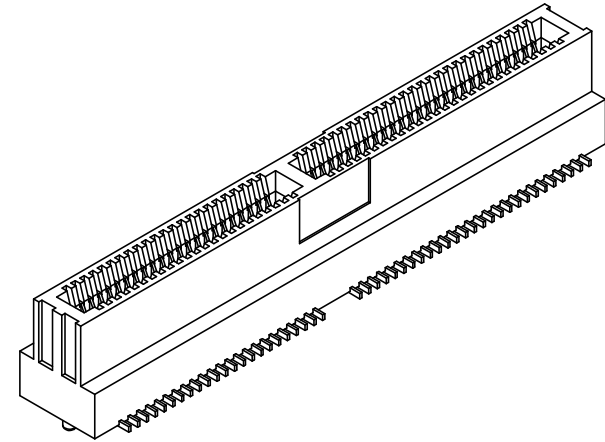
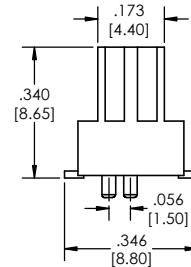
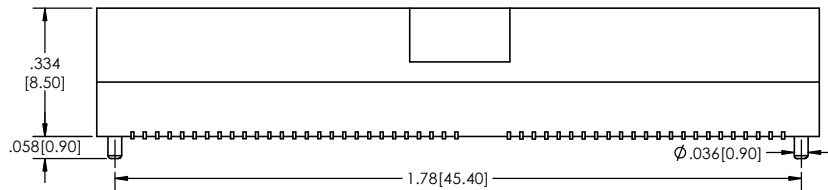
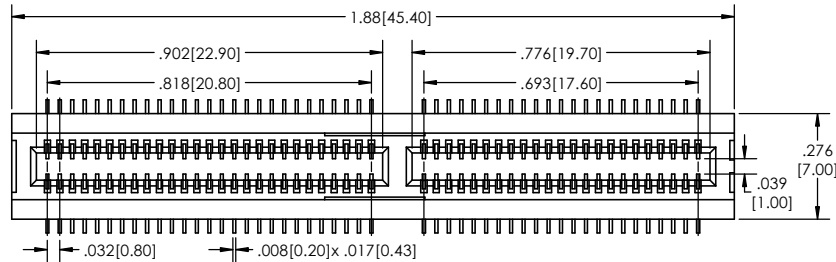
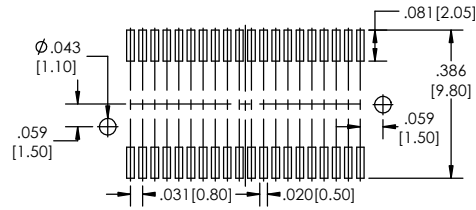


Recommended P.C. Board Layout
SCALE 4:1



PCI-8 **50** - **D** - **03** - **XX** - **XX** - **X** - **LF**
(A) (B) (C) (D) (E) (F) (G)

- A. Pins Per Row
- 50
B. Row Specification
- D= Dual Row
C. 03= Surface Mount
D. Plating Options
- See Plating Options below
E. Packing Information
- TR= Tape and Reel
F. Pad
- P= Plastic
- If left blank, packaging at MLE's discretion
Pad may be metal, mylar or plastic
G. Lead Free

Plating options	
G	-10µ Gold on Contact Area/ Flash on Tail
T	-Matte Tin all Over
F	-Gold Flash over Entire Pin

Specifications:
Current Rating: 0.5 Ampere
Insulation Resistance: 1000M Ohms Min.
Dielectric Withstanding: 300V AC
Contact Resistance: 20M Ohms Max.
Operation Temperature: -40°C to +105°C

Materials:
Contact Material: Phosphor Bronze
Insulator Material: High Temperature Thermoplastic, UL 94V-0 LCP

Tails may be clipped to achieve specific dimensions
Products cut to specific sizes are uncancellable/ nonreturnable.
Parts are subject to change without notice.



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UNLESS OTHERWISE SPECIFIED:		NAME		DATE
DIMENSIONS ARE IN INCHES		DRAWN		EOW 01/12/15
TOLERANCES:		CHECKED		GWE 01/12/15
ONE DECIMALS ±.100		ENG APPR.		
TWO DECIMAL ±.010		MFG APPR.		
THREE DECIMAL ±.005		INTERPRET GEOMETRIC TOLERANCING PER:		
FOUR DECIMAL ±.002		MATERIAL		
NEXT ASSY	USED ON	FINISH		
APPLICATION		DO NOT SCALE DRAWING		
ADDRESS:		2533 Centennial Blvd. Jeffersonville, IN 47130 Phone: 812-670-4174 Fax: 812-670-4175 E-Mail: mle@mllectronics.com		
TITLE: PCI-850 SERIES		SIZE		REV
C		DWG. NO.		A
SCALE 4:1		SHEET 1 OF 1		

