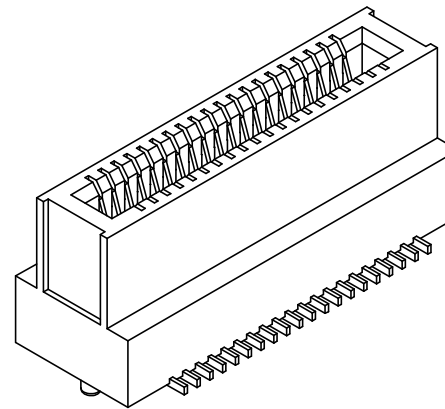
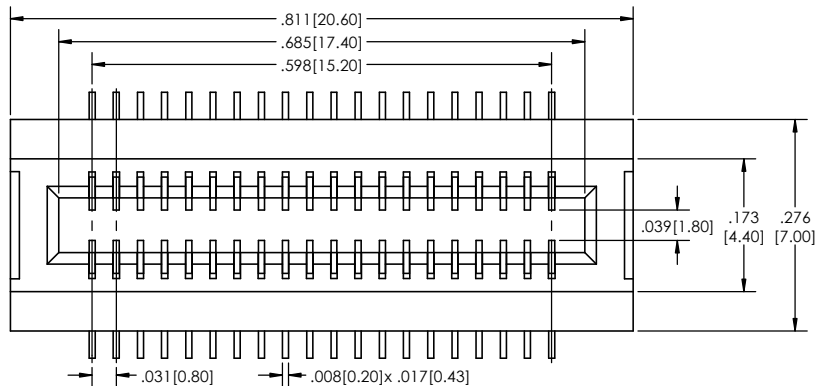
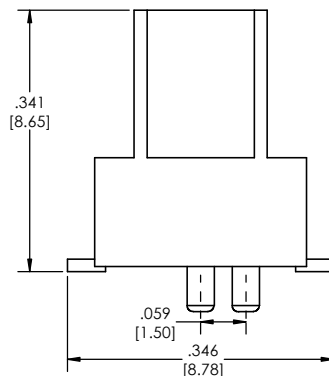
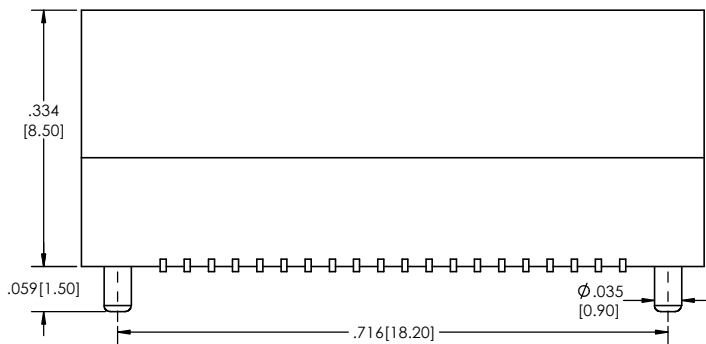
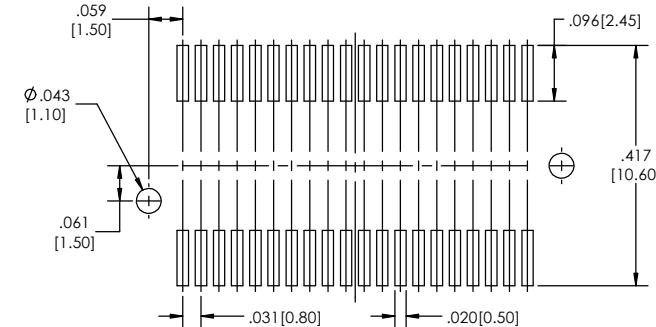




Recommended P.C. Board Layout
SCALE 6:1



PCI-8 **20** - **D** - **02** - **XX** - **XX** - **X** - **LF**
(A) (B) (C) (D) (E) (F) (G)

- A. Pins Per Row
- 20
- B. Row Specification
- D= Dual Row
- C. 02= Surface Mount
- D. Plating Options
- See Plating Options below
- E. Packing Information
- TR= Tape and Reel
- TB= Tube
- F. Pad
- P= Plastic
- O= No Pad
- If left blank, packaging at MLE's discretion
Pad may be metal, mylar or plastic
- G. Lead Free

Plating options	
G	-10µ Gold on Contact Area/ Flash on Tail
T	-Matte Tin all Over
F	-Gold Flash over Entire Pin

Specifications:
Current Rating: 0.5 Ampere
Insulation Resistance: 1000M Ohms Min.
Dielectric Withstanding: 300V AC
Contact Resistance: 20M Ohms Max.
Operation Temperature: -40°C to +105°C

Materials:
Contact Material: Phosphor Bronze
Insulator Material: High Temperature Thermoplastic, UL 94V-0 LCP

Tails may be clipped to achieve specific dimensions
Products cut to specific sizes are uncancellable/ nonreturnable.
Parts are subject to change without notice.



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UNLESS OTHERWISE SPECIFIED:		NAME	DATE
DIMENSIONS ARE IN INCHES		DRAWN	EOW 01/12/15
TOLERANCES:		CHECKED	GWE 01/12/15
ONE DECIMALS ±.100		ENG APPR.	
TWO DECIMAL ±.010		MFG APPR.	
THREE DECIMAL ±.005		ADDRESS:	
FOUR DECIMAL ±.002		2533 Centennial Blvd.	
INTERPRET GEOMETRIC TOLERANCING PER:		Jeffersonville, IN 47130	
MATERIAL:		Phone: 812-670-4174	
FINISH:		Fax: 812-670-4175	
NEXT ASSY USED ON:		E-Mail: mle@mllectronics.com	
APPLICATION		DO NOT SCALE DRAWING	
TITLE: PCI-820 SERIES		SIZE	REV
DWG. NO.		C	A
SCALE 16:1		SHEET 1 OF 1	

