EENG 3520-001 Electronics II

Instructor: Shuping Wang
Office: NTDP F130
Phone: 940-369-8895
Email: shuping@unt.edu

Fall 2020
Time: (MoWe) 11:30am - 12:50pm
Meeting Place: Remote
Office Hours: (TuTh) 4:00pm - 5:30pm

GA: Aldamin, Mahmood
Email: MahmoodAldamin@my.unt.edu
Office hour: By appointment

Course Description

This course is a continuation of EENG 3510 (Electronics I). Topics include single- and multi-stage amplifier, differential amplifier, feedback, and frequency response. The goals of this course are to expand the students’ knowledge of basic electronics, to provide the students the design and analysis of advanced analog electronics circuits, and to expose the students to a variety of tradeoffs for practical electronics design.

Course Requirements

Prerequisites
EENG 3510 Electronics I.

Required Text

Attendance
The class will be conducted via Zoom during the scheduled days and times (i.e. Mondays and Wednesdays from 11:30pm to 12:50pm). The lecture will also be recorded and posted on Canvas, so students can repeat as many times as needed.

Homework
- Homework needs to be uploaded to Canvas at the beginning of the class period on the due date.
- Homework turned in after class will be penalized 50%. No homework accepted after 24 hours.
- Students have one week to contest any grade once grade posted.

Exams
There will be three exams (this includes the final exam), each worth 100 points. Exams will be based on text readings, handouts, class exercises, and class lectures and discussions. Students are responsible for all text material, regardless of whether we review the text material in class or not.
Missed Exams
There are no make-up Tests. If you cannot take the test for any reason, the weight of the test will be put onto the final, so that the final is worth 55% of your grade. Make-up exam accommodations for the Final Exam will only be made if you have a documented university excused absence (refer to UNT Policy 06.039).

Grading Elements and Weights
- Homework: 20%
- Test 1: 25%
- Test 2: 25%
- Final Exam: 30%

Grade Distribution

<table>
<thead>
<tr>
<th>Points</th>
<th>Letter Grade</th>
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<tbody>
<tr>
<td>90.0% - 100%</td>
<td>A</td>
</tr>
<tr>
<td>80.0% - 89.9%</td>
<td>B</td>
</tr>
<tr>
<td>70.0% - 79.9%</td>
<td>C</td>
</tr>
<tr>
<td>60.0% - 69.9%</td>
<td>D</td>
</tr>
<tr>
<td>59.9% &amp; Below</td>
<td>F</td>
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</tbody>
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Student Evaluation of Instruction
SPOT is a requirement for all organized classes at UNT. The survey will be made available at the end of the semester.

Disabilities Accommodation

The University of North Texas complies with Section 504 of the 1973 Rehabilitation Act and with the Americans with Disabilities Act of 1990. The University of North Texas provides academic adjustments and auxiliary aids to individuals with disabilities, as defined under the law. Among other things, this legislation requires that all students with disabilities be guaranteed a learning environment that provides for reasonable accommodation of their disabilities. If you believe you have a disability requiring accommodation, please see the instructor and/or contact the Office of Disability Accommodation at 940-565-4323 during the first week of class.

Additional Policies and Procedures

Synchronous (live) sessions in this course will be recorded for students enrolled in this class section to refer to throughout the semester. Class recordings are the intellectual property of the university or instructor and are reserved for use only by students in this class and only for educational purposes. Students may not post or otherwise share the recordings outside the class, or outside the Canvas Learning Management System, in any form. Failing to follow this restriction is a violation of the UNT Code of Student Conduct and could lead to disciplinary action.
Tentative Course Outline/Schedule

- Building Blocks of Integrated-Circuit Amplifiers (8/24, 8/26, 8/31, 9/2, 9/9)
- Differential and Multistage Amplifiers (9/14, 9/16, 9/21, 9/23)
- **Test 1 (Wednesday, 9/30)**
- Frequency Response (9/28, 10/5, 10/7, 10/12)
- Feedback (10/14, 10/19, 10/21, 10/26)
- **Test 2 (Monday, 11/2)**
- Output Stages and Power Amplifiers (10/28, 11/4, 11/9)
- Operational-Amplifier Circuits (11/11, 11/16, 11/18, 11/23)
- Review (11/30)
- **Final Exam: (Monday, 12/07, 10:30am-12:30pm)**