

University of North Texas, College of Engineering

Department of Electrical Engineering

EENG 2710: Digital Logic Design

Fall 2025

Section 002: Tuesdays and Thursdays 10:00 AM - 11:20 AM
Classroom: D201

Instructor

- Dr. R. Thomas Derryberry, Office: NTDP B240 Email: tom.derryberry@unt.edu
Office Hours: Monday, and Wednesday 1:00 – 4:00 PM
Tuesday, and Thursday 2:30 – 4:00 PM

(Additional appointments can be requested by email using your **my.unt.edu** account.)

NOTE: I will correspond via email **ONLY** if you use your **my.unt.edu** student account.
Anything else will be ignored and deleted.

While I want to make myself as available as possible to each of you, I do have to place some limitations on when I can be contacted. I would prefer that most general questions go through the Q & A forum in the Discussion Board area. If you have a general question about the course or assignments, please post it there. Either I will answer it, or, one of your classmates will. This way we can all benefit from questions asked, and they can be answered in a venue that the whole class can see. You may also want to find someone in class to be a "buddy" with. This will give you at least one other person who you can email with questions.

If you have a private question, please contact me via email and I will respond within 24 hours on weekdays (usually sooner). Please do not expect a response over the weekend.

- GA: TBD, Office Hours: TBD, Email: [TBD](#)

Required Textbook

- Fundamentals of Logic Design, Author: Charles H. Roth, Jr., Larry L. Kinney, and Eugene B. John, CENGAGE, 2021. ISBN: 9781337620352

Course Description

- Introduction to digital computers and information processing systems, Boolean algebra, principles and methodology of logic design, machine language programming, register transfer logic, microprocessor hardware, software and interfacing, fundamentals of circuits and systems, computer organization and control, memory systems, and arithmetic unit design.

Prerequisites

- Engineering or Engineering Technology majors.

Co-requisite

- EENG 2711 – Digital Logic Design lab for Electrical Engineering Majors

Course Webpage

- ALL course related material will be posted on the course webpage which is available through Canvas (<https://unt.instructure.com>).

Course Objectives

1. **Know** *what* digital systems are, *how* they differ from analog systems and *why* it is advantageous to use the digital systems in *many applications*.
2. **Comprehend** different number systems including the binary system and Boolean algebraic principles.
3. **Apply** Boolean algebra to switching logic design and simplification.
4. **Analyze** a given digital system and decompose it into logical blocks involving both *combinational* and *sequential circuit* elements.
5. **Synthesize** a given system starting with problem requirements, identifying and designing the building blocks, and then integrating these blocks into a complete system.
6. **Validate** the system functionality and evaluate the relative merits of different designs.

Course Learning Outcomes (CLOs)

Course Learning Outcomes (CLOs), that is, the areas for student learning in this course are:

- [CLO-1] Digital and Analog Systems: Basic Concepts and Historical Perspective
- [CLO-2] Number Systems and Digital Logic Gates
- [CLO-3] Boolean Algebra, Switching Functions and Canonical Forms
- [CLO-4] Combinational Circuit Minimization, Analysis, and Synthesis
- [CLO-5] Sequential circuit elements and sequential logic circuits
- [CLO-6] Modular Sequential Logic – Counters and shift registers
- [CLO-7] Analysis and Design of synchronous sequential circuits

Course Requirements and General Policies

- Class attendance is mandatory however roll call will not be conducted. Research has shown that students who attend class are more likely to be successful. You should attend every class unless you have a university excused absence such as active military service, a religious holy day, or an official university function as stated in the [Student Attendance and Authorized Absences Policy](#). If you cannot attend a class due to an emergency or an item falling under the [Student Attendance and Authorized Absences Policy](#), let me know prior to class in order to be considered for possible make up work. Your safety and well-being are important to me.
- Lectures and class discussions will contain vital information needed to do well on the exams. Canvas will be your primary electronic interface for this course e.g., all announcements, notes, assignments, etc.

- Students are required to come prepared to every class with the material discussed in the previous class.
- Everyone must turn in individual homework. Homework must be done individually (you will learn the most from this). Any evidence of group participation or simply copying other's homework will be treated and interpreted as academic dishonesty.
- Please remember to turn off phones prior to class. Please note that portable phones, pagers, and late arrivals are disruptive to the instructor and to your peers. The use of cell phones, beepers, or communication devices is disruptive and is therefore absolutely prohibited during class or while taking exams or quizzes. Turn off your cell phone while in class. If I catch you using these devices, your final grade will be reduced by 10% for every transgression and you will be asked to leave the class. Except in emergencies, students using such devices must leave the classroom for the remainder of the class period. I know that some of you may wish to take notes directly on your computer and I have no problem with that. If however, you choose to access your email, search the web, play games, or instant messenger your friends during class, you will have 5% deducted from your final grade for each and every transgression. This penalty will be at the sole discretion of the instructor. If for some reason I am late arriving to class, it will be because of circumstances beyond my control. You are expected to remain for 15 minutes past the scheduled class start time while I attempt to communicate my situation and relay instructions.
- Please visit <http://www.unt.edu/csrr> for your rights and responsibilities.

Disability Accommodation

- The University of North Texas makes reasonable academic accommodation for students with disabilities. Students seeking reasonable accommodation must first register with the Office of Disability Access (ODA) to verify their eligibility. If a disability is verified, the ODA will provide you with a reasonable accommodation letter to be delivered to faculty to begin a private discussion regarding your specific needs in a course. You may request reasonable accommodations at any time; however, ODA notices of reasonable accommodation should be provided as early as possible in the semester to avoid any delay in implementation. Note that students must obtain a new letter of reasonable accommodation for every semester and must meet with each faculty member prior to implementation in each class. Students are strongly encouraged to deliver letters of reasonable accommodation during faculty office hours or by appointment. Faculty members have the authority to ask students to discuss such letters during their designated office hours to protect the privacy of the student. For additional information, refer to the Office of Disability Access website (<http://www.unt.edu/oda>). You may also contact ODA by phone at (940) 565-4323.

Assignments, Quizzes, and Exams

- **Students are expected to practice good time management skills. Hence there will be a 30% reduction on assignments received within 24 hours after the due date/time and no emailed assignments will be accepted except in extenuating circumstances. Homework is due before the class in hardcopy form. There are no extra credit opportunities.**
- Exams will be based on textbook readings, handouts, class exercises, class lectures, discussions, and homework assignments. Students are responsible for all text material, regardless of whether we review the text material in class or not.

Successful Completion of the Course

Every student in this class should have the right to learn and engage within an environment of respect and courtesy from others. We will discuss our classroom's habits of engagement, and I also encourage you to review UNT's student code of conduct so that we can all start with the same baseline civility understanding (Code of Student Conduct) (<https://deanofstudents.unt.edu/conduct>).

The best way to ensure you pass this course with the grade you desire is to work consistently throughout the semester. In any engineering discipline, course topics always build one upon the other, which makes it very difficult to catch up later if you fall behind. I will not entertain any pleas for extra credit or offers to do additional work.

Secret to Success in this Course (Shsssh, don't tell anyone!)

- To do well in this course, you should be thoroughly familiar with this material. However, several years of research and practice including many trial and error attempts have resulted in the development of this long sought after but elusive recipe to success. The best kept secret to achieving success in this course is cordially shared with you below:
 1. Read and comprehend the required text (Yes, you have to read!).
 - a. Know and explain how to correctly solve the examples provided in the text independently.
 2. Know and explain how to correctly solve the homework assignments independently.
 3. Know and explain how to correctly solve the programming assignments independently.
 4. Arrive on time, engage in, and practice active listening for all of the class lectures.
- By the way, there are rumors this recipe works very well when applied to other courses.

Please do not wait until the last minute!! If you are having trouble with this class, please take full advantage of the vast resources available to you:

1. Stop by my office during my office hours. I am also available by email.
2. Reach out to the grader.
3. Take advantage of the IEEE's recitation sessions throughout the semester.
4. Take advantage of UNT Tutoring Services.
5. YouTube
6. Etc.

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Grading Policies

- There will be no extra credit.
- No make-up quizzes or exams will be offered unless **prearranged** with the instructor for a university approved absence. (Defined by [UNT Policy 06.039](#).)
- You have 1 week to contest any grade once returned.
 - Assuming there is a GA/TA for the course, you must **first** contact them regarding a grade dispute prior to seeing me.

COURSE PERFORMANCE MEASURES	
Homework (≥ 8)	15%
Quizzes (Unannounced) (≥ 7)	25%
Midterm Exam	30%
Final Exam	30%
Total	100%

- Keep all of your graded assignments, quizzes, and tests for study and review. You should track your own progress using Canvas or other appropriate means and be aware of current grades throughout the term however the **official gradebook** will be

my **Excel spreadsheet**. I will make all the effort to return the graded assignments, but it is your responsibility to collect back the graded assignments from the grader or the instructor if it is not given back to you.

- A composite course score will be computed for the grading assessment. Failure to achieve passing performance for the composite score will result in a final course grade of **F**. Once passing performance has been determined, the final grading for the composite course score will be done as follows: **A** > 90%, 90% > **B** > 80%, 80% > **C** > 70%, 70% > **D** > 60% and **F** < 60%. Grades will be curved if deemed necessary. **If a curve is deemed necessary, the minimum expectation to pass with a D is 50%.**
- Grades cannot be changed after they have been electronically entered into the university's system except for instructor error. Any extenuating circumstances that may adversely affect your grade must be brought to my attention before the final course grades are recorded. To be considered, such circumstances must be unusual, unavoidable, and verifiable.

Academic Dishonesty

- All the provisions of the University code of academic integrity apply to this course. Any student found to have participated in academic dishonesty will receive an F in the class, a record of the offense will be kept in the Office of the Dean of Students and may be subject to further disciplinary action. Acts of academic dishonesty include but are not limited to: academic fraud (e.g. changing solutions to appeal a grade), copying or allowing one's work to be copied, fabrication/falsification, plagiarism, sabotage of others' work, substitution (e.g. taking an exam for someone else). In addition, it is my understanding and expectation that your signature on any test or assignment means that you neither gave nor received unauthorized aid. For homework and lab assignments, while discussion is allowed, direct copying is not, and students must turn in individual submissions. Realize that mastery of the material in the homework and lab assignments will be essential for good performance on the exams! All students are required to know, observe, and help enforce the UNT Code of [Student Academic Integrity](#).

Course Outline and Tentative Schedule

All course materials, including syllabus, lecture notes, homework assignments, and grades are available in Canvas at <https://unt.instructure.com>

- **Section 002:**

- **First Day of Class, Aug. 19, Tuesday, 10:00 - 11:20 AM.**
- **Midterm Exam (Tentative) Thursday, Oct 9, 2025, 10:00 - 11:20 AM**
- **Last Day of Class, Dec. 4, Thursday, 10:00 - 11:20 AM.**
- **Final Exam Thursday, Dec 11, 2025, 8:00 – 10:00 AM**

WEEK	TOPIC	READING ASSIGNMENT
1	Course Introduction – Number Systems and Conversion – Binary Arithmetic	1.1 – 1.3
2	Negative Numbers – Binary Codes – Boolean Algebra	1.4 – 1.5, 2.1 – 2.5
3	Laws of Boolean Algebra – SOP & POS Forms & Simplification	2.6 – 2.8, 3.1 – 3.5
4	Minterms/Maxterms & Applications of Boolean Algebra – Truth Tables and Incomplete Functions – Introduction to Karnaugh Maps	4.1 – 4.7, 5.1 – 5.3
5	Karnaugh Maps with 4 & 5 Variables – Prime Implicants – Quine-McCluskey method	5.1 – 5.3, 6.1 – 6.6
6	Midterm Exam	
7	Multi-Level Gate Circuits – NAND and NOR Gates	7.1 – 7.7
8	Combinational Circuit Design, Gate Delays, Timing Diagrams, and Hazards	8.1 – 8.4
9	Multiplexers, Buffers, Decoders, Encoders, and ROMs	9.1 – 9.5
10	Latches and Flip-Flops – Registers and Counters	11.1 – 11.10, 12.1 – 12.3
11	Registers and Counters – Analysis of Clocked Sequential Circuits	12.4 – 12.6, 13.1 – 13.4
12	Derivation of State Graphs and Tables – Reduction of State Table State Assignment (1)	14.1 – 14.6, 15.1 – 15.5
13	Reduction of State Table State Assignment – Sequential Circuit Design	15.6 – 15.9, 16.1 – 16.3
14	Sequential Circuit Design	
15	Other topics (Time-permitting), e.g. Digital Logic Testing, Economics	

Useful Links

- UNT Catalogs: <http://www.unt.edu/catalog/>
- Office of the Registrar: <http://essc.unt.edu/registrar/> (schedule of classes and exams, etc.)
- Eagle Student Services Center: <http://essc.unt.edu/>
- Canvas: <https://unt.instructure.com>

Last updated: 08/05/2025