Instructor Information

Instructor:  King Man Siu
- Office: NTRP B233 (Monday to Friday)
- Tel: (940) 369-7890
- Email: Kingman.siu@unt.edu
- All Email communication with the instructors requires using your UNT student Email account.

TA: Aravind Pothula
- Email: AravindPothula@my.unt.edu

Course Description

The aims of this course are to introduce contemporary electronic devices, terminal characteristics of active semiconductor devices, and models of the bipolar transistors (BJTs) and metal-oxide-semiconductor field effect transistors (MOSFETs) in cutoff and saturation regions. Incremental and DC models of junction diodes, BJTs, and MOSFETs are studied to design single and multistage amplifiers.

Course Information

Prerequisites
EENG 2610 Circuit Analysis, solid algebra skills, and basic calculus.

Required Text

Attendance
Attendance is expected. Research has shown that students who attend class are more likely to be successful. To encourage your attendance, punctuality, and learning, I will provide pop-up quizzes of the previous lecture’s material in class. You will have the ability to earn up to 5 bonus points toward your overall grade over the semester by attending the lectures. The pop-up quizzes will be last for about 5 minutes at a random time. Plan to arrive on time because you must be present when the class begins to take the quiz.

COVID-19 Impact on Attendance
Please inform me if you are unable to attend class meetings because you are ill, in mindfulness of the health and safety of everyone in our community. If you are experiencing any symptoms of COVID (https://www.cdc.gov/coronavirus/2019-ncov/symptoms-testing/symptoms.html) please seek medical attention from the Student Health and Wellness Center (940-565-2333 or
askSHWC@unt.edu) or your health care provider PRIOR to coming to campus. UNT also requires you to contact the UNT COVID Team at COVID@unt.edu for guidance on actions to take due to symptoms, pending or positive test results, or potential exposure.

**Homework**

Homework will be assigned to assess understanding and reinforce the materials covered in the lecture.
- Homework needs to be uploaded to Canvas at the due date/time.
- Homework turned in late will be penalized 50%. No homework is accepted after 24 hours.
- Students have one week to contest any grade once the grade is posted.

**Exams**

There will be three exams (this includes the final exam), each worth 100 points. Exams will be based on text readings, handouts, class exercises, and class lectures and discussions. Students are responsible for all text material, regardless of whether we review the text material in class or not.

**Missed Exams**

There are no make-up Tests. Make-up exam accommodations for the Final Exam will only be made if you have a documented university excused absence (refer to UNT Policy 06.039).

**Schedules of exams**

- Test 1: October 13, 2:30-3:50 pm.
- Test 2: November 10, 2:30-3:50 pm.
- Final: December 13, 1:30-3:30 pm.
- [http://registrar.unt.edu/exams/final-exam-schedule/fall](http://registrar.unt.edu/exams/final-exam-schedule/fall)

**Grading Elements and Weights**

<table>
<thead>
<tr>
<th>Component</th>
<th>Weight</th>
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<tbody>
<tr>
<td>Homework</td>
<td>20%</td>
</tr>
<tr>
<td>Test 1</td>
<td>25%</td>
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<tr>
<td>Test 2</td>
<td>25%</td>
</tr>
<tr>
<td>Final Examination</td>
<td>30%</td>
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<tr>
<td>Pop-up quiz &amp; Bonus</td>
<td>5%</td>
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**Grade Distribution**

<table>
<thead>
<tr>
<th>Points</th>
<th>Letter Grade</th>
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<tbody>
<tr>
<td>90.0% - 100%</td>
<td>A</td>
</tr>
<tr>
<td>80.0% - 89.9%</td>
<td>B</td>
</tr>
<tr>
<td>70.0% - 79.9%</td>
<td>C</td>
</tr>
<tr>
<td>60.0% - 69.9%</td>
<td>D</td>
</tr>
<tr>
<td>59.9% &amp; Below</td>
<td>F</td>
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**Tentative Course Outline**

- Review of Circuit Analysis
- Signals and Amplifiers
- Semiconductors
Diodes
- MOS Field-Effect Transistors (MOSFETs)
- Bipolar Junction Transistors (BJTs)
- Transistor Amplifiers

**Student Evaluation of Instruction**
SPOT is a requirement for all organized classes at UNT. The survey will be made available at the end of the semester.

**UNT Policies**

**ODA Policy**
UNT makes reasonable academic accommodation for students with disabilities. Students seeking accommodation must first register with the Office of Disability Accommodation (ODA) to verify their eligibility. If a disability is verified, the ODA will provide a student with an accommodation letter to be delivered to faculty to begin a private discussion regarding one's specific course needs. Students may request accommodations at any time, however, ODA notices of accommodation should be provided as early as possible in the semester to avoid any delay in implementation. Note that students must obtain a new letter of accommodation for every semester and must meet with each faculty member prior to implementation in each class. For additional information see the [ODA website](https://disability.unt.edu/).

**Academic Integrity Policy**
According to UNT Policy 06.003, Student Academic Integrity, academic dishonesty occurs when students engage in behaviors including, but not limited to cheating, fabrication, facilitating academic dishonesty, forgery, plagiarism, and sabotage. A finding of academic dishonesty may result in a range of academic penalties or sanctions ranging from admonition to expulsion from the University.

**Prohibition of Discrimination, Harassment, and Retaliation (Policy 16.004)**
The University of North Texas (UNT) prohibits discrimination and harassment because of race, color, national origin, religion, sex, sexual orientation, gender identity, gender expression, age, disability, genetic information, veteran status, or any other characteristic protected under applicable federal or state law in its application and admission processes; educational programs and activities; employment policies, procedures, and processes; and university facilities. The University takes active measures to prevent such conduct and investigates and takes remedial action when appropriate.