Course Number, Title, Credit Hours:

EENG4010.011 Mixed Signal Circuit Design, 3 Credit Hours

Course Description:

This course covers advanced topics in Mixed Signal circuit design. State-of-the-art, design and simulation software will be used throughout the course, including Mixed Signal Behavioral Modeling. Each student will complete a medium complexity design project.

Prerequisite(s): Consent of the instructor.

Course Topics:

- Review of MOSFET devices as switches and amplifiers.
- SPICE simulation and MOSFET analog and digital models.
- Special-purpose digital circuits: Schmitt triggers, Multivibrators, Voltage Generators and Digital Phase Locked Loops.
- Transistor-level analog circuits: Current sources and sinks, References, Differential and Operational Amplifiers.
- Transistor-level mixed signal circuits: Level shifters and other nonlinear elements, Switched capacitor circuits, Data converter fundamentals, Analog-to-Digital and Digital-to-Analog converters.
Textbook(s) and/or required material:


Additional material, as required, will be provided on Canvas.

Course Objectives: (TAC of ABET Criteria and Program Educational Objectives supported).

1. Be knowledgeable about the basic elements of Analog and Mixed Signal Design. (1,2,3,4,5,6,7)
2. Understand how to use standard CAD software to design and analyze Analog and Mixed Signal circuits. (1,2,3,4,5,6,7)
3. Understand the transistor level structure of current mirrors, sinks and references. (1,2)
4. Understand the design process for transistor level implementation of Feedback, Differential and Operational amplifiers. (3,4)
5. Understand the building elements of level shifters, multivibrators and Schmidt Triggers. (5)
6. Understand the structure and design of Phase Locked Loops. (6,7)
7. Understand the structure and quality metrics of data converters. (8)
8. Be knowledgeable about the standard analog-to-digital and digital-to-analog converter architectures. (8)
9. Understand how to implement and execute a testing plan for a mixed-signal circuit. (9)

Student Learning Outcomes: (Course Objectives Supported)

At the end of this course, the student should be able to:

1. Design, test and evaluate current sources and sinks. (1,2,3)
2. Design, test and evaluate voltage and current references (1,2,3).
3. Identify critical steps in the transistor level design of general amplifiers (1,2,4).
4. Analyze the transistor level operation of feedback, differential and operational amplifiers. (1,2,4)
5. Design and analyze level shifters, multivibrators and Schmidt triggers. (1,2,5)
6. Identify the building blocks of a Phase Lock Loop. (1,2,6)
7. Design a Phase Locked Loop from given specifications. (1,2,7)
8. Test and evaluate different Analog-to-Digital and Digital-to-Analog architectures. (7,8)

9. Test and evaluate complex analog and mixed-signal circuits. (9)

**Grading:**

- Project: 20%
- Exam 1: 25%
- Exam 2: 25%
- Final: 30%

Grading scale (based on total course points):

- 90% - 100%  A
- 80% - 89.99%  B
- 70% - 79.99%  C
- 60% - 69.99%  D
- 00% - 59.99%  F

**NOTES:**
The exam schedule is as follows:
- Test 1 is on Wednesday October 2nd (Week 6)
- Test 2 is on Wednesday November 13th (Week 12)

**The final exam will take place on Wednesday December 11th 1:30PM – 3:30PM in NTDP B142**

During each test (including the final exam) you can consult the textbook (open book tests) ONLY. No notes or other material. You will have full access to the course (Canvas) web site during all tests (including the final exam) but limited access to the internet. You will also have access to any required software.

During tests the use of electronic devices such as cell phones, smart phones, smart watches, pagers, photographic devices and/or other electronic or communication devices, with the exception of calculators, is strictly prohibited. Such devices must be turned off during the tests. You will also be asked to leave you backpacks by the classroom door.

**Missed Exams:**

You will be allowed to make up a missed exam only if you have a documented university excused absence and received prior approval. For more details visit the UNT Dean of Students’ web page: [https://deanofstudents.unt.edu/resources](https://deanofstudents.unt.edu/resources)

**Assignments:**

Homework will be assigned but not graded. Solutions will be provided on Canvas and will be discussed in class.
**Academic Dishonesty:**

Cheating will not be tolerated. Anyone found guilty of cheating on a test or assignment will be awarded an F grade for the course. Discussions of problems and assignment with your classmates is welcome and encouraged, however, sharing of solutions is not. If you need help, you should ask the instructor. Cheating includes, but is not limited to, all forms of plagiarism and misrepresentation. For your rights and responsibilities please refer to [http://www.unt.edu/csrr](http://www.unt.edu/csrr)

**Course Evaluation:**

The Student Perceptions of Teaching (SPOT) is a requirement for all organized classes at UNT. This short survey will be made available to you at the end of the semester, providing you a chance to comment on how this class is taught. I am very interested in the feedback I get from students, as I work to continually improve my teaching. I consider SPOT to be an important part of your participation in this class.

**Disabilities Accommodation:**

The University of North Texas complies with Section 504 of the 1973 Rehabilitation Act and with the Americans with Disabilities Act of 1990. The University of North Texas provides academic adjustments and auxiliary aids to individuals with disabilities, as defined under the law. Among other things, this legislation requires that all students with disabilities be guaranteed a learning environment that provides for reasonable accommodation of their disabilities. If you believe you have a disability requiring accommodation, please see the instructor and/or contact the Office of Disability Access at 940-565-4323 during the first week of class. Information and requests for supporting letters can be obtained from: [https://disability.unt.edu/](https://disability.unt.edu/)

**Additional Policies and Procedures:**

1. State common law and federal copyright laws protect my lectures. They are my own original expression. Whereas you are authorized to take notes in class thereby creating a derivative work from my lecture, the authorization extends only to making one set of notes for your own personal use and no other use. You are not authorized to record my lectures, to provide your notes to anyone else or to make any commercial use of them without expressed prior permission from me.
2. This syllabus is subject to change at any time during the semester with changes to be announced in class.
3. Cell Phones: Please remember to turn off phones prior to class.
4. An I (incomplete) grade is given only for extenuating circumstances and in accordance with University and Departmental Policies.
5. To comply with FERPA policies, I will communicate via email (email me at elias.kougianos@unt.edu) but I will only respond to UNT email accounts.
6. Each student should retain graded lecture notes, pop quizzes, homework, tests, software-generated files, and laboratory reports to document errors in recorded grades.
7. Requests for review of graded work must be submitted during the lecture in which such work is returned to the students. The request should be accompanied by a written justification of the request including any supporting data.
8. Challenges to the course grade must be presented within 60 days of receipt of grade notices mailed by the university. This will ensure that instructor’s records are still available to allow a review of the assigned grade. You should first discuss your complaint with the instructor. If you wish to carry it further, contact the Program Coordinator by calling (940) 891-6872. To further pursue your complaint, contact the Department Chair, but ONLY after first discussing your concern with the previous two individuals.