SIGNAL INTEGRITY ENGINEER RESUME

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Career Goal:

To be able to work in your organization as a Signal Integrity Engineer where I can fully utilize my skills in HSpice, PSpice and Ansoft Designer

Technical Skills:

Strong technical command of EM modeling tools:

- HFSS
- PowerSI
- OptimizePI

Solid understanding of layout tools:

- Cadence Allegro
- Mentor
- Altium

Exceptionally skilled at analysis tools:

- JMP
- DesignExplorer
- Excel

Thorough understanding of lab instruments:

- HP Spectrum Analyzer
- Agilent Vector

Conceptual knowledge of signal integrity concepts:

- Link optimization
- System memory design
- LabView

Comprehensive knowledge of test equipments:

- Tektronix Oscilloscopes
- HP Function Generator

Quick and excellent in power integrity tools:

- ADS
- Ansys HFSS
- Intel PDA

Sound abilities to:

- Conduct peak distortion analysis and use circuit simulation tools
- Understand network analyzers and OSI models

Relevant Experience:

Signal Integrity Engineer
Scitor Corporation
January 2012 – Present
Wilmer, TX

- Formulated measurement requirements for cable interfaces.
- Managed probing stations and executed VNA measurements.
- Conducted training sessions about VNA and TDA measurements.
- Implemented TX test capabilities for HDMI interfaces.
- Participated in signal integrity functions for interfaces such as MIPI.
- Assisted in SI simulations and designed MS Surface pro devices.
- Developed EMC radiation simulations for Kinect devices.
- Maintained simulated data and power integrity performance.

Signal Integrity Engineer
Micron Technology, Inc
November 2010 – January 2012
Milpitas, CA

- Assisted in design reviews for clients regarding board routing.
- Resolved application engineering issues and participated in component design reviews.
- Conducted bench testing and implemented electrical simulation techniques.
- Designed and developed eye diagram masks for bus topology.
- Provide suitable feedback to external vendor regarding board designs.
- Managed board routability and modified schematics.
- Participated in simulation of DC losses for power supplies.
- Provided support for measurements to execute data collection and design analysis.

Educational Background:

Bachelor's Degree in Information Technology
Trident Technical College
August 2006 – May 2010
Charleston, SC

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