

Prepared for Life

INFORMATION TECHNOLOGY THEORY EXAM. GRADE 11

NAME:	GRADE:
DATE: 20 JULY 2022 EXAMINER: MR SC EILERTSEN	MARKS: XX TIME: 2,5 HOURS
MODERATOR: MR C SEEWALD	

INSTRUCTIONS:

- 1. This class test is made up of XX pages. Please ensure that your paper is complete.
- 2. There are no trick questions on this paper e.g. a comma that did not print clearly would not be the intention of any of the questions.
- 3. It is in your interests to write legibly and neatly.
- 4. You may use a non-programable calculator.
- 5. Additional paper is provided at the end of this examination paper. If you use it please label your answer clearly using the same numbering system as the original question.

Question One

Short questions

Match the term in column A with its definition in column B. Use the answer table below for your answers – write the letter next to the question number. There are more definitions than there are terms.

7	Column A	30	Column B
1.1	browser cache	А	That part of the operation system that is always kept in RAM.
1.2	kernel	В	This memory extends the number of addresses that RAM has – pages can be swopped between RAM and the hard drive.
1.3	interpreter	С	The time taken for a component to respond to a request for service – this time should be as short as possible.
1.4	SATA	D	Instead of reading a program line by line from a slow hard drive, several lines are read so subsequent reads are faster.
1.5	buffer	E	Stores the programs and data currently being used.
1.6	multitasking	F	The program that performs the boot sequence which ends with the entire operation system being loaded.
1.7	latency	G	A signal sent to the CPU from hardware or software indicating the need for the CPU's attention.
1.8	CMOS	Н	A parallel bus that connects all components on the mother board.
1.9	disk cache	ł	When you visit a website, a copy is kept here to increase performance should you visit the same website in future.
1.10	interrupt	J	A temporary storage area in RAM to hold data coming in and going out to compensate for momentary delays.
		K	A battery backed, volatile memory that stores hardware and user settings that are loaded on boot up.
		L	Software that reads each a program line, converts it to machine code, executes it and then moves onto the next line.
		M	A computer bus interface used to connect mass storage devices (mechanical or solid state) to a computer motherboard.
		N	A technique used by the operation system to appear to be running several programs simultaneously.

(10)

Answer table

Ques	Ans								
1.1		1.2		1.3		1.4		1.5	
1.6		1.7		1.8		1.9		1.10	

Question two

Hardware

Busses connect all the computer components, but they operate at different speeds and connect different types of hardware to the computer system.

2.1) Fill in the table below. Note that the internal bus is also know as the front side bus (FSB) or system bus.

Α	В	С	D
Types of busses.	Write "internal bus" or "external bus".	Does this bus offer a direct connection between the CPU to the RAM? Write Yes or No.	Briefly describe what each bus does mentioned in column A does. You can also mention the type of components you would expect to find connected to the bus in question.
Control bus			
PCI express			
			,
			×
SATA			
NVMe			
		<u> </u>	*

Types of busses – continued.	Write "internal bus" or "external bus"	Does this bus offer a direct connection between the CPU to the RAM? Write Yes or No.	Briefly describe what each bus does mentioned in column A does. You can also mention the type of components you would expect to find connected to the bus in question.
Data bus			
Address bus			
Universal Serial Bus			
			diagram A and diagram B.

2	(21)
2.2) Refer to Addendum A question 2.2. There is diagram A and diagram B.	
2.2.1) Explain which one illustrates hyperthreading – A or B?	
2.2.2) Explain which one illustrates parallel processing – A or B?	
	(3)

2.3) Explain multiprocessing	
2.4) Examine the three computers in Addendum A – see Question 2.4 . We are going to compare performances of computers 1, 2 and 3 with one another. Combining all your knowledge about coarchitecture, cache memory, shared memory, hyperthreading, multiprocessing, registers, bus specially three questions.	the theoretical omputer
2.4.1) Which one would offer the best performance? (1,2 or 3). Explain.	
	(3
2.4.2) Which one would offer mid-range performance? (1,2, or 3). Explain.	
	(3
2.4.3) Which one would offer the lowest performance? (1,2, or 3). Explain.	
Question three System Software	
3.1) The boot up process. Describe four advantages of UEFI over traditional BIOS.	
	(4)

•	3.2) Study the image in Addendum B question 3.2 and explain whether this illustrates hyperthreading, multitasking, multithreading, or multiprocessing?				
	multitireading, or multiprocessing?				
	(3)				

Question four

Networking overview

Write "node", "connecting device", "connection media", "topology" or "internet protocol" in column B, next to each of the words in the column A

4.1)

Column A	Column B
Personal computer	
NIC	
TCP/IP	
bus	
Switch	
DHCP	
Server	
Printer	
Radio waves	
NFC (near field communication)	
Mobile devices	
star	
UDP	
laptop	
Router	
Bridge	
Shielded twisted pair	
Workstation	
ARP	
ring	
Fibre optic	
hybrid	
Bluetooth	
Network interface card	
repeater	
DNS (domain name service)	

(26 divide by 2)

4.2) Internet protocols. Write ICP or UDP next to each situation below.
4.2.1) When watching a movie on Netflix (a streaming media service) which internet protocol will be used? (1
4.2.2) When downloading an application to install on your local computer(1)
4.2.3) When chatting to someone on your mobile phone using WhatsApp(1
4.2.4) When sending email to a friend.
4.2.5) When submitting your assignment to Ms Teams(1
4.3) Generally, when creating a LAN in a local area (school or office) which popular networking technology are your
likely to use?(1
4.4) Generally, when creating a LAN in a local area (school or office) which popular networking topology are your likely to use?(1
4.5) Generally, when creating a LAN in a local area (school or office) which popular IP addressing system are your likely to use?(1
Question 5 Scenario.
The Free State country town Rhese has a declining population. It has a solid but old-fashioned infrastructure that is falling into disuse. The only large buildings are the town hall, the old movie theatre, and a church while the rest of the town is made up of single-story buildings that are either shops along the main street or houses. The town is divided down the middle by the Vaal River; the commercial side of the town is on one side of the river while the houses tend to be on the other side. The whole region around the town is very flat. The nearest large city is Bloemfontein 40 kms away which has a modern and sophisticated IT infrastructure. It is decided to use the existing town infrastructure and open a satellite campus of the University of Free State (ufs) which is found in Bloemfontein. The university's IT network can be extended to the town.
5.1) What would be the best way to link the university network to the town? Explain your answer.
5.2) Once linked to the city the entire town is going to be networked. What are the advantages of creating such a network both for students as well as for the university?
(5

created that will give access to the universities main central database in the city. Each office will have a desktop computer. These offices need maximum IT security to protect valuable university data as well as student data. Describe how you would design this network to ensure maximum security, speed, convenience, and reliability.
Mention the networking topology, the network technology, the cabling, and the connection devices that you would use.
·
•
·
(10
5.4) The town hall is going to need several high-powered servers to create a client-server environment. Which type (not brand) of servers would you install while explaining what function each server would fulfil for their connected client workstations.
(3)
5.5) The houses on the other side of the river are going to be used for student accommodation.
5.5.1) Explain why UTP cabling cannot be used.
·
(3)

	(3)
5.5.3) Cost is always an issue. Some feel that a bus topology, which is cheaper to install would be a good choice feel that a star topology, although more expensive is a better option. Explain the advantages of a star topology a bus topology.	
4	(5
5.5.4) The whole network will continue to use TCP/IP just like the city campus. As each node logs on to the syste the morning it is automatically allocated an IP address from a pool of available address. What do we call this prothat allocates IP addresses?	em i otoc
5.5.5) The University of Free State has a website i.e "ufs.ac.za" (the domain name). The IP address of this websit "146.182.9.12" What do we call the service that links the IP address with the domain name?	
5.5.6) Is this IP address "146.182.9.12" IPv4 or IPv6. Explain your answer.	(1
	(3
5.5.7) Security is always an issue. If illegal activity is detected on the network the IP manager can query the OSI model, going down to the data link layer to find the MAC address of the culprit. Why would this identify the exacomputer? Why could you not use the IP address to identify the culprit?	ıct
	(2
5.5.8) Students are going to need internet, email, research tools, use the administrations services etc from their student accommodation. What IT networking infrastructure would you install in each student residence to serve their needs?	e

5.5.9) University administrators require all students to own a device with will allow Ms Teams to run. Why do you think the university thinks this is important? What functionality does Ms Team offer that would benefit both the students and the university.

(4)

Section Seven

Solutions development

7.1)

Java "for loop" advanced example

The following is a legal line of Java code showing an example of an advanced version of the "for loop"

So, each of the three sections of the "for loop" are allowed to have more than one expression. We are going to use this knowledge in the pseudocode algorithm below.

Explanation of the algorithm	Diagram A
We have two integer arrays. The first element must be added to the last, the second must be added to the second last, the third must be added to the third last etc.	2 4 3 7 4 6 3 6 2 5 6 1
Finally, the six values should be added together creating a final value for total.	

A loop similar the Java "for loop" above would be useful for this application.

Here is the pseudocode version, shown with line numbers

- 1 integerArr1 = $\{2, 4, 3, 7, 4, 6\}$
- 2 integerArr2 = $\{3, 6, 2, 5, 6, 1\}$
- 3 total = 0
- 4 for (i = 0, j = 5; i < 6; i++, j--)
- 5 begin

```
6     total = total + (i + j)
7     end
8     display ("The total is " + total)
```

The results of the pseudocode above are **not** as expected. Using the grid below create a trace table to demonstrate what the faulty pseudocode algorithm above does do.

• Start your trace table at line 3. The number of lines in the grid below is not exactly correct and is merely provided for your convenience. Make a column for every variable, the calculation result, the condition, and the output. Your trace table must also clearly show the looping action using the line numbers. Suggestion — use a pencil.

Line number				
	_		*	
			·	
l				
		:8		

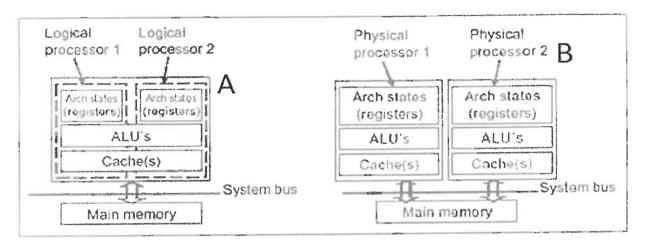
TE: Rewrite the wl	nole algorithm	starting from	line 1. Sugges	tion – use a pe	ncil.	
				•		
			3			

Total: xx

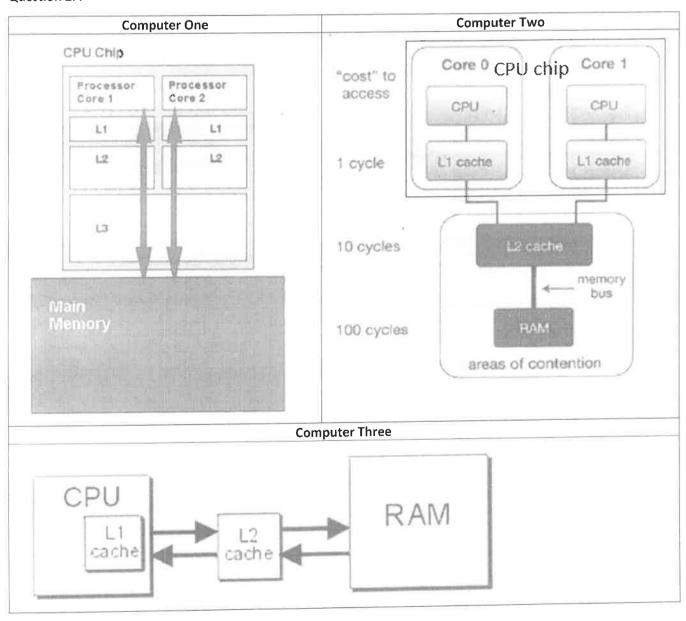
	•	

Addendum A

Question 2.2

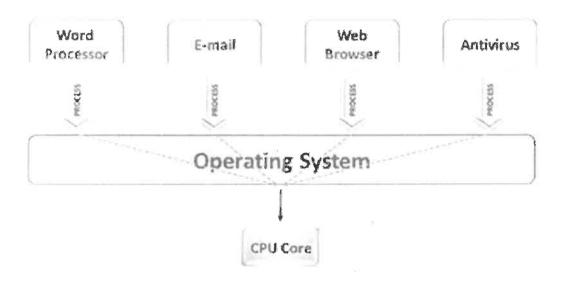


Question 2.4



Addendum B

Question 3.2



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4:

Additional Paper: Label your answers clearly