SMART4ALL Project Marketplace

Contribution of Artefacts to SMART4ALL Project Marketplace

All SMART4ALL funded PAEs will be required to contribute at least one artefact to the project Marketplace (https://marketplace.smart4all-project.eu/).

By the term “artefact” SMART4ALL refers to any tool, educational material, service and/or solution that has been produced by the funded PAE.

To offer some indicative examples we refer to the examples as indicated in section 3.3 of the KTE/FTTE/CTTE Guide for Applicants i.e.

Activity Examples

1) A low resource demanding compression algorithm is developed (output of the PAE is the algorithm)

2) A hardware accelerator for image processing is developed (output of the PAE is the HW design).

With respect to these example indicative examples of each one of the aforementioned types of “artefact” include but are not limited to:

Tools:

1) The compression algorithm itself at any form with adequate accompanying application notes and guidelines

2) If it is a commercial solution, an application notes or reference manual and a link to the company commercializing the algorithm.

3) The hardware accelerator FPGA bitstream with adequate accompanying application notes and guidelines

4) With respect to example 2) if it is a commercial solution, an application notes or reference manual and a link to the company commercializing the hardware accelerator algorithm.

5) A dataset produced by either example that can be useful for a research group of a company.

Education material:

1) An extended tutorial or MOOC course on compression algorithms and how they are designed/implemented/optimized

2) An extended tutorial MOOC course on compression algorithms and how they are designed/implemented/optimized

3) A series of hands-on examples concerning either example or/and laboratory exercises

Services:

1) A webservice demonstrating the compression and decompression functionality allowing the end user to specify the file/data to be compressed/decompressed

2) A webservice demonstrating the image processing functionality allowing the end user to specify the file/data to be processed and highlighting the acceleration compared to e.g. a software based solution

Solution:

1) A “closed” embedded system pre-programmed with the specific compression algorithm able to receive as uncompressed input data and provide the compressed data and vise-versa, utilizing standardized interfaces (e.g. USB interface) and specific application notes.

2) An ASIC hardware component offering the hardware accelerated image processing functionality utilizing standardized interfaces (e.g. USB interface) and specific application notes.

It is noted that these are indicative examples and that the planned to be contributed artefacts will be decided with the help of the assigned coaches and will be indicated in the initial individual mentoring planned formulated taking into consideration the specific nature, target and objectives of the PAE.