

3	2	1
---	---	---

L2-COS	L2-bits			L3-IPP	L3-bits													
					7	6	5	4+3				2	1	0				
0	0	0	0	0	0	0	0	00	0							0	-	-
								Default										
1	0	0	1	1	0	0	1	00	8	01	10	10	12	11	14	0	-	-
								CS1		AF11		AF12		AF13				
2	0	1	0	2	0	1	0	00	16	01	18	10	20	11	22	0	-	-
								CS2		AF21		AF22		AF23				
3	0	1	1	3	0	1	1	00	24	01	26	10	28	11	30	0	-	-
								CS3		AF31		AF32		AF33				
4	1	0	0	4	1	0	0	00	32	01	34	10	36	11	38	0	-	-
								CS4		AF41		AF42		AF43				
5	1	0	1	5	1	0	1	00	40					11	46	0	-	-
								CS5						EF				
6	1	1	0	6	1	1	0	00	48							0	-	-
								CS6										
7	1	1	1	7	1	1	1	00	56							0	-	-
								CS7										

	7	6	5	4	3	2	1	0
L3-TOS - IPP	X	X	X	X	X	X	X	X
L3-DSCP extensions				X	X	X		
DSCP extension bits				X	X			
always 0						X		
ignore for now, always 0							X	X

3 2 1
IPP DX 0

QoS DSCP handout

	3			2		1
AF33	0	1	1		1	0
2^n	32	16	8	4	2	1
	0	16	8	4	2	0
total						30

AF42
AF 4 2 0
100 10 0 XX
100100
= 36

