



```

// Select HPC_1
// 0x7400 : voir Doc XILINX UG885 page
sipif_writesipreg(AddrSipFMC12xI2cMaster+0x7400, 0x02); // 0x12402 + 0x7400 = 0x19802
Sleep(20);
    
```

The VC707 board implements a single I2C port on the FPGA (IIC\_SDA\_MAIN, IIC\_SDA\_SCL), which is routed through a 1-to-8 channel I2C bus switch (U52). The bus switch can operate at speeds up to 400 kHz.

The bus switch I2C address is 0x74 (0b01110100) and must be addressed and configured to select the desired downstream device.

Table 1-24: I2C Bus Addresses

I2C Bus	I2C Switch Position	I2C Address
PCA9548	NA	0b1110100 0x74
USER_CLK_SDL/SCL	0	0b1110000 0x70
FMC1_HPC_IIC_SDA/SCL	1	0bXXXXX00
FMC2_HPC_IIC_SDA/SCL	2	0bXXXXX00
EEPROM_IIC_SDA/SCL	3	0b1010100 0x54
SFP_IIC_SDA/SCL	4	0b1010000 0x50
IIC_SDA/SCL_HDMI	5	0b0111001 0x71
IIC_SDA/SCL_DDR3	6	0b1010000, 0b0011000 0x50,0x18
Si5324_SDA/SCL	7	0b1010000 0x50

Notes:  
1. Use the PCA9548 (U52) at I2C address 0x74 (0b01110100) to setup the path to these buses.

