

Notes:

- r... means any of the registers A, B, C, D, E, F, G, H, I, J, K, L, M, N, O, P, Q, R, S, T, U, V, W, X, Y, Z.
- ss is any of the register pairs BC, DE, FI, HI, LI, SI, DI, TI, PI, NI, CI, BI, AI.
- rr is any of the register pairs BC, DE, FI, HI, LI, SI, DI, TI, PI, NI, CI, BI, AI.
- 1 p/V flag is 0 if the result of BC-1 or DE-1 is zero.
- 2 Z flag is 1 if A = HL, otherwise Z = 0.
- e represents the extension in the relative address.
- e is a signed two's complement number between -128 and +127.
- e-2 in the op-code provides an effective address which is e-2 from (PC) to (PC+1).
- The notation sp indicates bit 0 to (n-1) inclusive.

Flag Notation: ● = flag not affected, ○ = flag not changed, ▲ = flag set, ▼ = flag reset

[illegible][illegible][illegible]