LH79524-10: Recommended Ethernet Circuit
Application Note 272

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Abstract

This document provides the recommended Ethernet circuit for Logic's LH79524-10 Card Engine.

REVISION HISTORY

REV	EDITOR	DESCRIPTION	APPROVAL	DATE
A	Nathan Kro	Release	KTL	2/2/2005

1   Introduction

The following schematics provide an example circuit that can be used to connect Ethernet on the LH79524-10. For information on interfacing to the Ethernet PHY see Broadcom's AC101L datasheet for the complete command set and registers.

2   Ethernet Circuit

The LH79524-10 uses the Broadcom AC101L Ethernet PHY. The schematics in Figure 2.1 shows the circuit implemented on the Logic SDK application board. Note: the magnetics are internal to the connector. The orientation of the magnetics is different than the recommended orientation stated in Broadcom's AC101L datasheet. Refer to the Pulse J0026D21 datasheet for magnetics orientation and the Broadcom AC101L datasheet for their suggested orientation.
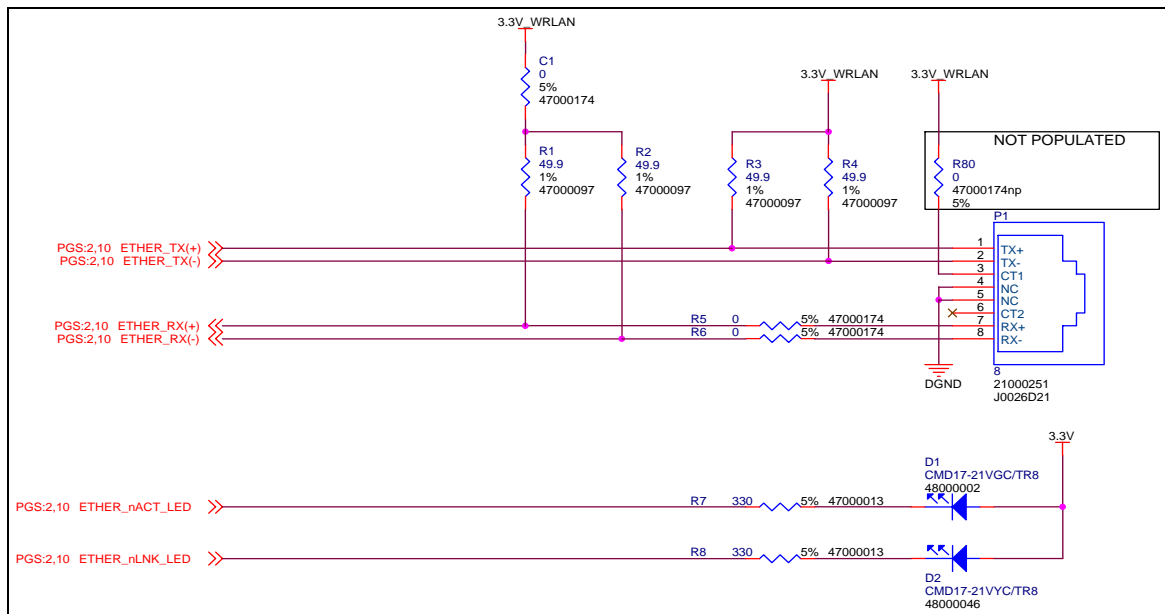

Figure 2.1: Ethernet Circuit

3   Summary

Logic has found the shown Ethernet impedance network to work well on the SDK application board. Customers should read and understand Broadcom's suggested circuit and implement the circuit that best fits their system requirements.