



DDR2 UNBUFFERED SDRAM DIMM

BL12864AA

BL25664AA

Features

- 240-pin dual in-line memory module (DIMM)
- Fast data transfer rates: PC2-8500 or PC2-6400
- 1GB (128 Meg x 64) and 2GB (256 Meg x 64)
- Exclusively designed for high-performance systems
- Critical performance parameters tested for functionality
- Speed verification performed in-system
- VDD = +2.0V ±0.1V, VDDQ = +2.0V ±0.1V
- VDDSPD = +1.7V to +3.6V
- Differential data strobe (DQ, DQS#) option
- Four-bit prefetch architecture
- Auto Refresh (CBR) and Self Refresh Mode
- 7.8125µs maximum average periodic refresh interval
- 64ms, 8,192-cycle refresh
- On-die termination (ODT)
- Serial Presence Detect (SPD) with EEPROM
- Gold edge contacts
- Aluminum heat spreader
- 1.18" (30mm) PCB Height



Timing Parameters

Module Marking	Module Bandwidth	Latency (CL - tRCD - tRP - tRAS)
106A	8.5 GB/s	5 - 5 - 5 - 15 *
80A	6.4 GB/s	4 - 4 - 4 - 12 *

* Performance is verified during testing.

Part Numbers

Part Number	Module Density	Memory Clock/ Data Bit Rate	Banking Configuration	Component Configuration
BL12864AA106A	1GB	1.87ns/1066 MT/s	Single	128 Meg x 8
BL12864AA80A	1GB	2.5ns/800 MT/s	Single	128 Meg x 8
BL25664AA80A	2GB	2.5ns/800 MT/s	Dual	128 Meg x 8



Crucial® Ballistix™ products are warranted to meet the datasheet specifications as found on its web page <http://www.crucial.com/ballistix/> Some system configurations may not be designed to operate or may not operate at the published Crucial Ballistix memory speed and timing settings. Overclocking, running your system faster than the speed for which it was designed or the published speed, or otherwise modifying your system timing may result in damage to computer components and Crucial disclaims any and all liability for such damage.

©2008 Micron Technology, Inc. All rights reserved. Information is subject to change without notice. Micron, Crucial, the Crucial logo, Ballistix, Ballistix Tracer, and the Ballistix Tracer logo are trademarks of Micron Technology, Inc. All other trademarks are the property of their respective owners.