



CICC 2018 Conference Announcement

General Chair: Kimo Tam, Analog Devices
Conference Chair: Alessandro Piovaccari, Silicon Laboratories
Technical Program Chair: Hua Wang, Georgia Institute of Technology

CICC showcases first-published innovative digital, analog and RF circuit and system papers covering a broad spectrum of topics, including: *Power Management, Analog Circuits, Data Converters, Wireless Transceiver and RF Circuits, Wireline Communication Circuits/Systems, Design Foundations, and Emerging Technologies.*

**Join Us for
CICC 2018**

**DoubleTree Hotel
San Diego, CA
April 8 – 11**



PLENARY SPEAKER:

**Mark Pinto: CEO Blue
Danube Systems**

Title: 5 G Wireless System
Trends

Highlights of 2018 CICC:

Technical Program

The technical sessions are the backbone of our CICC conference and this year we are proud to offer a strong program. Papers will be presented in the following areas: Wireline Techniques for Advanced Modulation Schemes; Advanced RF Transceivers; Advanced Analog Techniques; Power Management Circuits and Architectures; Sensor Interface Techniques; Human Body and Emerging Applications; High Performance Oscillators and Low-Power Digital Clock Generations; CMOS Biochips and Bioelectronics; THz, mm-Wave & RF Techniques; Nyquist ADCs; Design Foundations for Advanced Technologies; Cognitive Memories and Novel Accelerators; Wireline Transceivers and Building Blocks; Energy Harvesting Circuits and Systems; Oversampling A/D Converters; Timing Techniques; and Low-Power Radios for IoT and Medical Connectivity.

Educational Sessions

On Sunday, four Educational Sessions will take place. This year our educational sessions will again cover several topics of active researches including, (1) Data Converter; (2) Clocking; (3) High Speed; and (4) Low Power IoT. The presenters are all world-class experts in their respective areas.

Forum and Panel Sessions

Again in 2018, we are pleased to offer an expanded program of four Forums and three Panels. The Panels will address controversial debate topics. The Forums will cover four hot research areas.

Forum Sessions

(1) Self-Driving Car Technology and Associated Computational Power Requirements. (2) Vanishing Boundary between Digital and Analog. (3) Device and Integration at Advanced Technology Nodes. (4) The Next Wave of Machine and Deep Learning Hardware.

Panel Sessions

(1) What is the Sweet Spot of Voltage Regulator Integration? (2) Is the IC Startup Era Over or Just Transitioning? (3) What can/should Circuit Designers do to Ride on the Wave of Machine Learning?

For complete information on the program, registration and hotel information as well as general inquiries: Visit our website at www.ieee-cicc.org.

Contact: IEEE Custom Integrated Circuits Conference, 19803 Laurel Valley Place, Montgomery Village, MD, 20886 Telephone: 301/527-0900 x1, Fax: 301/527-0994, Email: melissaw@widerkehr.com



LUNCHEON SPEAKER

Dave Robertson

GM Fellow/Analog Devices

Title: IC Technology and
Innovation Life Cycles (or What
Does It Take For My Paper to
Get Accepted)

Visit our website at www.ieee-cicc.org