

Training for Engineering Professionals

Besser Associates - San Diego Courses March 3-7

Besser Associates courses are returning to San Diego this March 3-7. Browse the programs below and start making your plans to attend! You can also reserve a spot in any course with no commitment. Just send an email to info@besserassociates.com letting us know which course interests you.

Introduction to Data Converters

This course will introduce the fundamental principles of Analog to Digital Converters (ADC) and Digital to Analog Converters (DAC) including the most common Nyquist-rate and oversampling architectures. The course will cover basic system and circuit architectures, performance metrics, data converter characterization, performance limitations, practical implementations, and design procedures.

Mar 03-Mar 05, 2014 Course 239-4627

Presented by Ayman Fayed

Register by 2/3/2014 and pay \$1495, otherwise pay \$1595

The Radio Modem: RF Transceiver From Antenna to Bits and Back

The goal of this course is to apply intuitive system design methods to dissect the radio modem at RF, analog and digital domains with emphasis on: a) physical understanding of the interaction between components and different radio architectures and b) quantitative performance evaluation using simple hand calculations and simulation. Throughout the course, students will be exposed not only to theoretical analysis but also to concrete examples of radio architectures from existing commercial systems.

Mar 03-Mar 07, 2014 Course 241-4645

Presented by Waleed Khalil

Register by 2/3/2014 and pay \$1995, otherwise pay \$2195

Behavioural Modeling & Digital Pre-Distortion of RF Power Amplifiers

This course explains the nonlinear behaviour of RF power amplifiers, developing general modeling techniques to describe the nonlinearities and memory effects. A system-level approach to the modeling and linearization of the PA is adopted, and techniques for implementation of DPD in practical situations are described.

Mar 04-Mar 06, 2014 Course 212-4628

Presented by John Wood

Register by 2/3/2014 and pay \$1495, otherwise pay \$1595

Hardware DSP: A guide to building DSP Circuits in FPGAs

This three day course covers implementation techniques for building DSP circuits in field programmable gate arrays (FPGAs). As the conversion rates increase for both analog to digital converters (ADCs) and digital to analog converters (DACs) the point of digitization of the analog signal gets closer to the antenna. However the resultant high data rates are often too much for a typical DSP processor and so the DSP algorithms needed to process the high data rate must be built directly in hardware. FPGAs are a cost effective choice for this task.

Mar 05-Mar 07, 2014 Course 244-4651

Presented by Sean Gallagher

Register by 2/7/2014 and pay \$1495, otherwise pay \$1595

Note: Mention this posting was delayed on the IEEE website due to technical issues and ask for a discount despite passing the deadline.