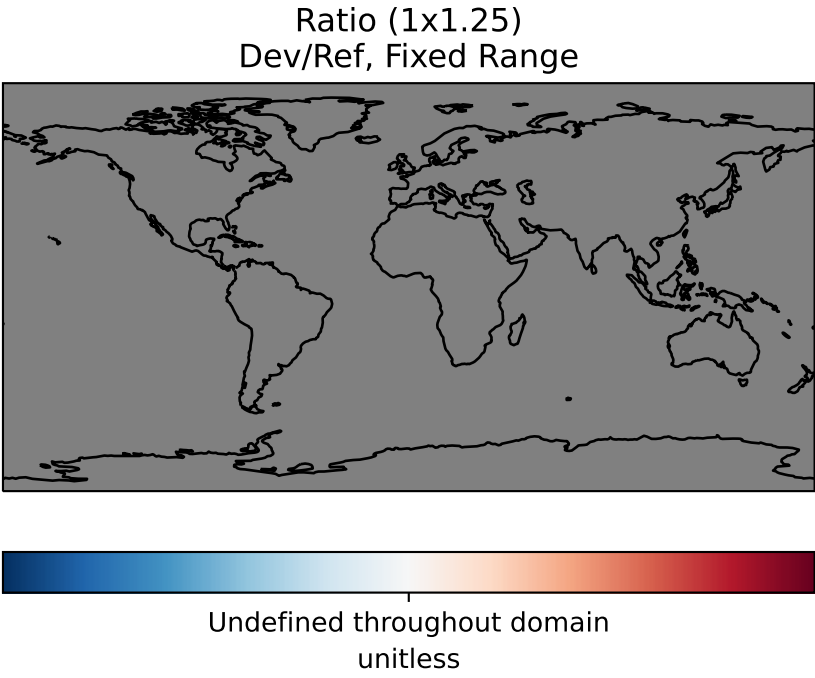
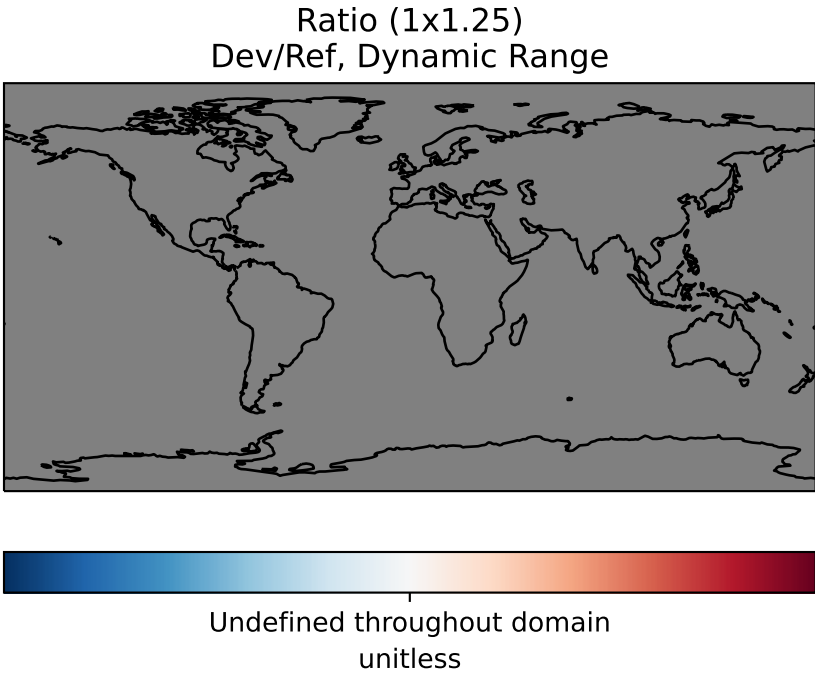
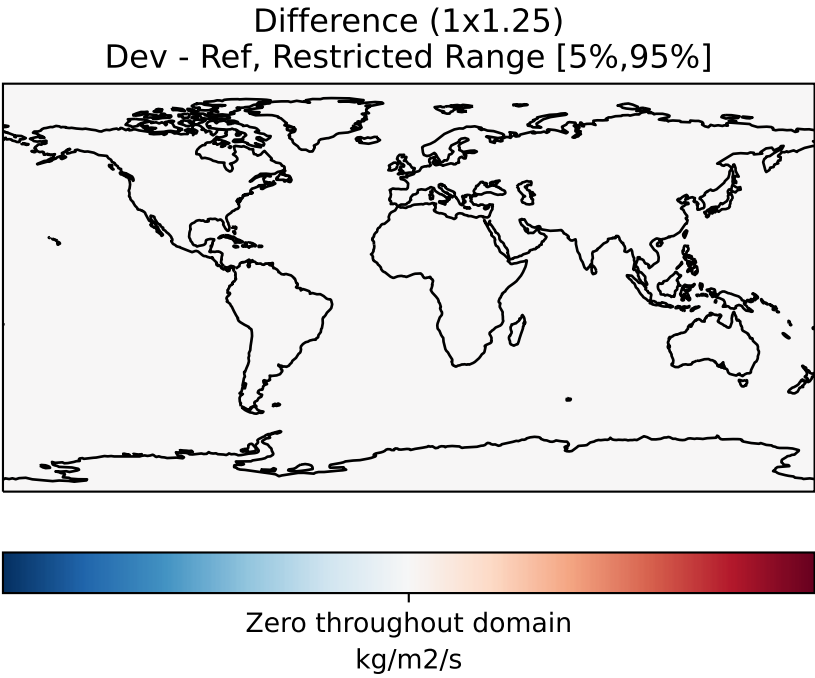
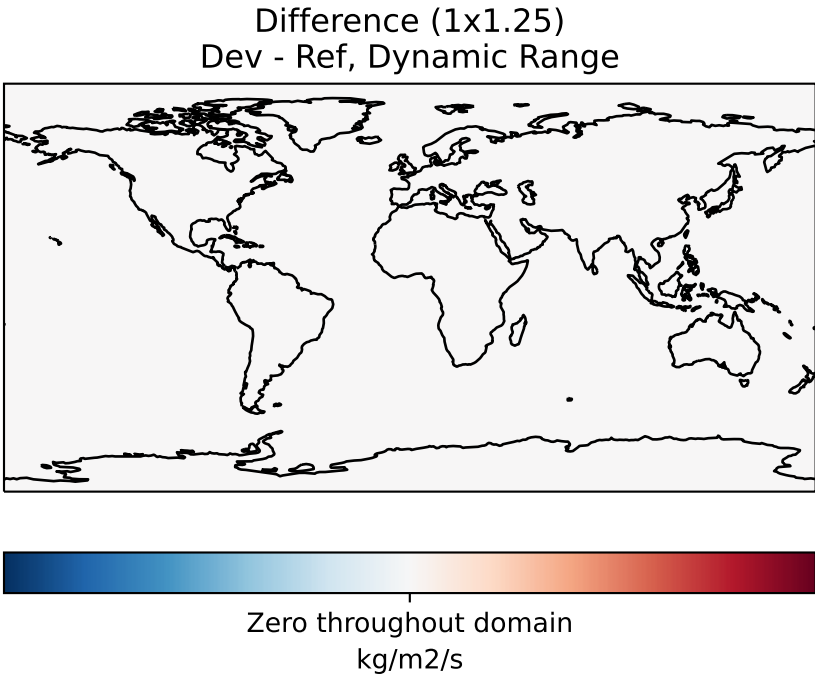
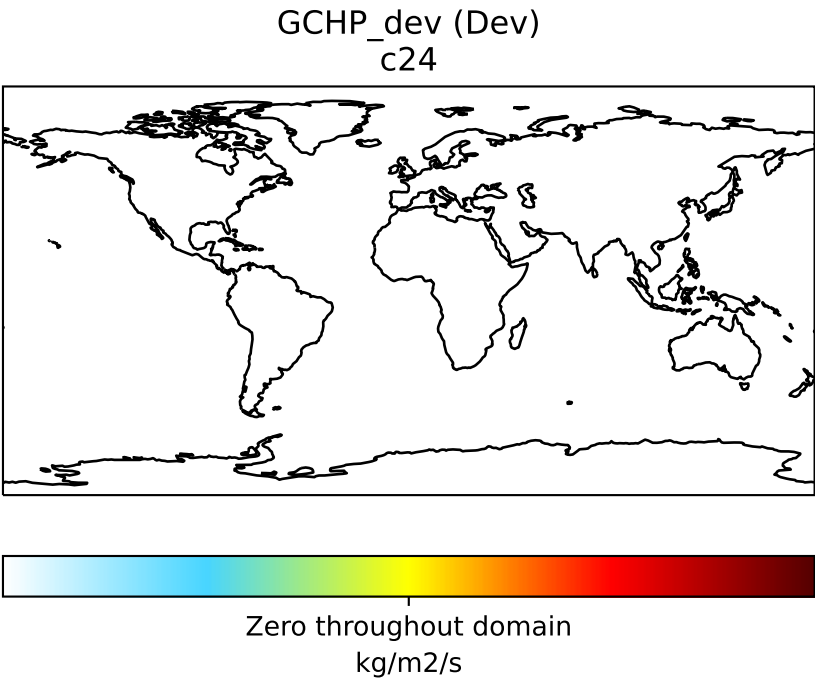
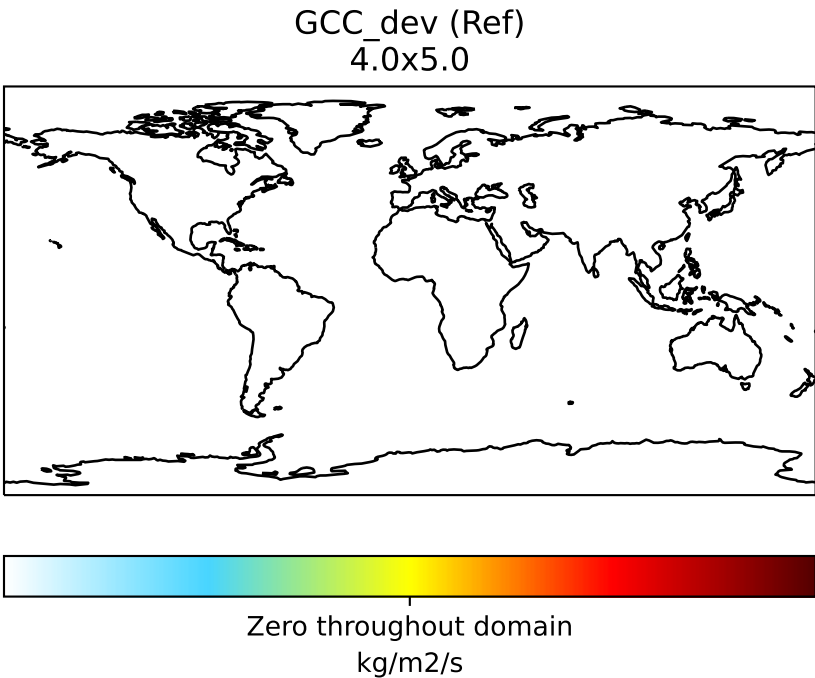
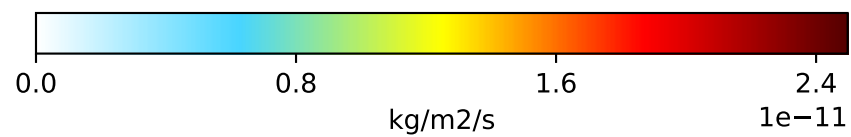


# EmisALD2\_Ship

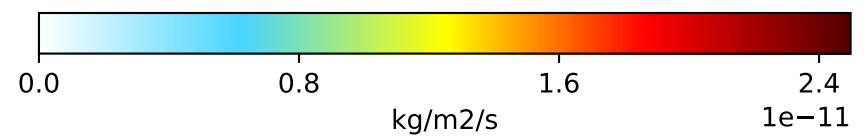
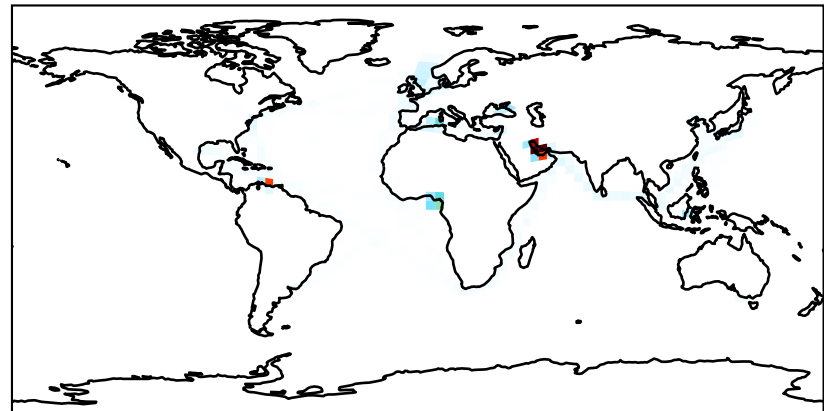


# EmisALK4\_Ship

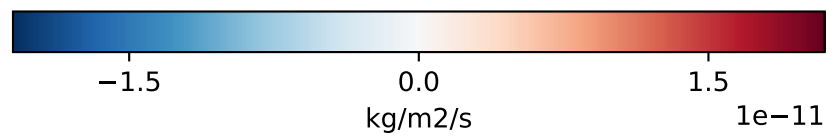
GCC\_dev (Ref)  
4.0x5.0



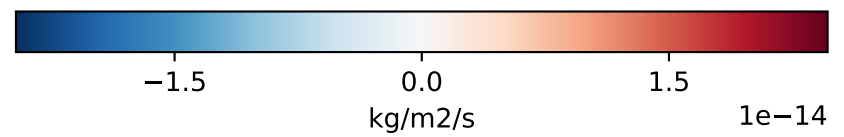
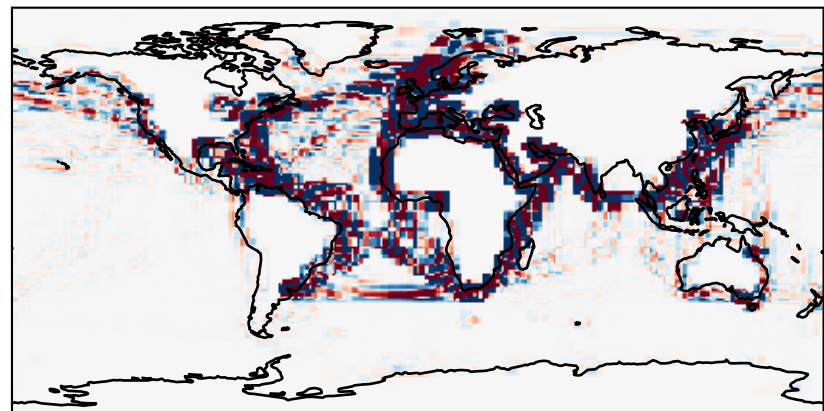
GCHP\_dev (Dev)  
c24



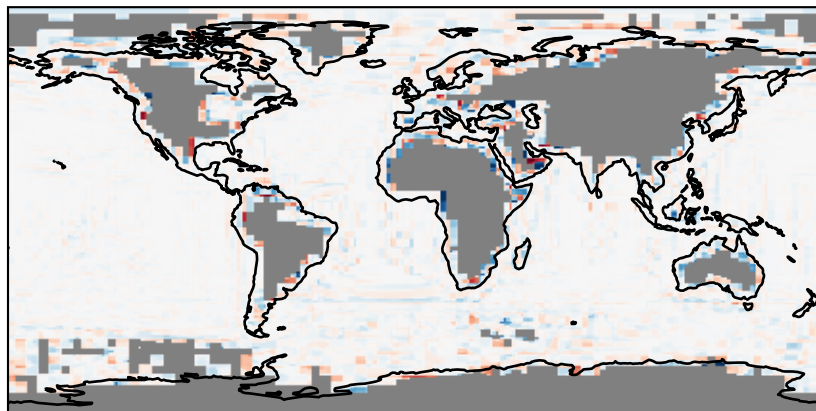
Difference (1x1.25)  
Dev - Ref, Dynamic Range



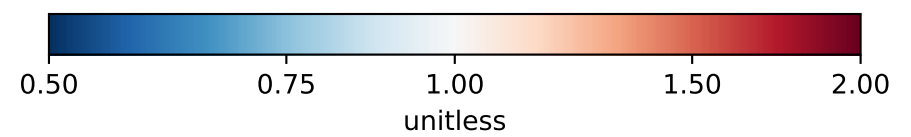
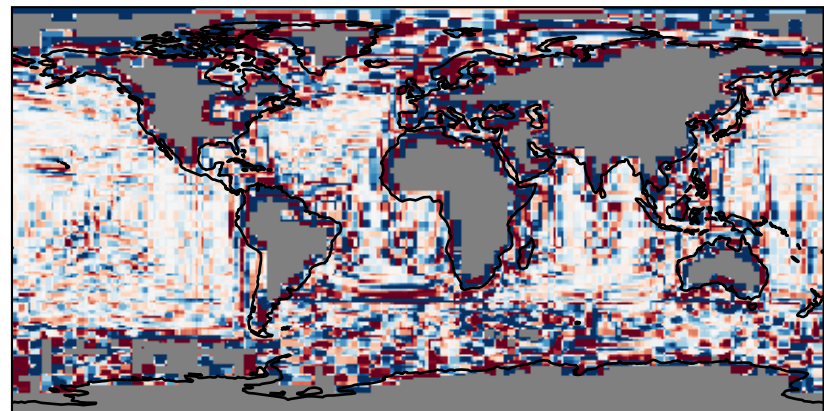
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

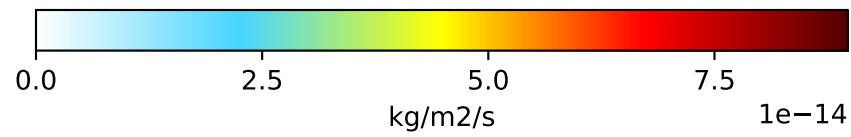
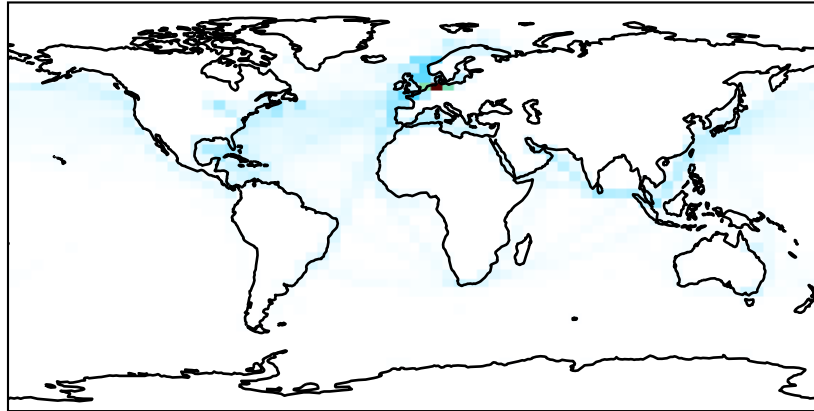


Ratio (1x1.25)  
Dev/Ref, Fixed Range

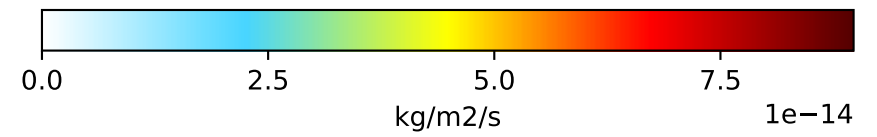
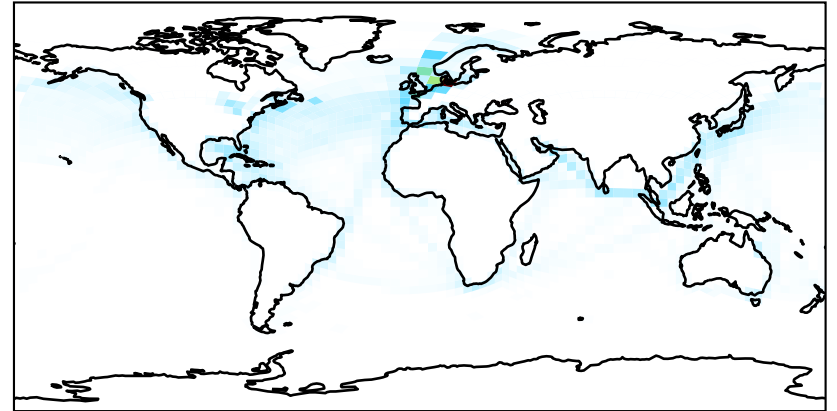


# EmisBCPI\_Ship

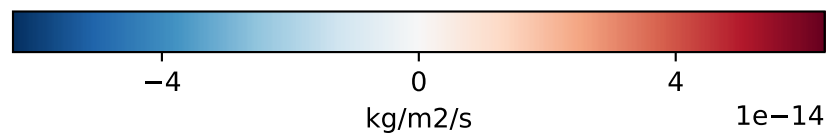
GCC\_dev (Ref)  
4.0x5.0



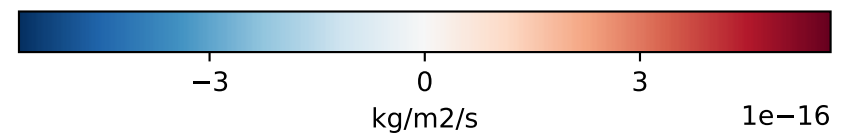
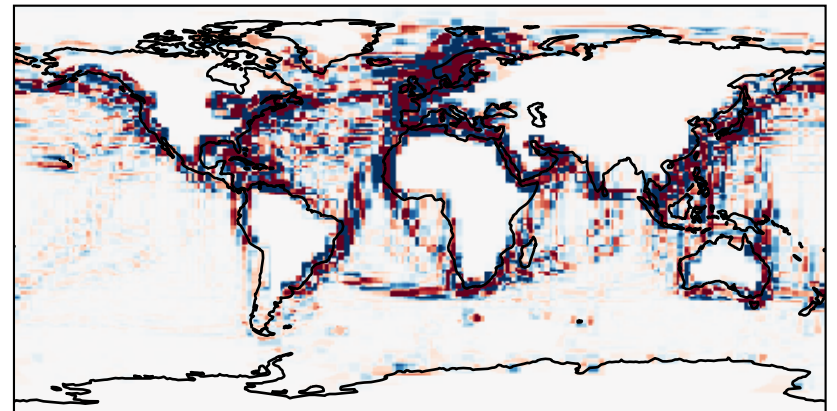
GCHP\_dev (Dev)  
c24



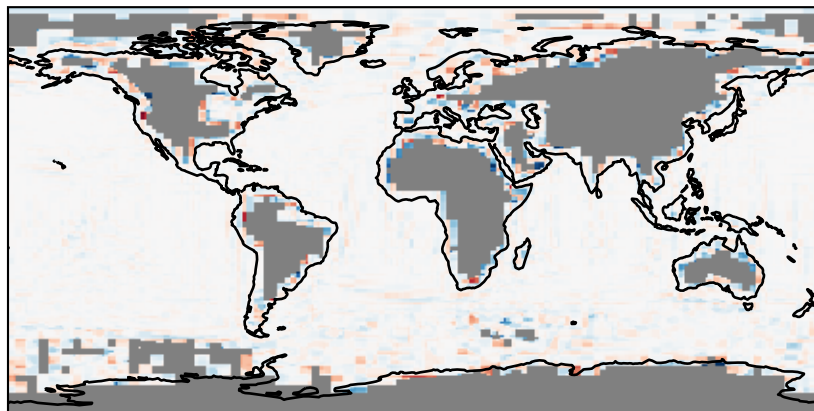
Difference (1x1.25)  
Dev - Ref, Dynamic Range



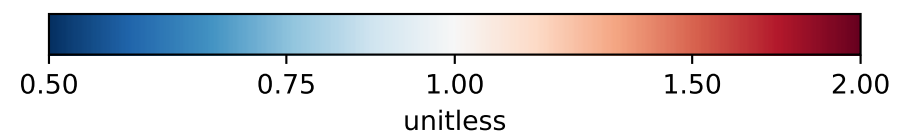
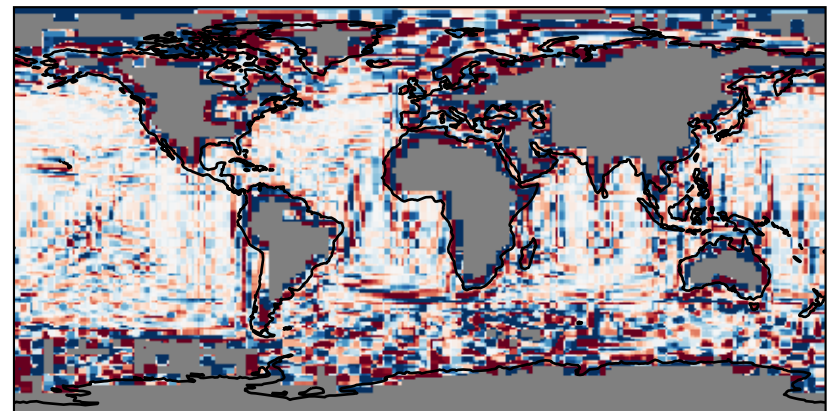
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

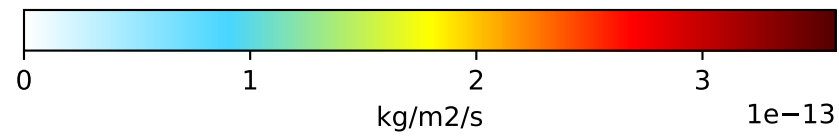
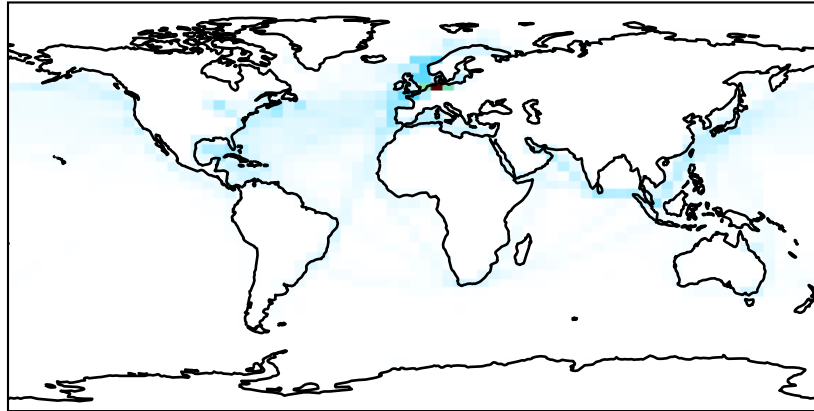


Ratio (1x1.25)  
Dev/Ref, Fixed Range

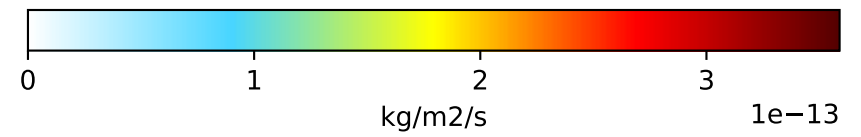
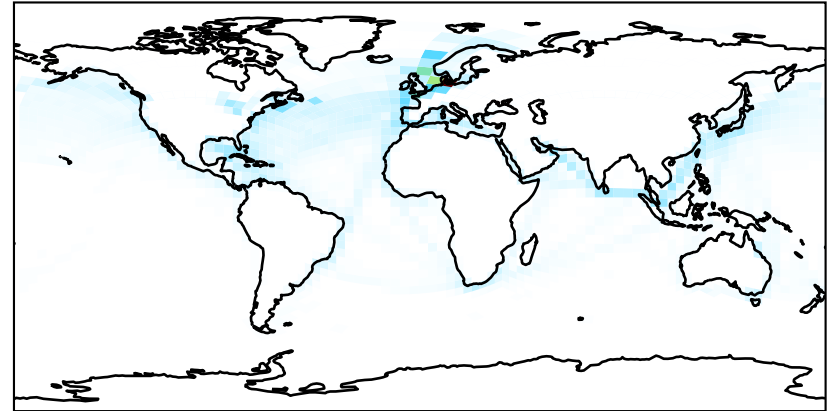


# EmisBCPO\_Ship

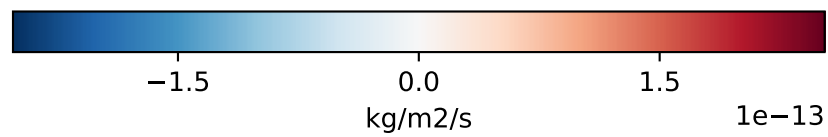
GCC\_dev (Ref)  
4.0x5.0



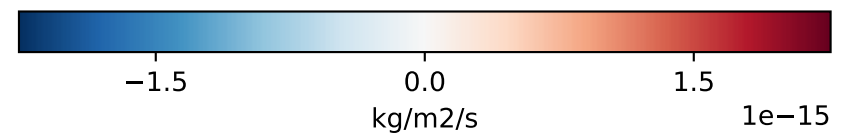
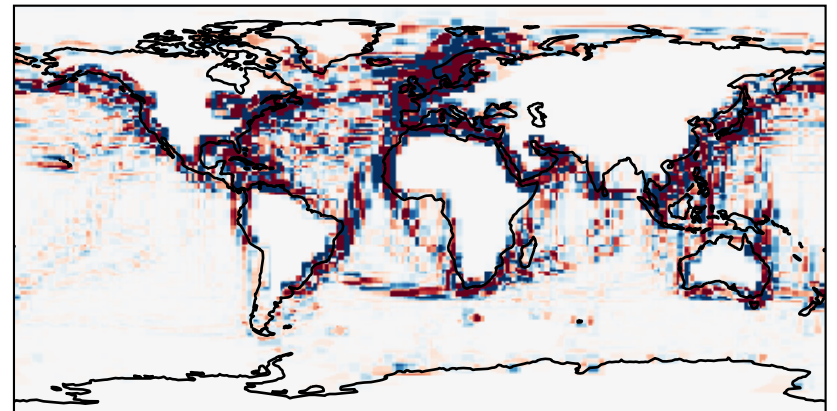
GCHP\_dev (Dev)  
c24



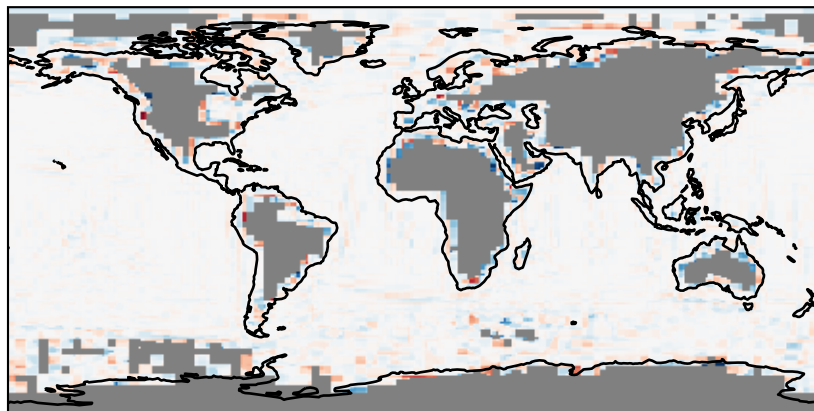
Difference (1x1.25)  
Dev - Ref, Dynamic Range



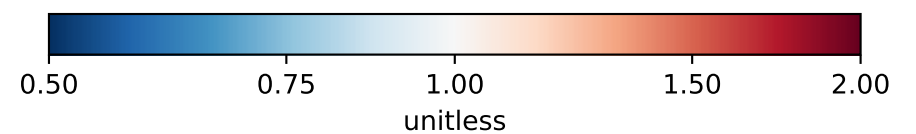
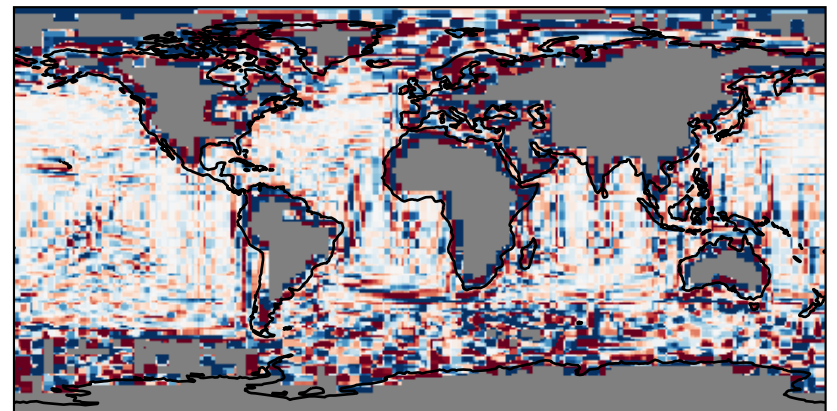
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

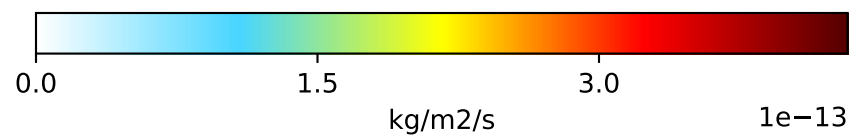
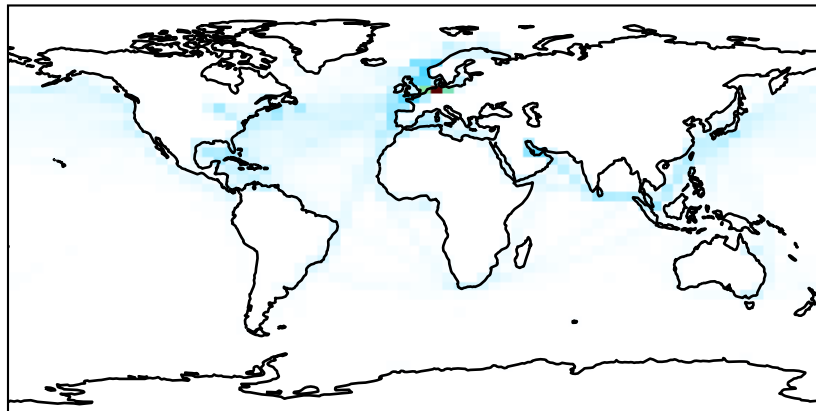


Ratio (1x1.25)  
Dev/Ref, Fixed Range

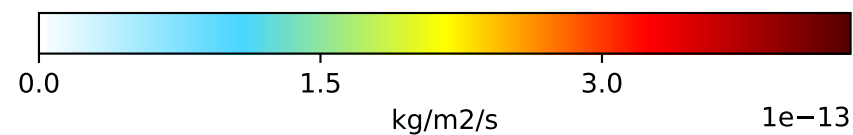
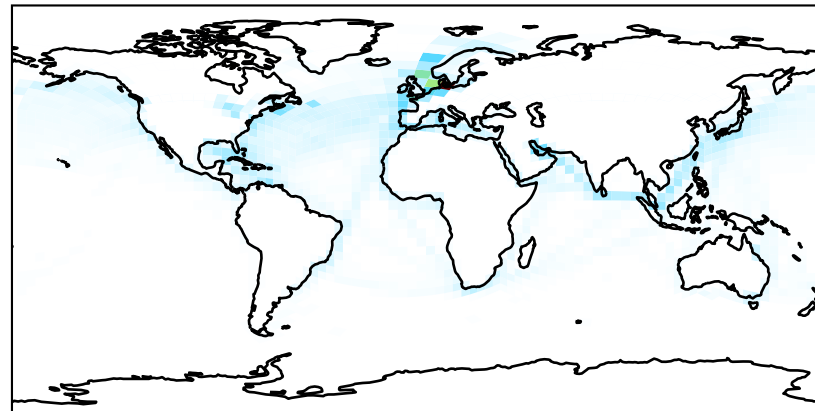


# EmisBENZ\_Ship

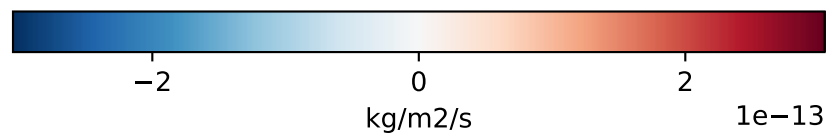
GCC\_dev (Ref)  
4.0x5.0



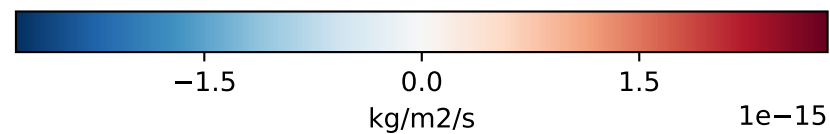
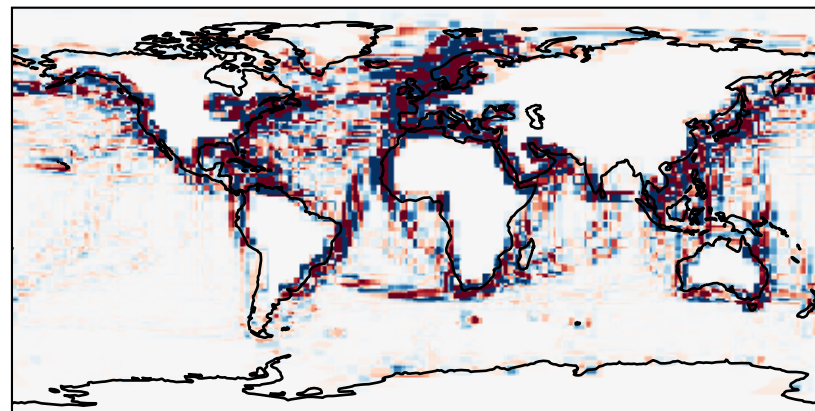
GCHP\_dev (Dev)  
c24



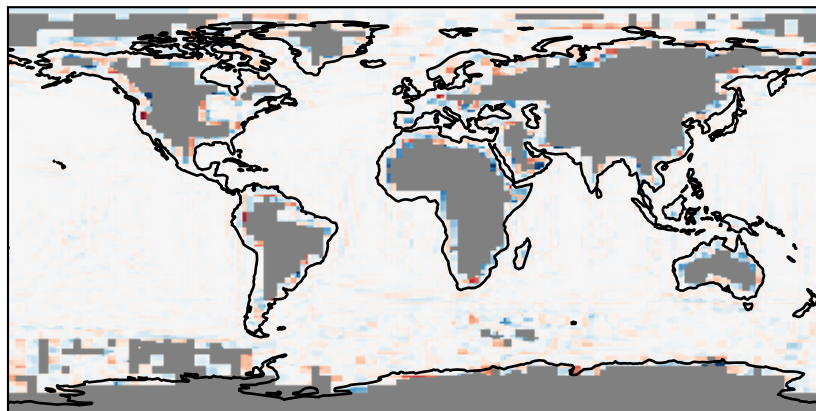
Difference (1x1.25)  
Dev - Ref, Dynamic Range



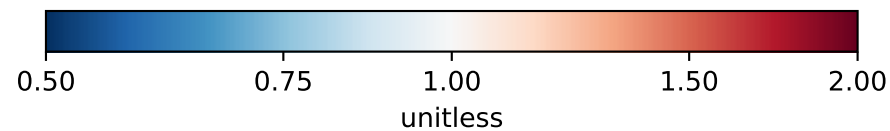
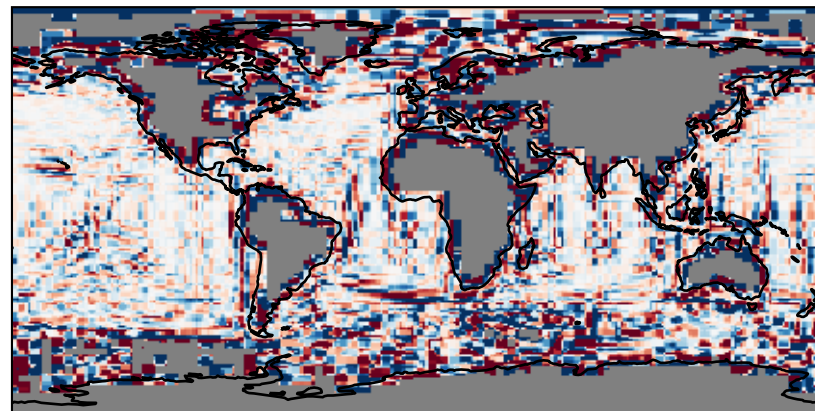
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

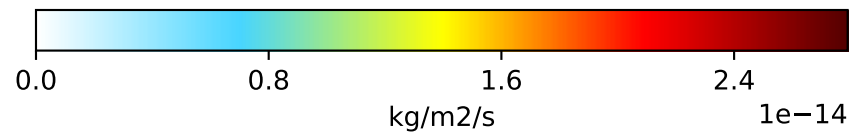
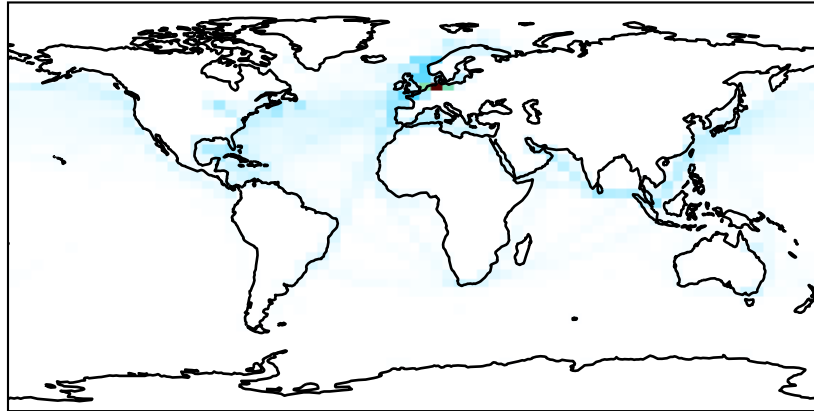


Ratio (1x1.25)  
Dev/Ref, Fixed Range

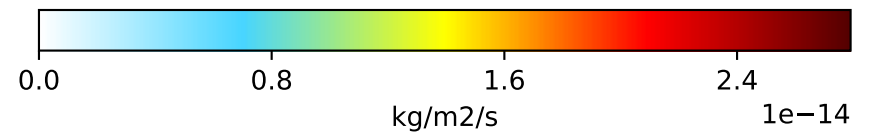
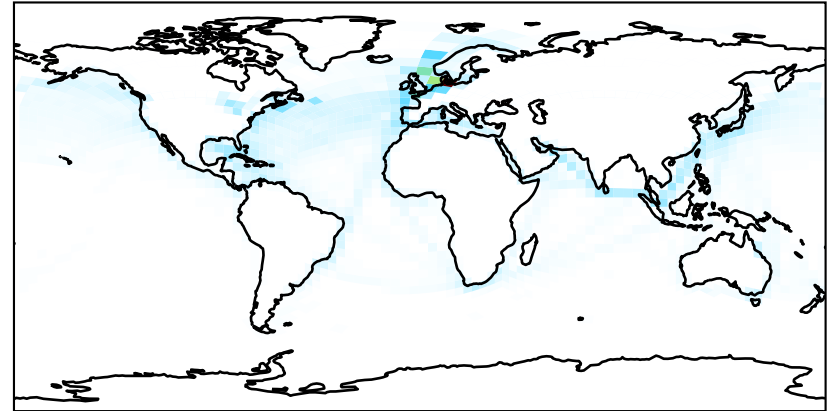


# EmisC2H2\_Ship

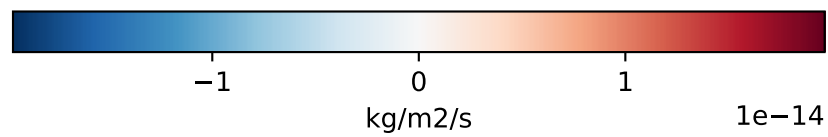
GCC\_dev (Ref)  
4.0x5.0



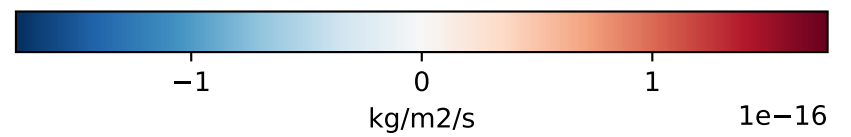
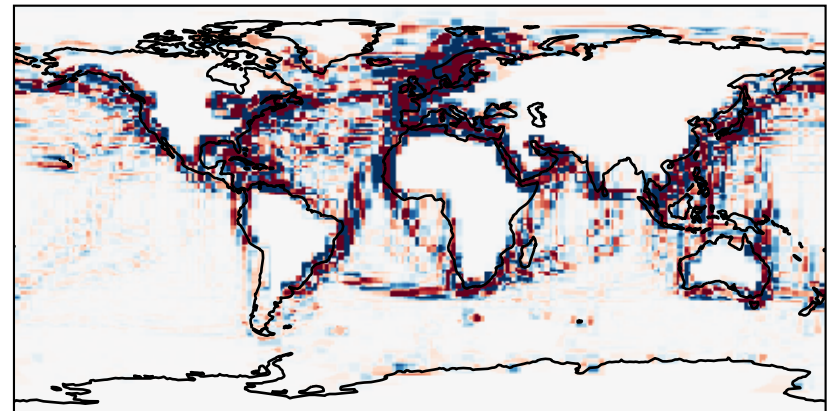
GCHP\_dev (Dev)  
c24



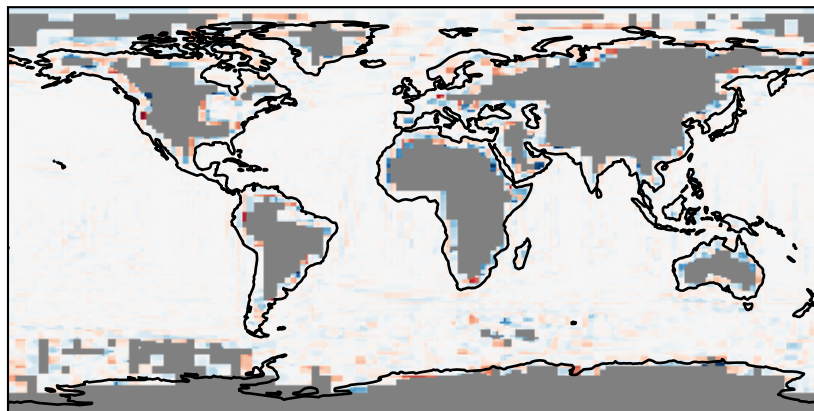
Difference (1x1.25)  
Dev - Ref, Dynamic Range



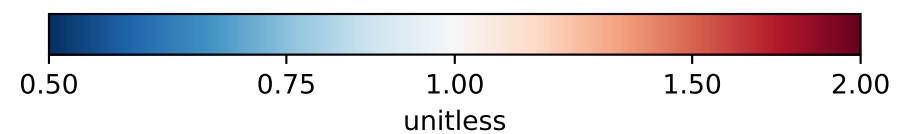
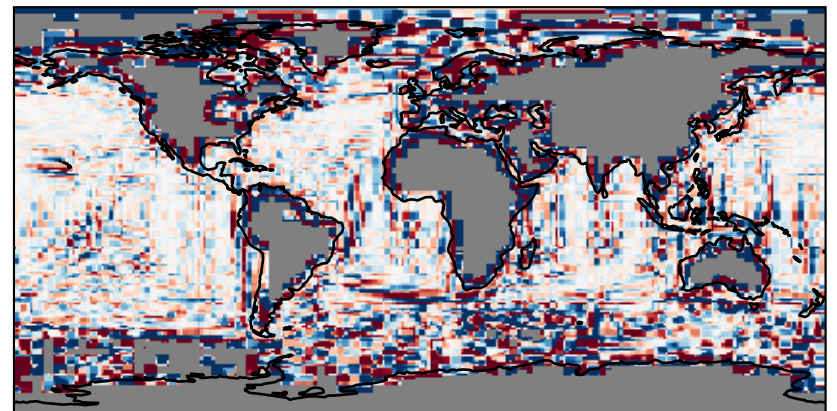
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

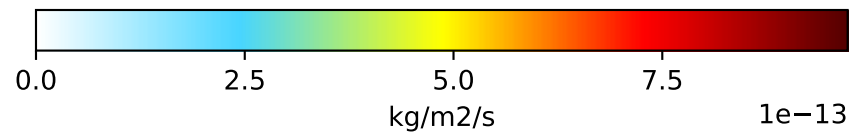
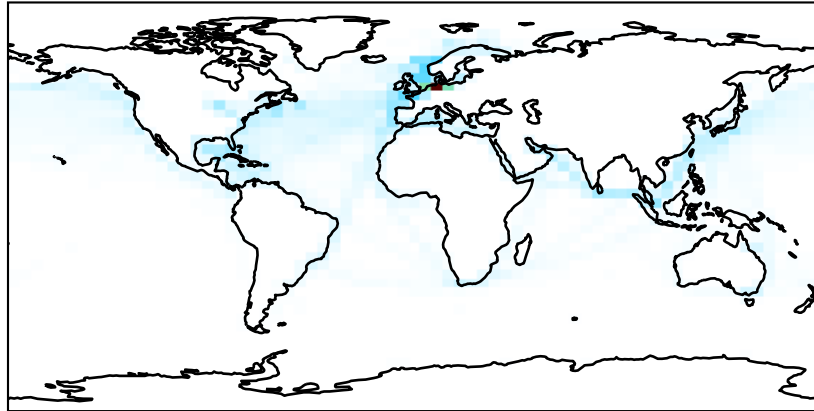


Ratio (1x1.25)  
Dev/Ref, Fixed Range

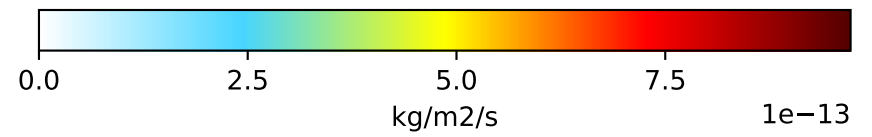
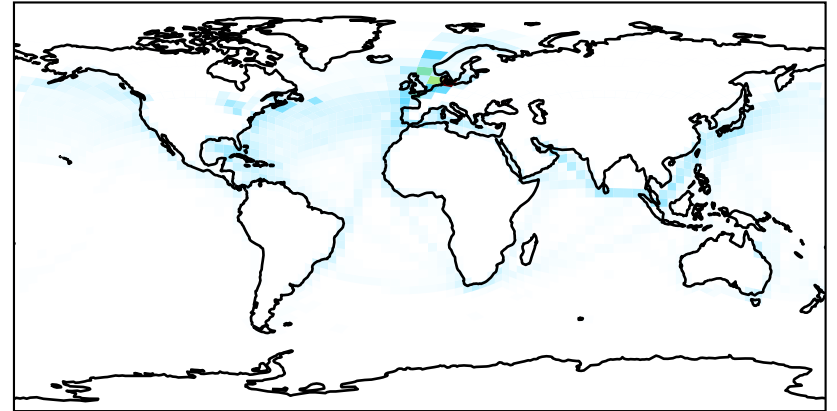


# EmisC2H4\_Ship

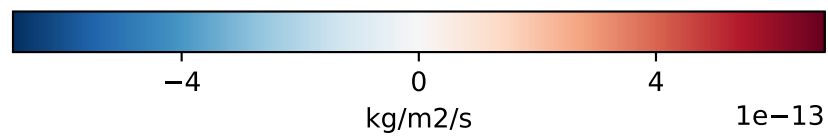
GCC\_dev (Ref)  
4.0x5.0



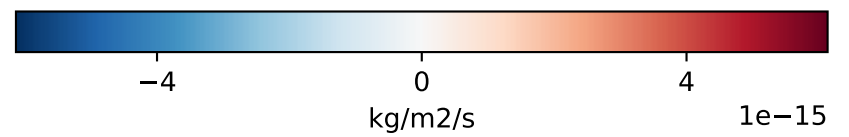
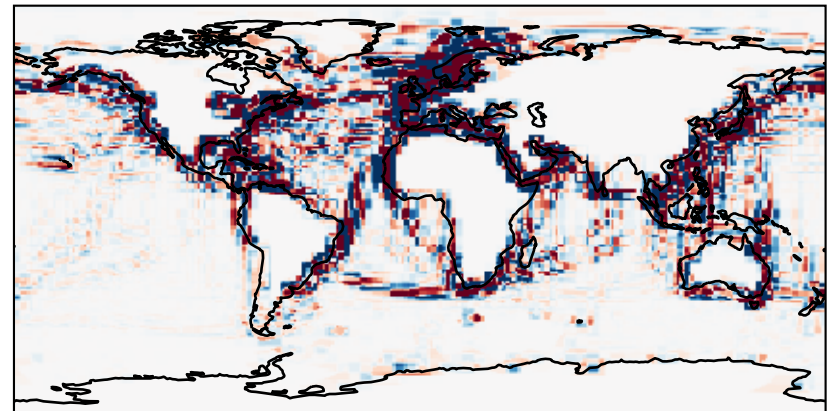
GCHP\_dev (Dev)  
c24



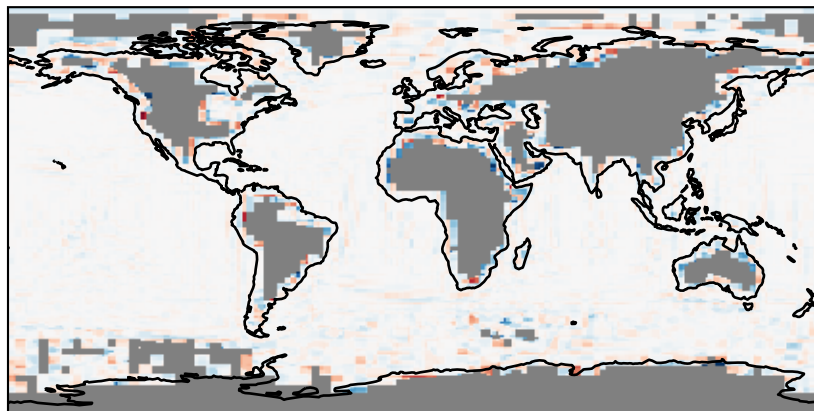
Difference (1x1.25)  
Dev - Ref, Dynamic Range



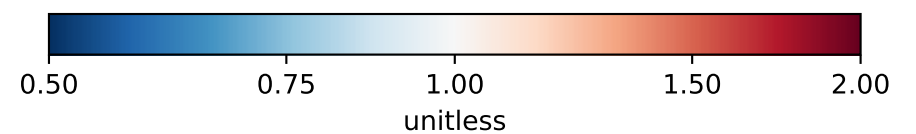
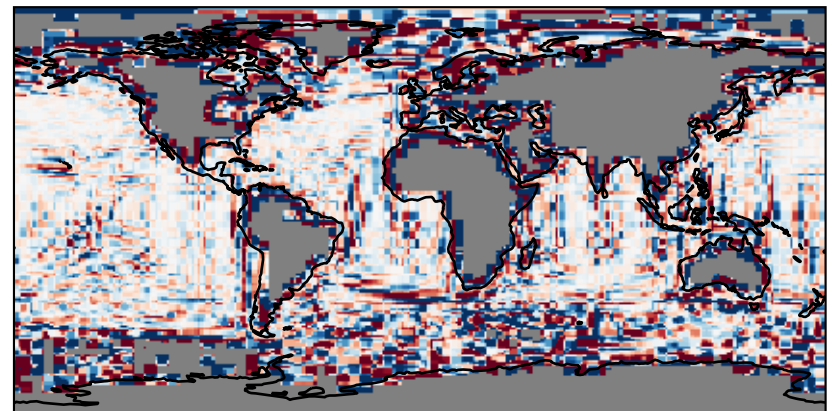
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

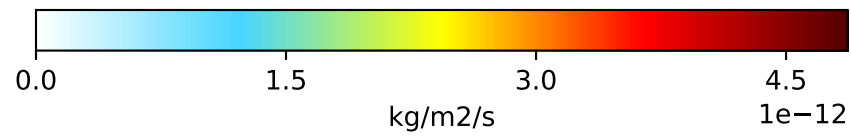


Ratio (1x1.25)  
Dev/Ref, Fixed Range

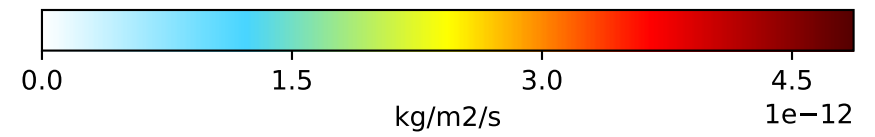
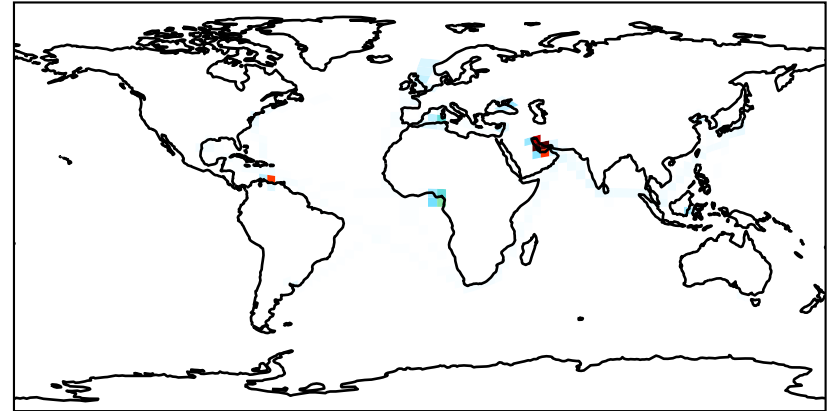


# EmisC2H6\_Ship

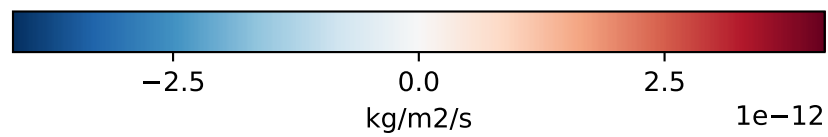
GCC\_dev (Ref)  
4.0x5.0



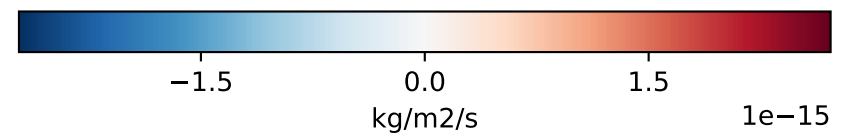
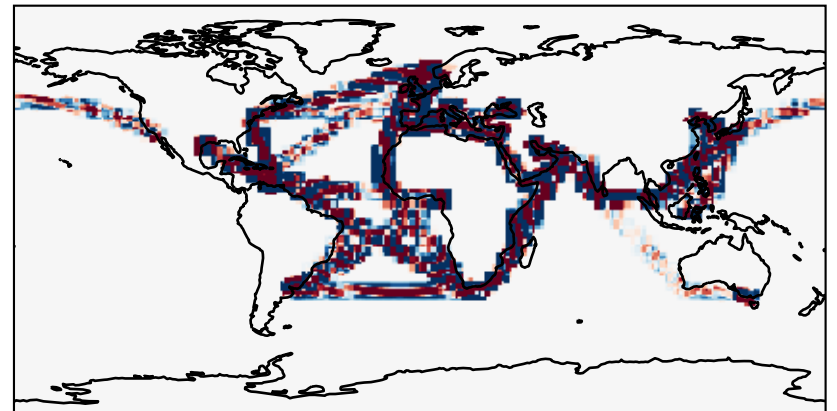
GCHP\_dev (Dev)  
c24



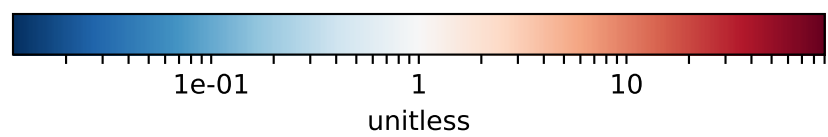
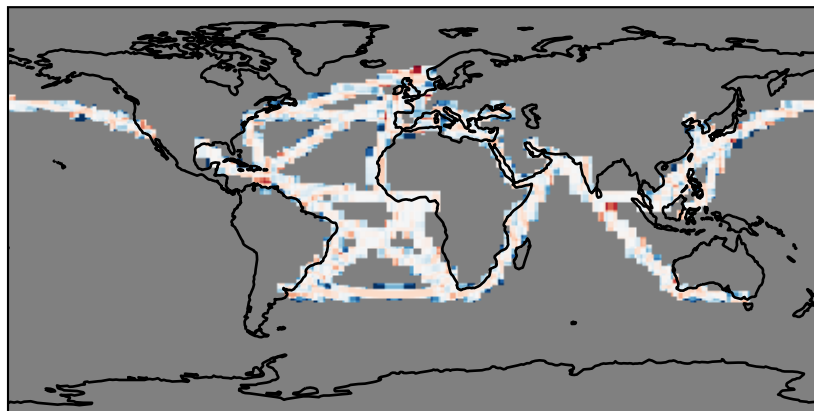
Difference (1x1.25)  
Dev - Ref, Dynamic Range



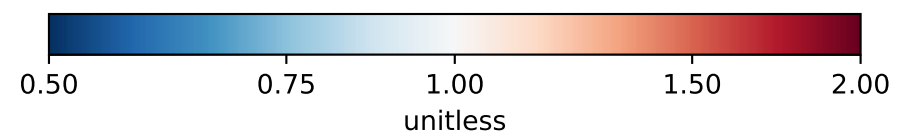
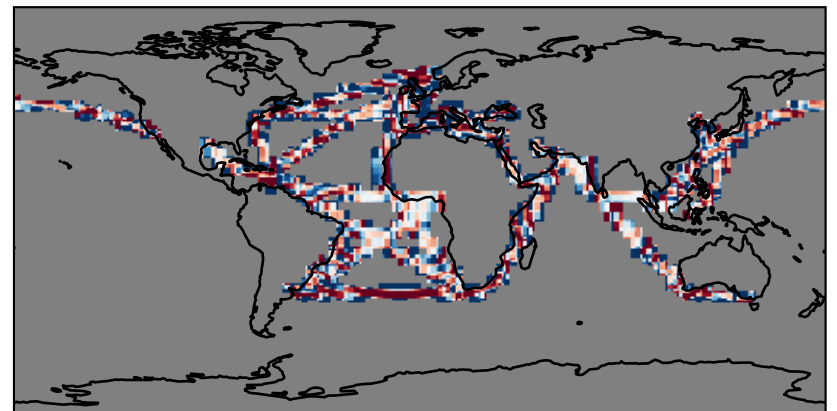
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

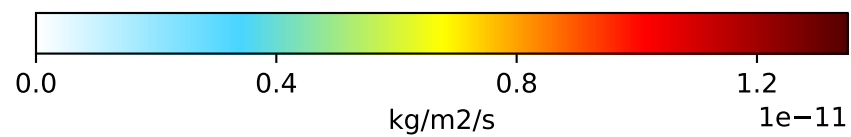


Ratio (1x1.25)  
Dev/Ref, Fixed Range

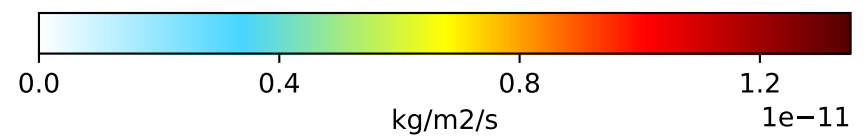
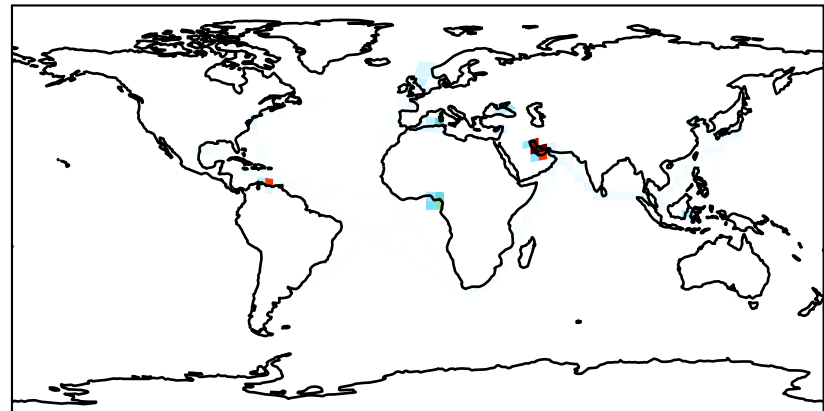


# EmisC3H8\_Ship

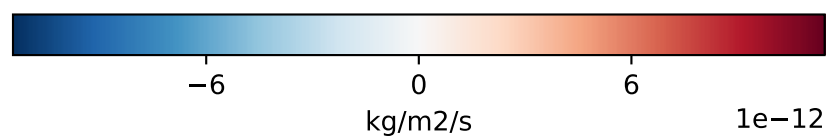
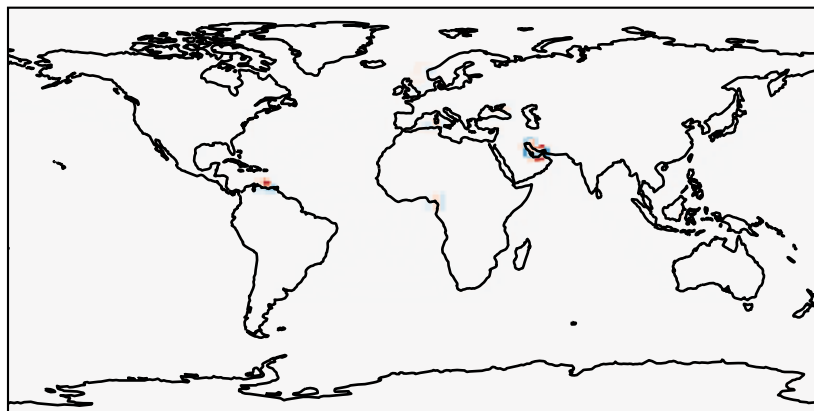
GCC\_dev (Ref)  
4.0x5.0



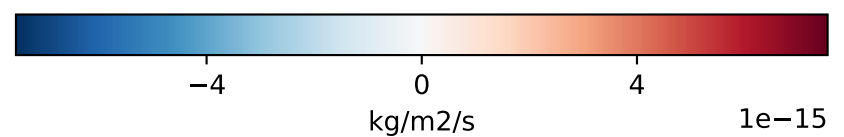
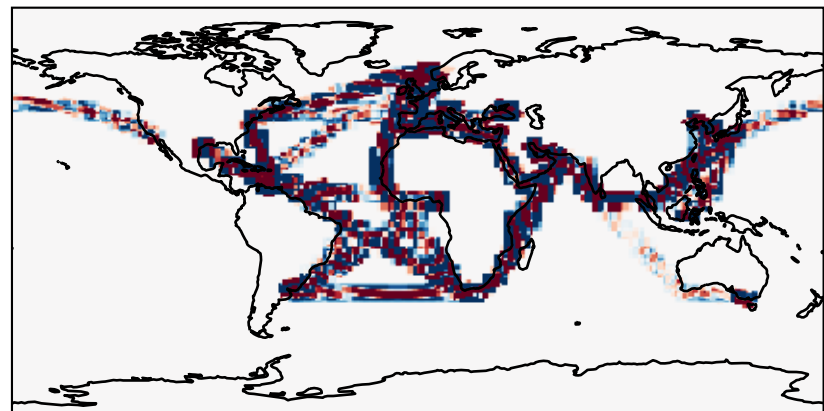
GCHP\_dev (Dev)  
c24



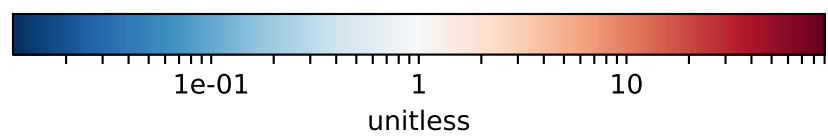
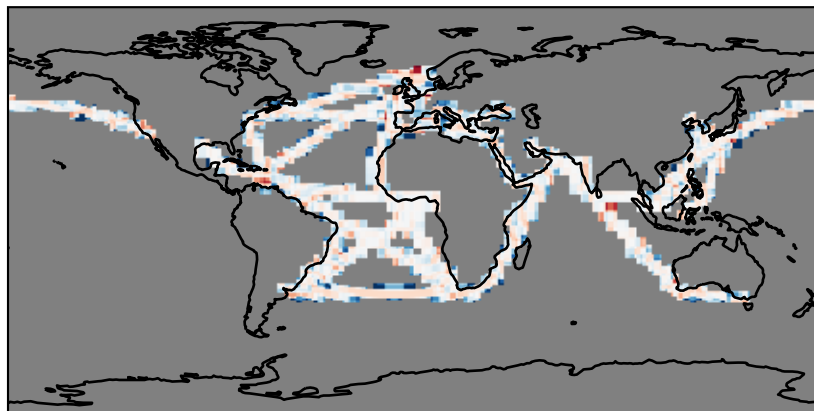
Difference (1x1.25)  
Dev - Ref, Dynamic Range



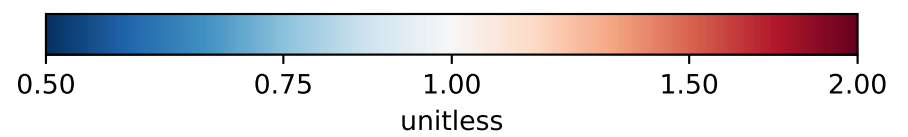
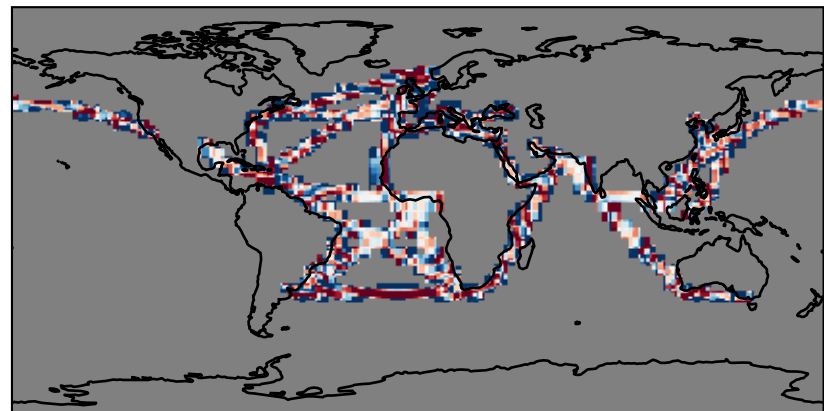
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

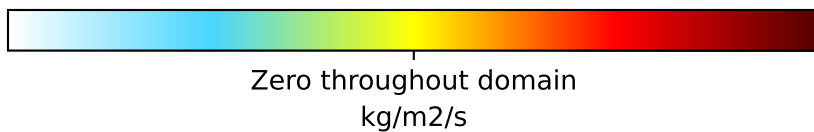


Ratio (1x1.25)  
Dev/Ref, Fixed Range

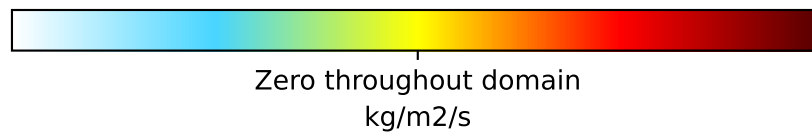
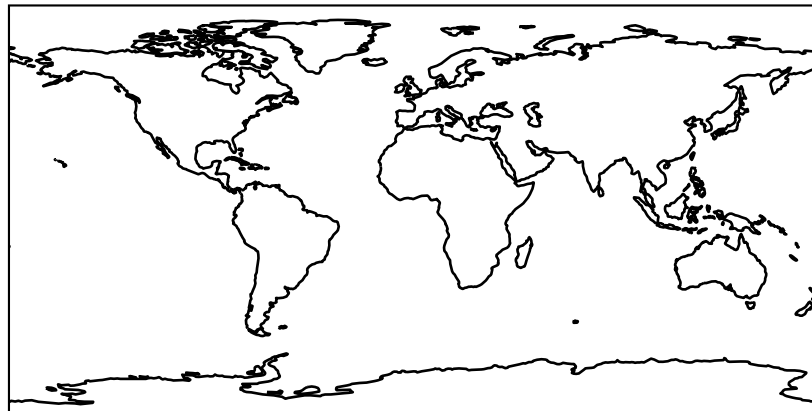


# EmisCH2O\_Ship

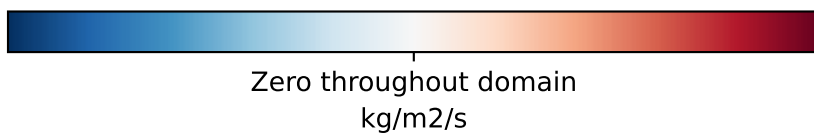
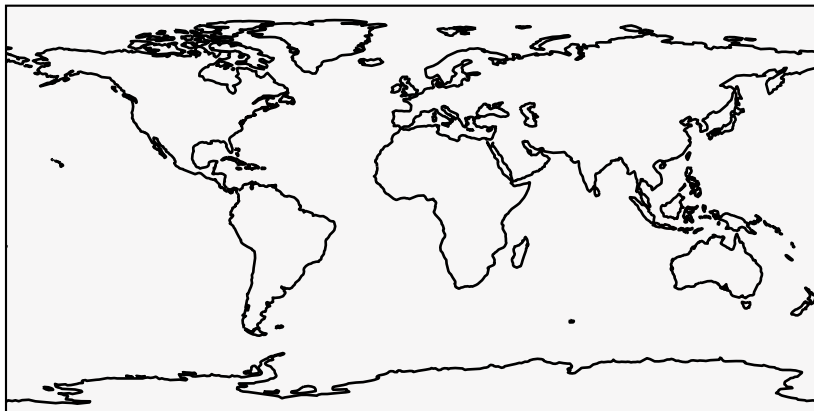
GCC\_dev (Ref)  
4.0x5.0



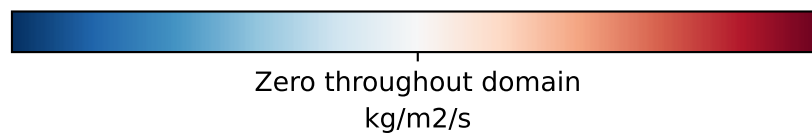
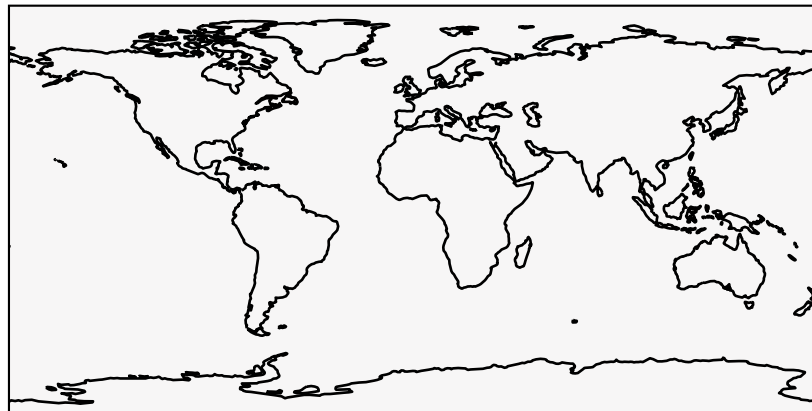
GCHP\_dev (Dev)  
c24



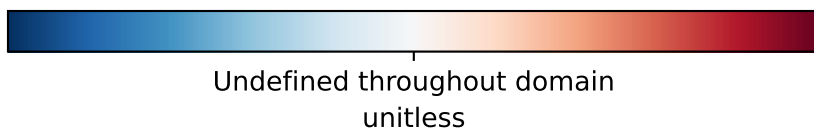
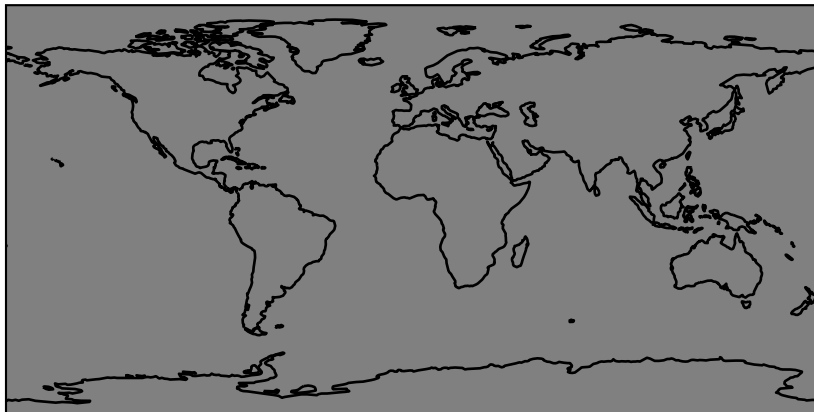
Difference (1x1.25)  
Dev - Ref, Dynamic Range



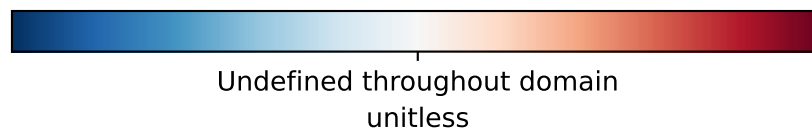
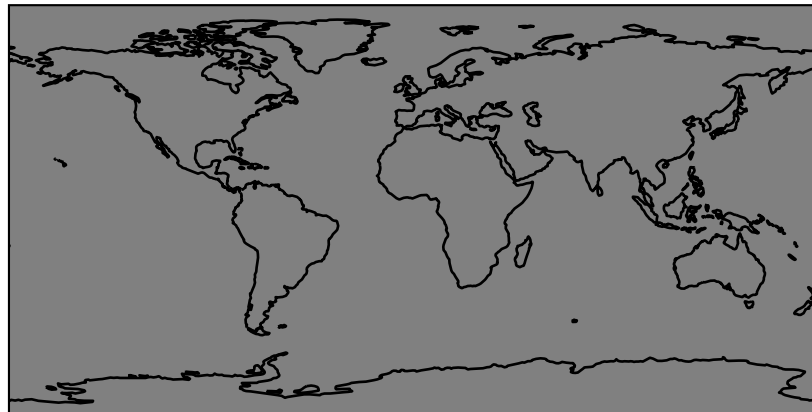
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

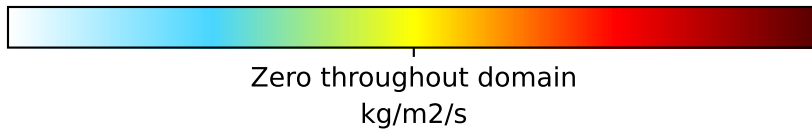


Ratio (1x1.25)  
Dev/Ref, Fixed Range

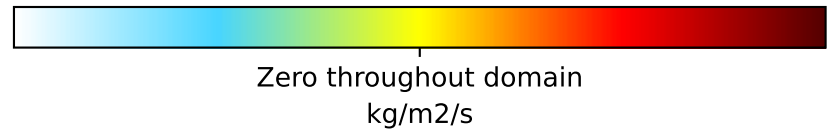
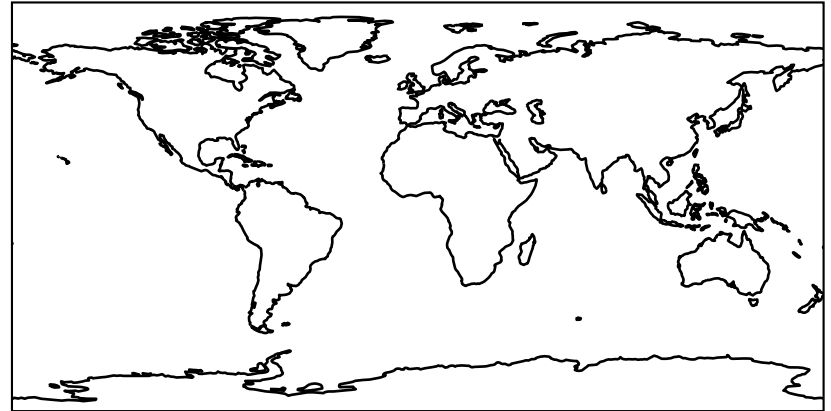


# EmisCO2\_Ship

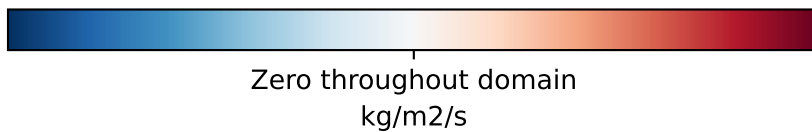
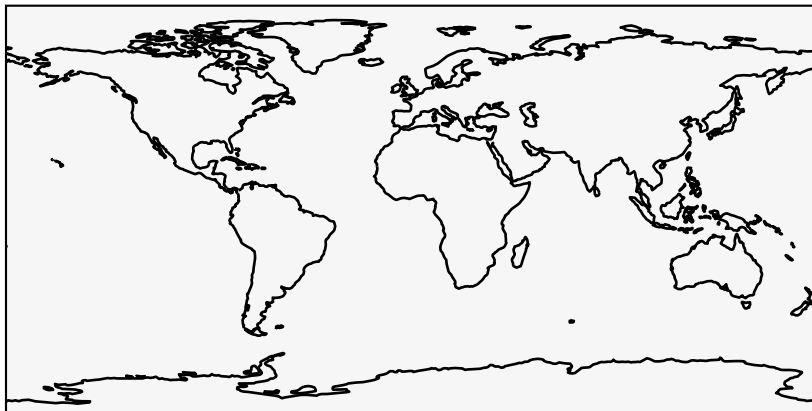
GCC\_dev (Ref)  
4.0x5.0



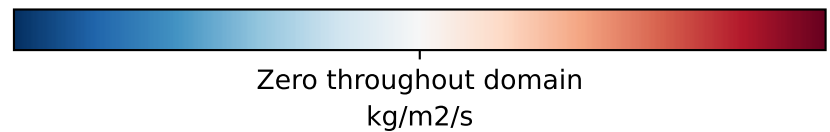
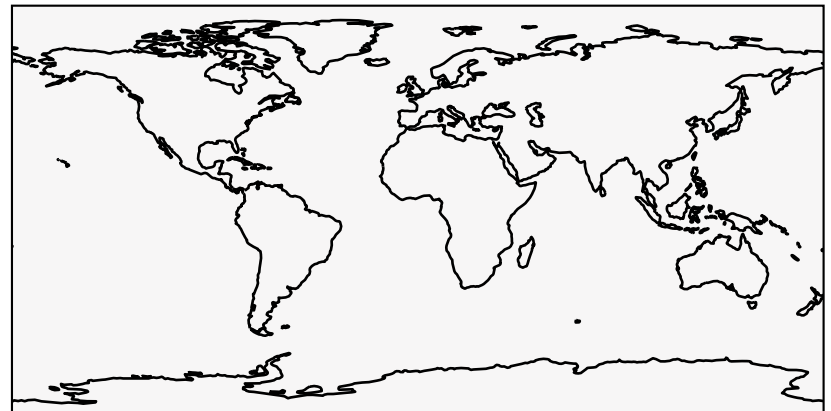
GCHP\_dev (Dev)  
c24



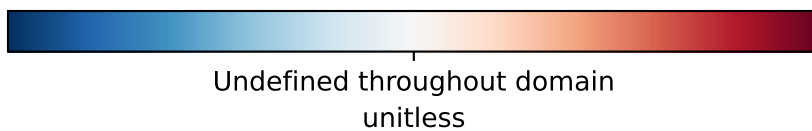
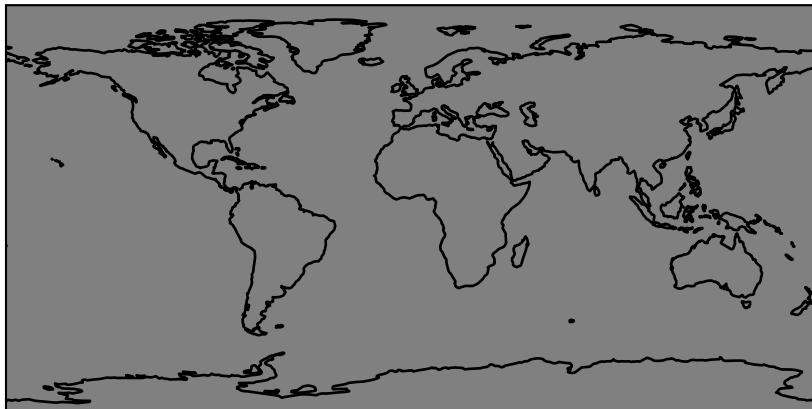
Difference (1x1.25)  
Dev - Ref, Dynamic Range



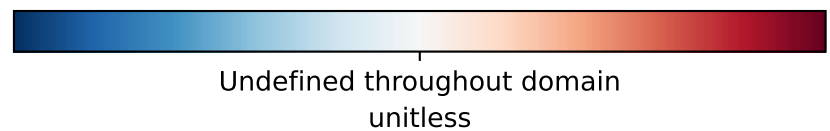
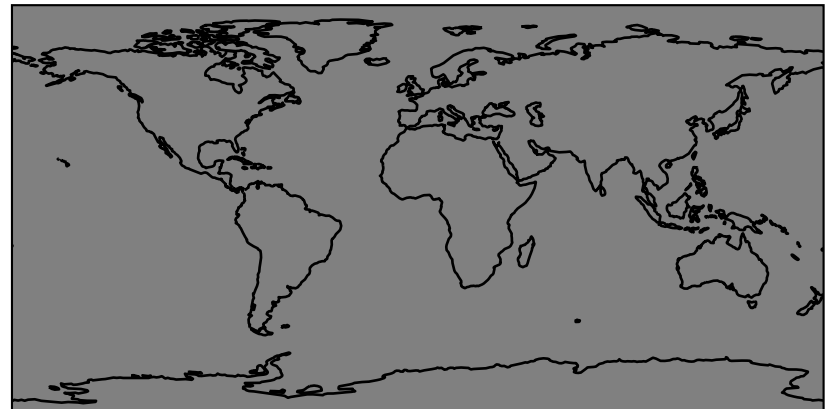
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

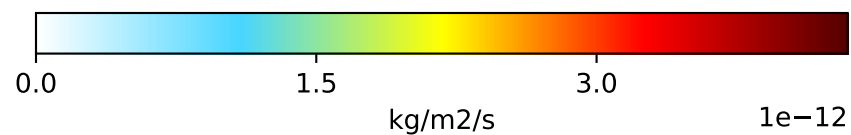
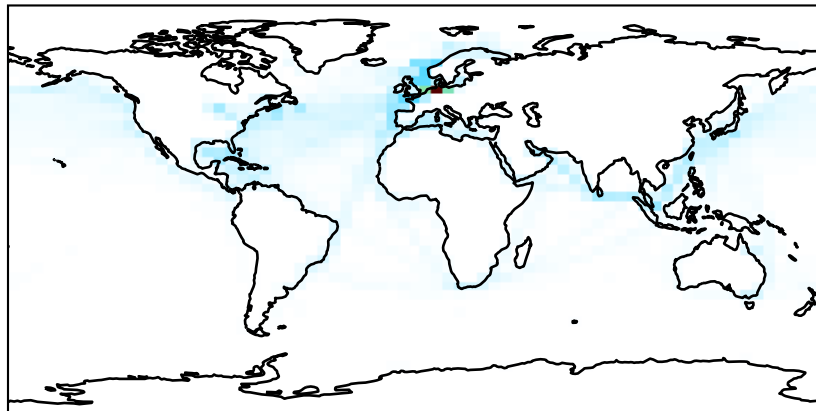


Ratio (1x1.25)  
Dev/Ref, Fixed Range

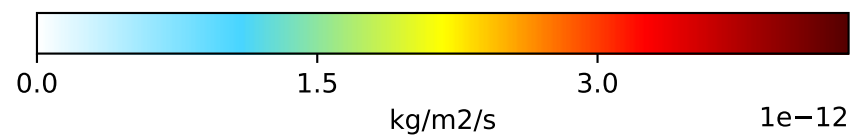
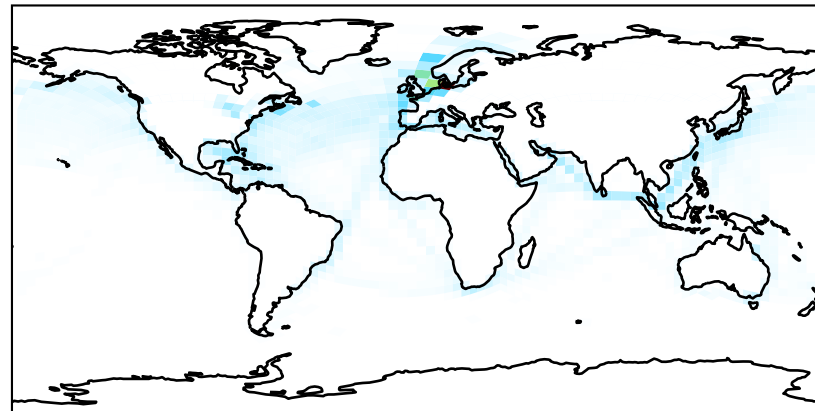


# EmisCO\_Ship

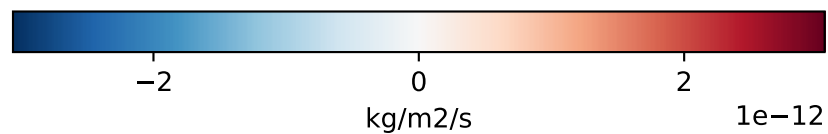
GCC\_dev (Ref)  
4.0x5.0



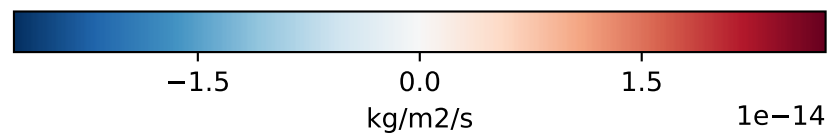
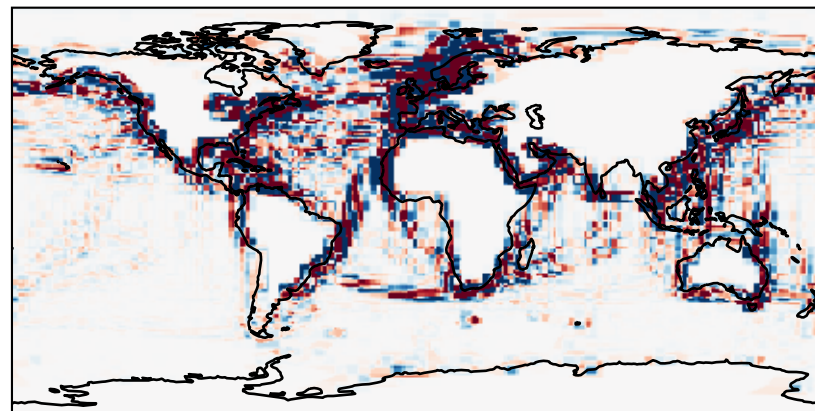
GCHP\_dev (Dev)  
c24



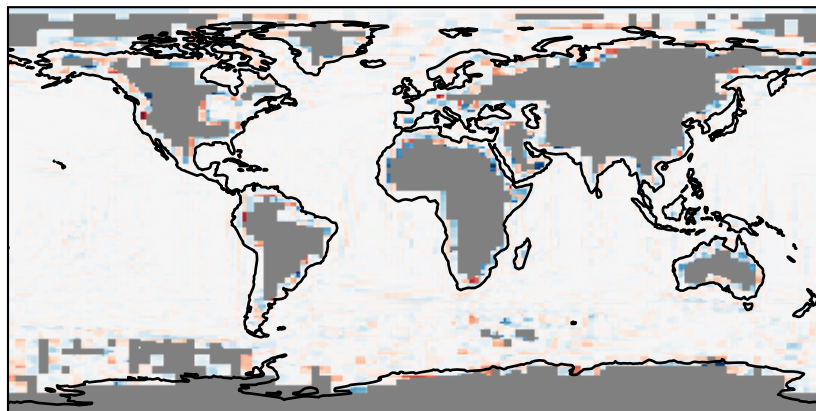
Difference (1x1.25)  
Dev - Ref, Dynamic Range



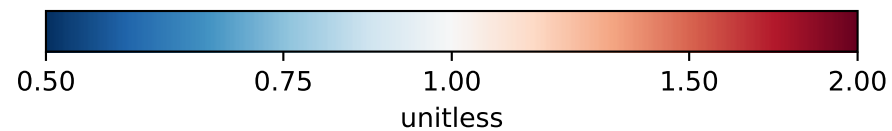
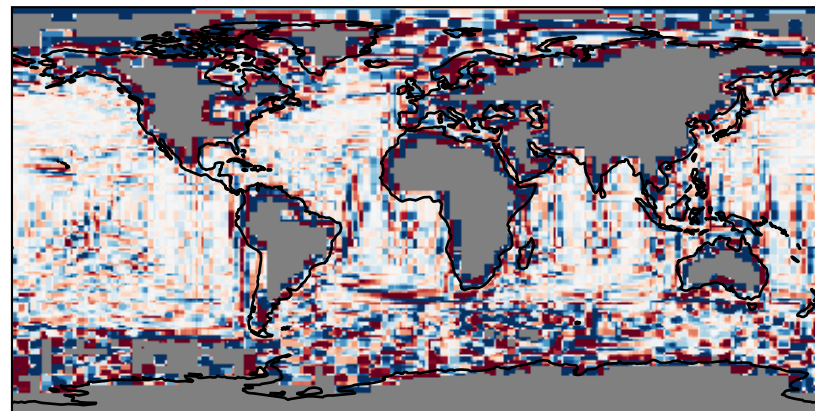
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

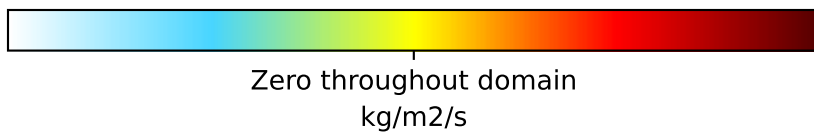


Ratio (1x1.25)  
Dev/Ref, Fixed Range

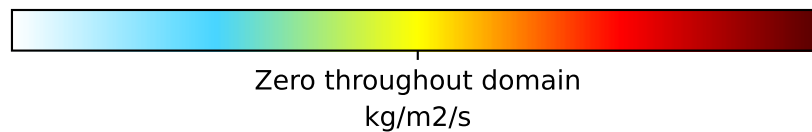
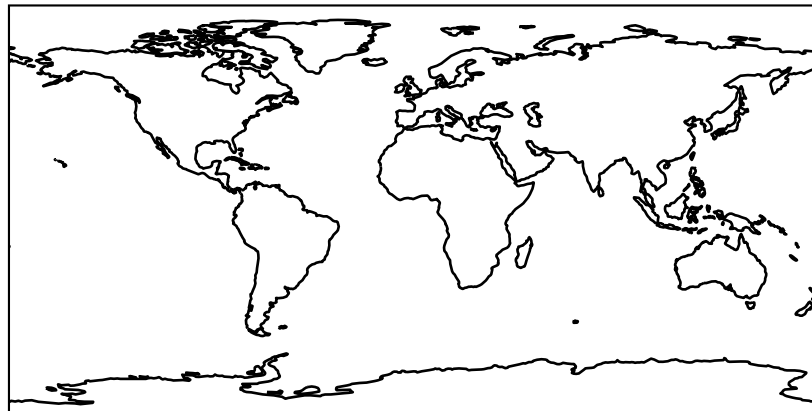


# EmisEOH\_Ship

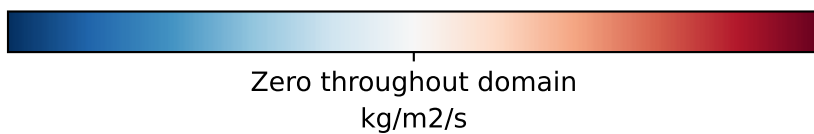
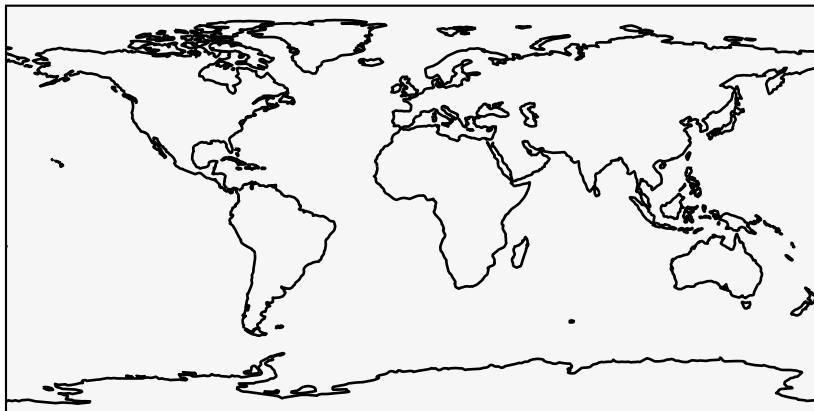
GCC\_dev (Ref)  
4.0x5.0



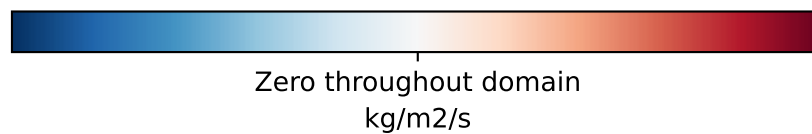
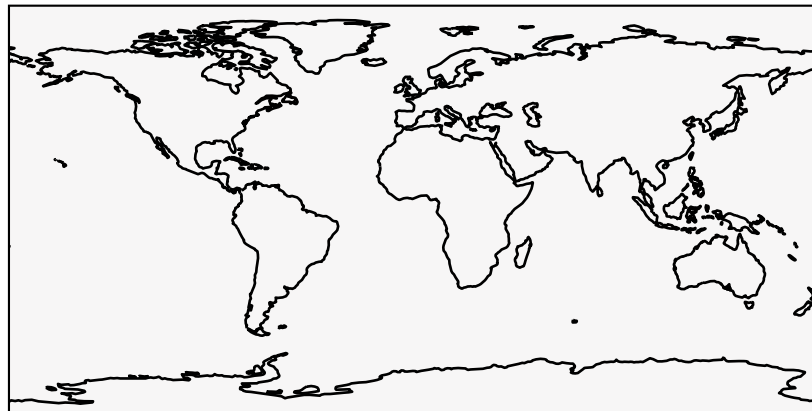
GCHP\_dev (Dev)  
c24



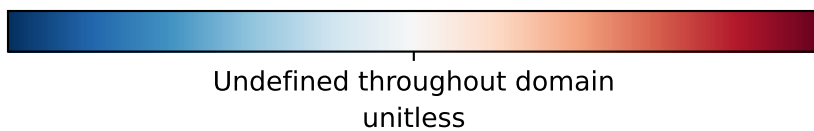
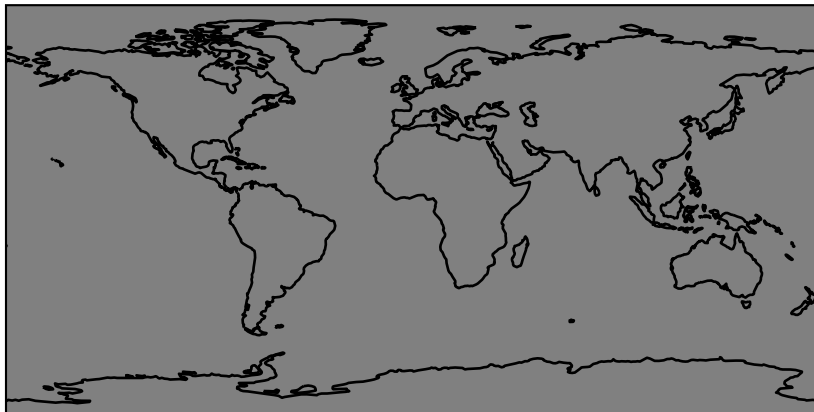
Difference (1x1.25)  
Dev - Ref, Dynamic Range



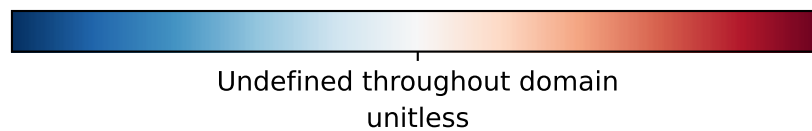
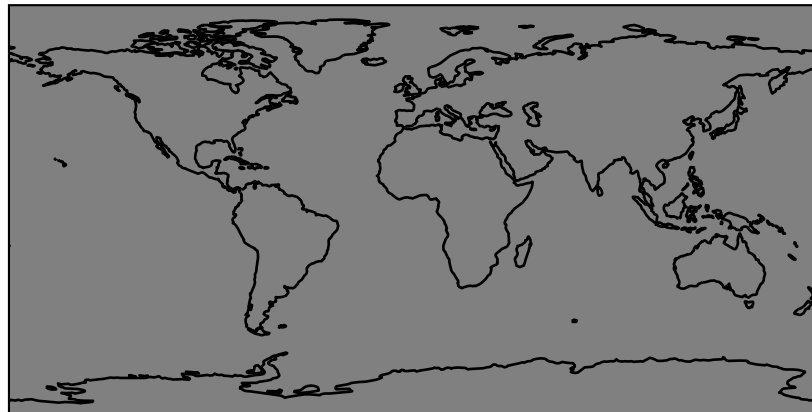
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

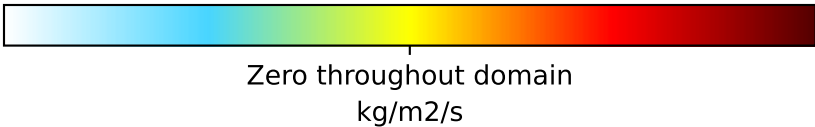
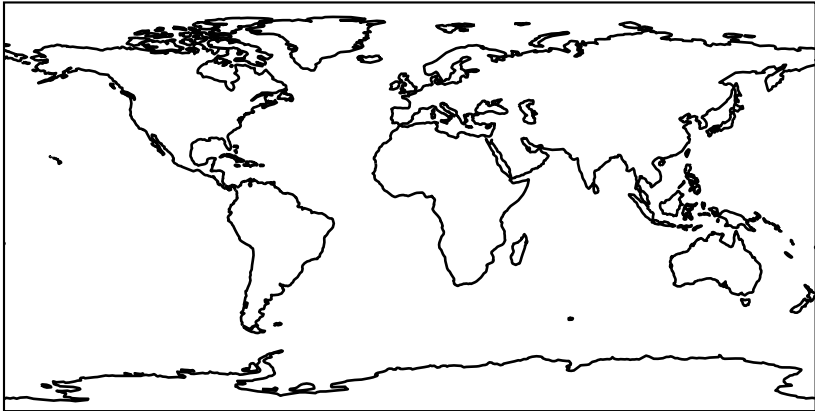


Ratio (1x1.25)  
Dev/Ref, Fixed Range

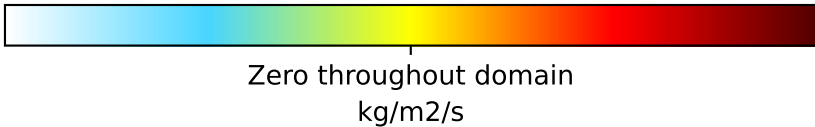
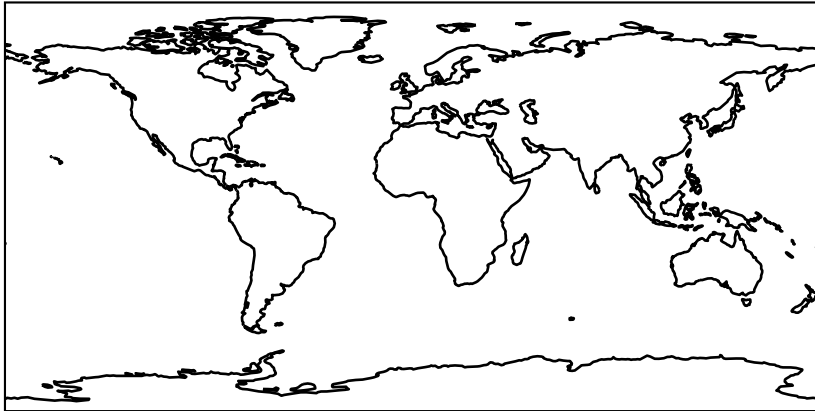


# EmisHCOOH\_Ship

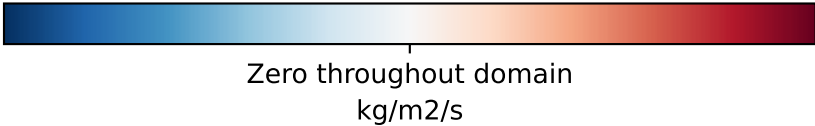
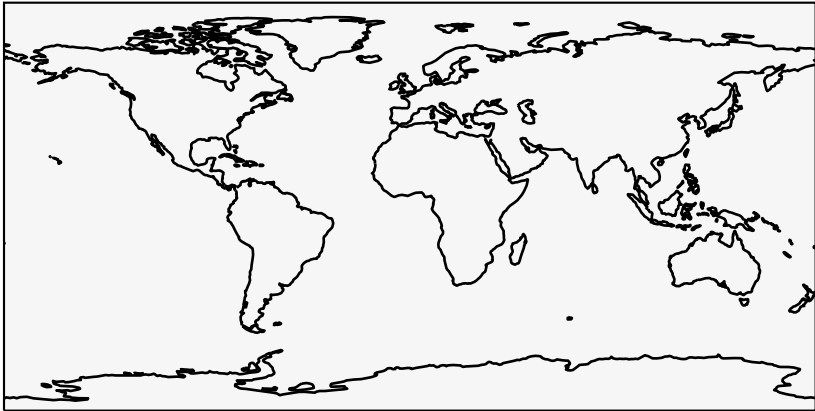
GCC\_dev (Ref)  
4.0x5.0



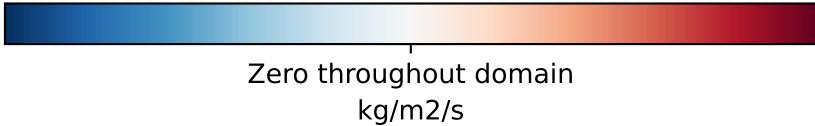
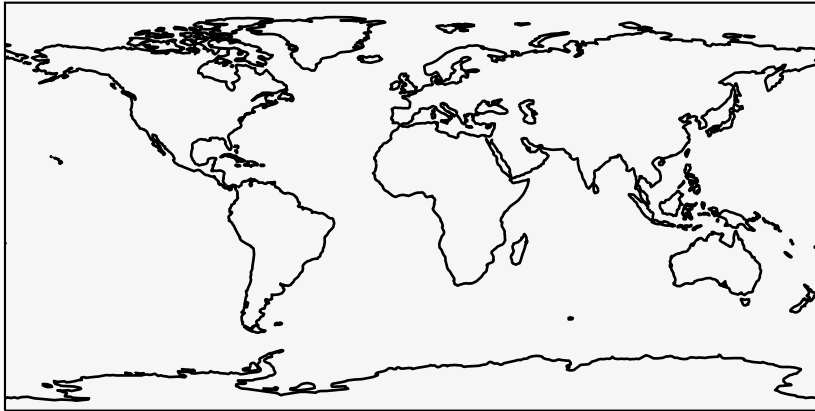
GCHP\_dev (Dev)  
c24



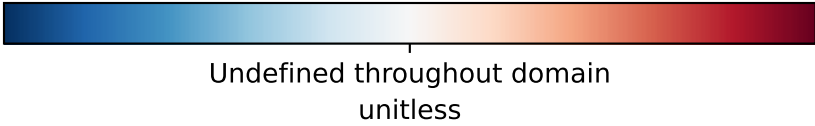
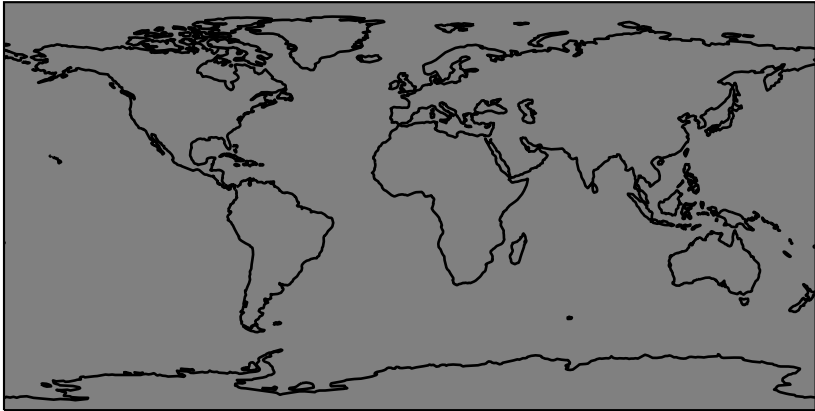
Difference (1x1.25)  
Dev - Ref, Dynamic Range



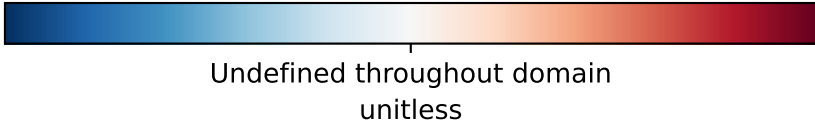
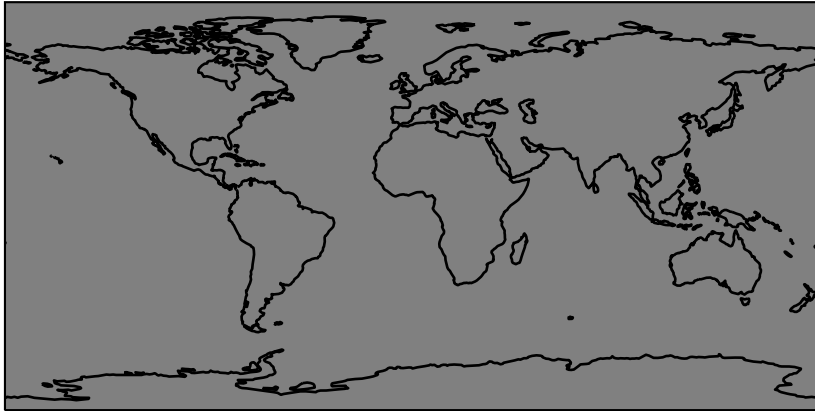
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

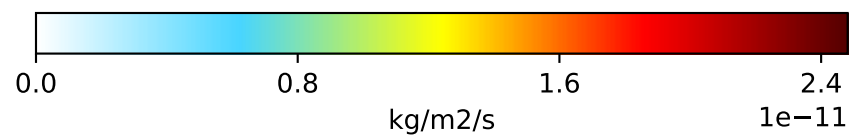
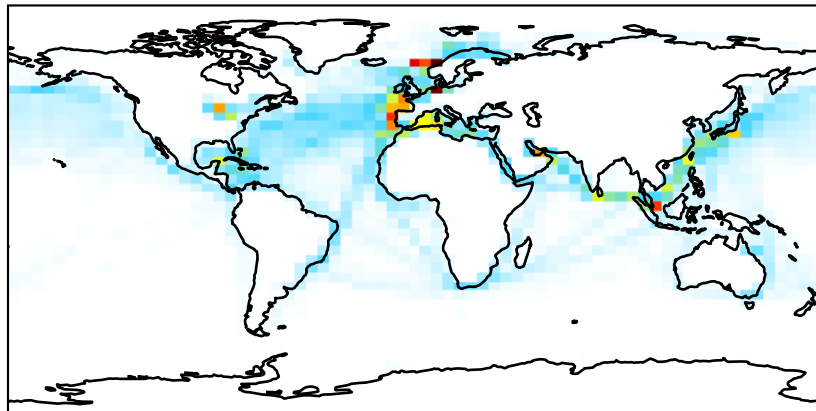


Ratio (1x1.25)  
Dev/Ref, Fixed Range

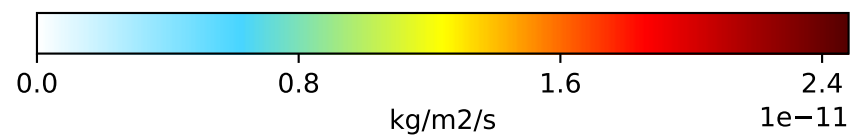
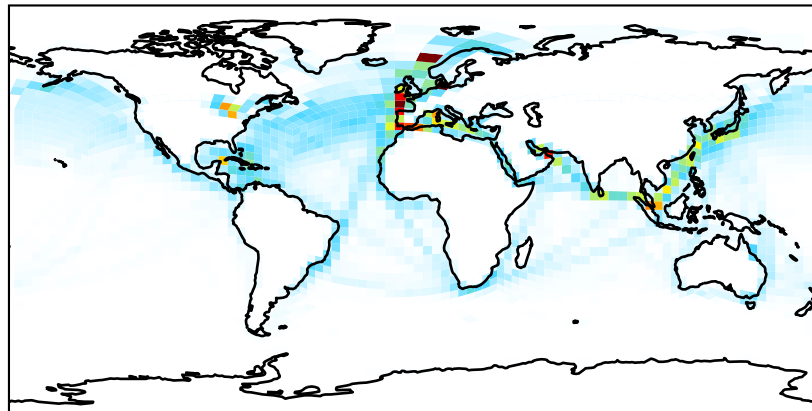


# EmisHNO3\_Ship

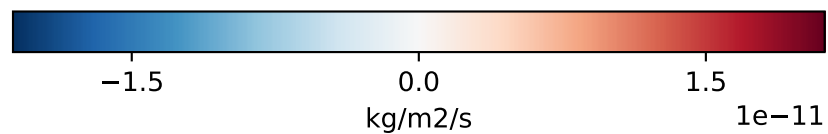
GCC\_dev (Ref)  
4.0x5.0



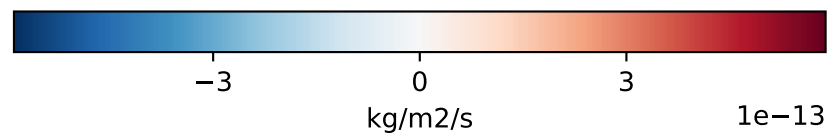
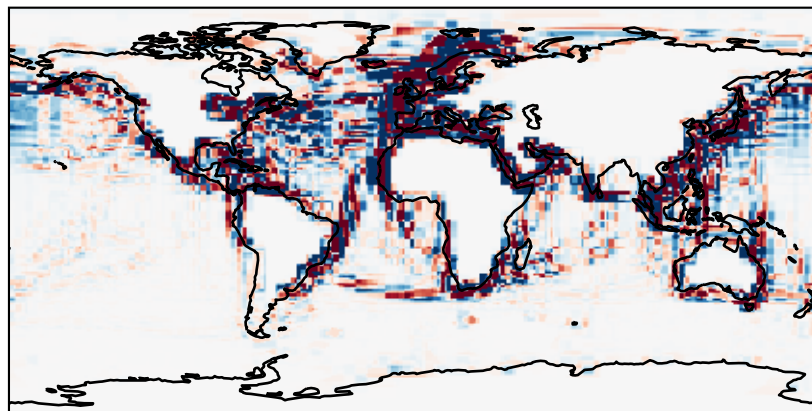
GCHP\_dev (Dev)  
c24



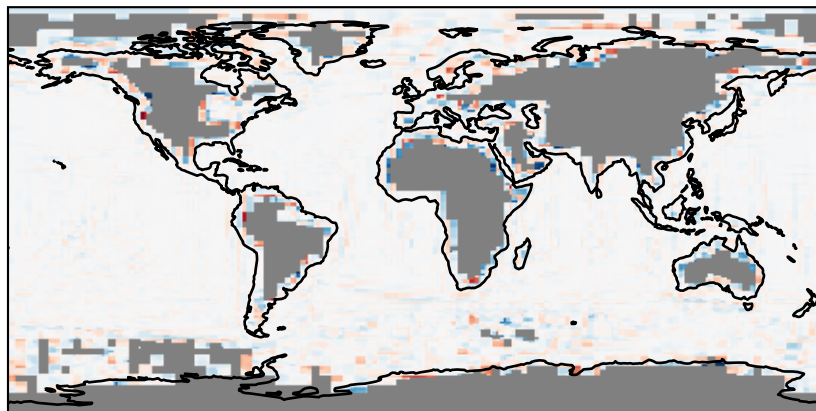
Difference (1x1.25)  
Dev - Ref, Dynamic Range



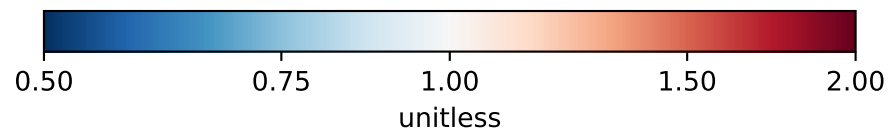
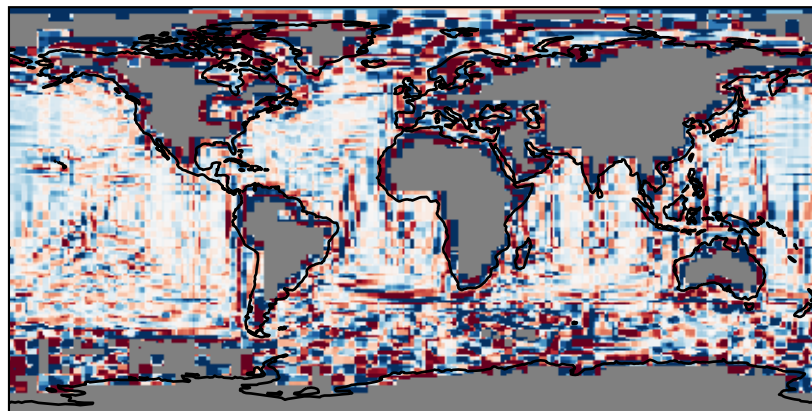
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

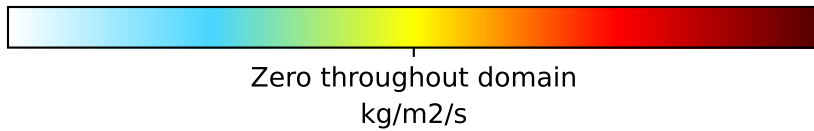
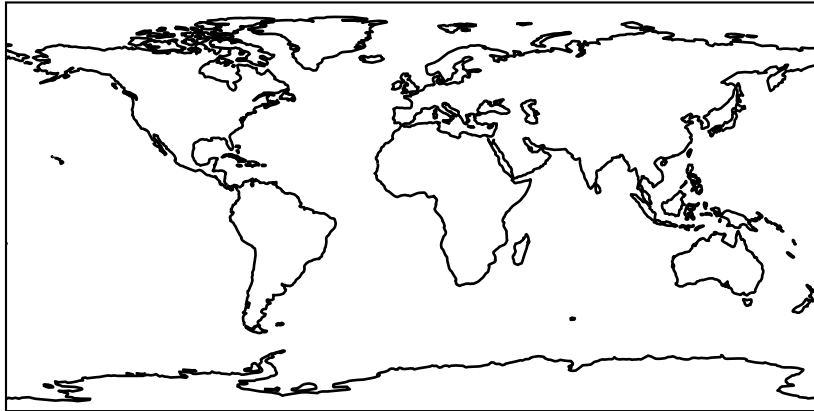


Ratio (1x1.25)  
Dev/Ref, Fixed Range

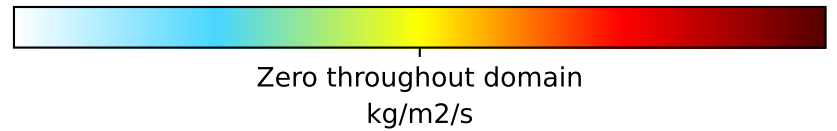
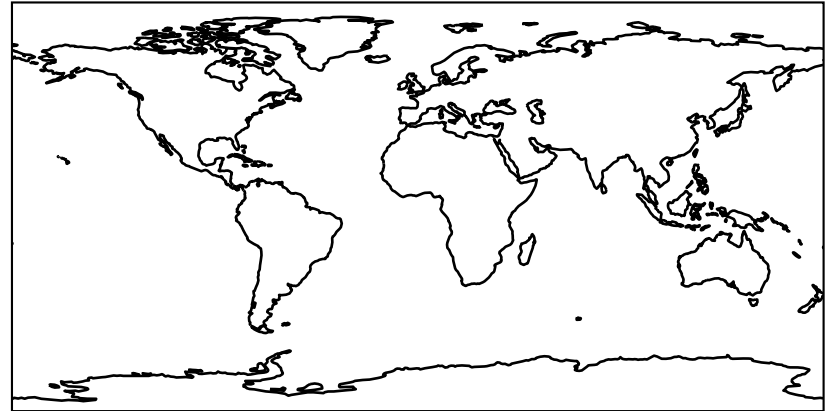


# EmisMEK\_Ship

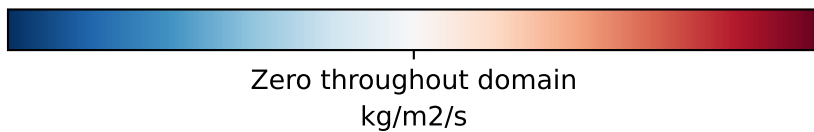
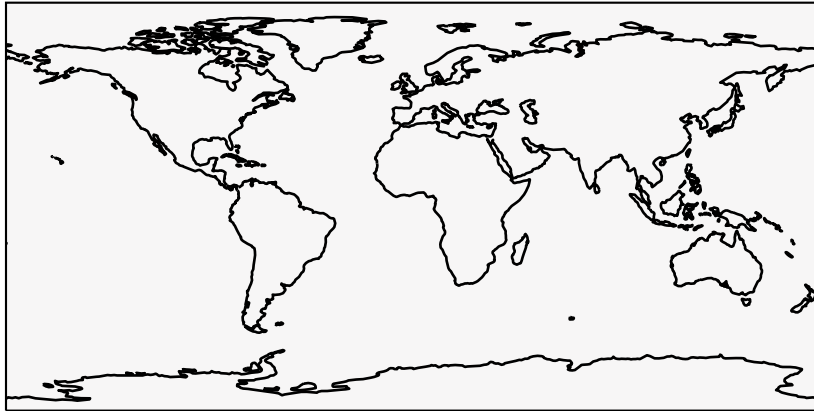
GCC\_dev (Ref)  
4.0x5.0



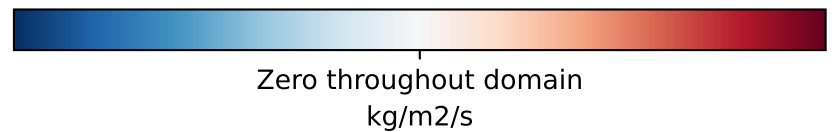
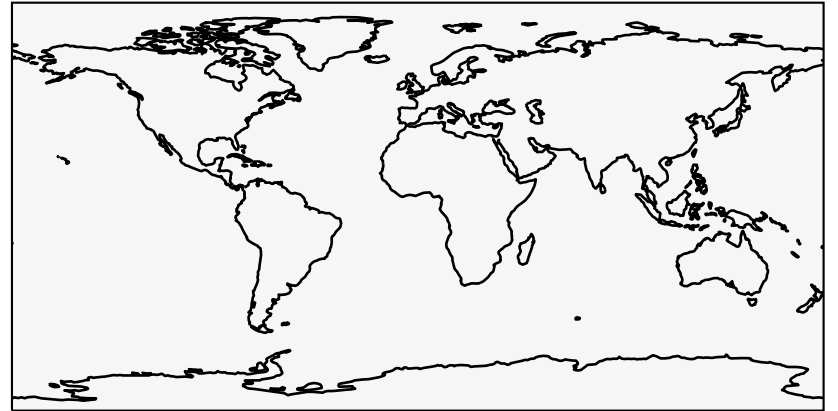
GCHP\_dev (Dev)  
c24



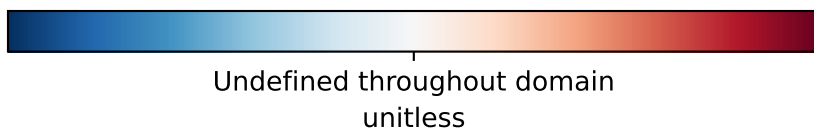
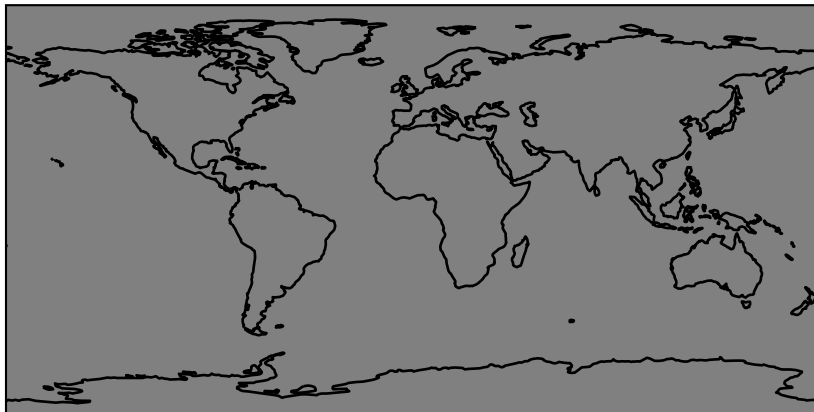
Difference (1x1.25)  
Dev - Ref, Dynamic Range



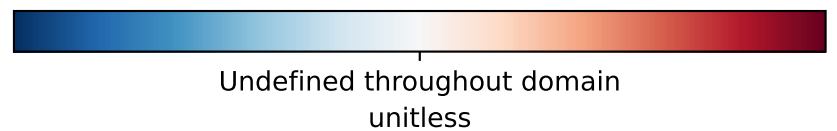
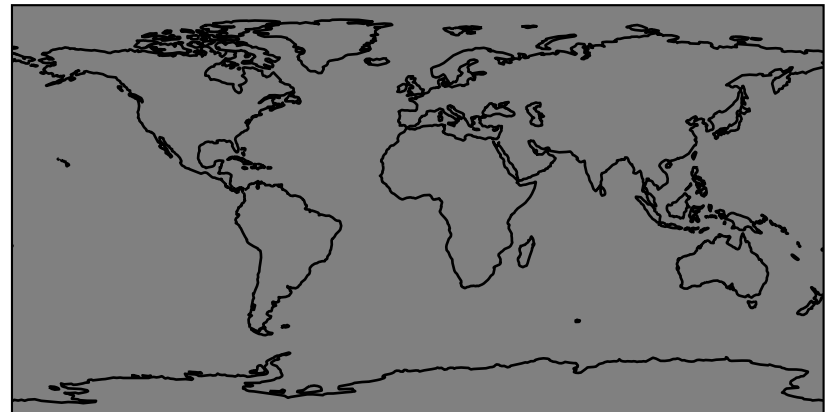
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

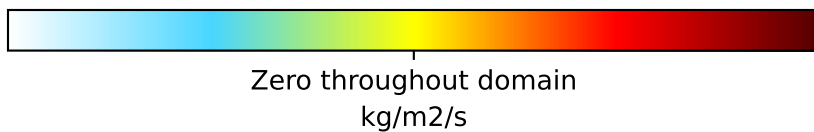


Ratio (1x1.25)  
Dev/Ref, Fixed Range

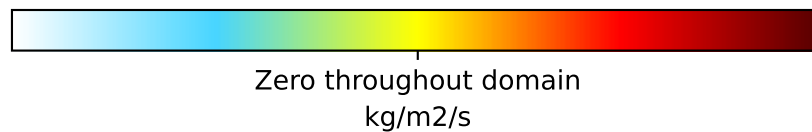
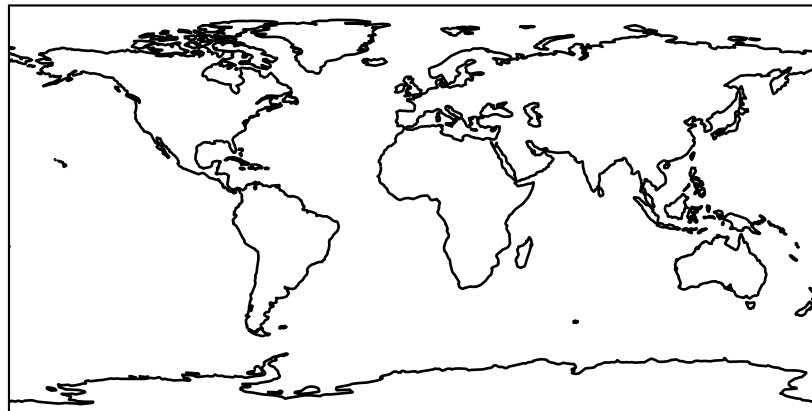


# EmisMOH\_Ship

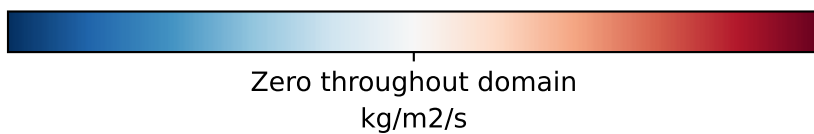
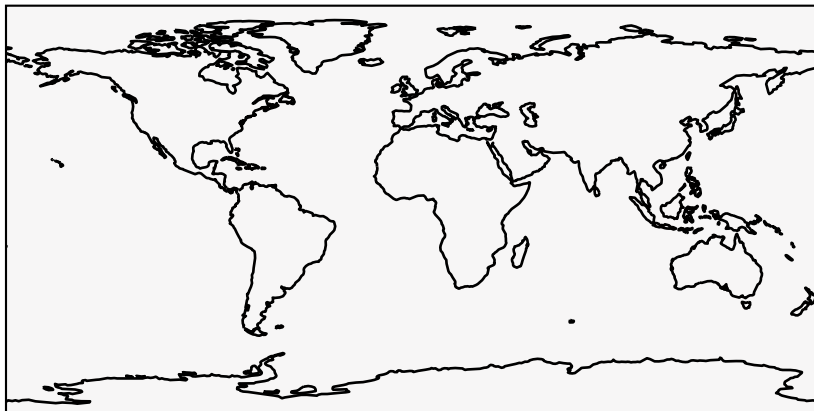
GCC\_dev (Ref)  
4.0x5.0



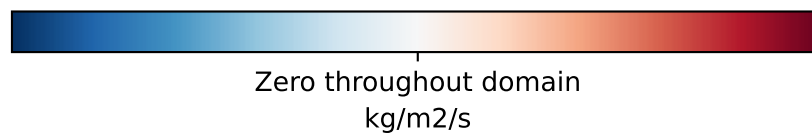
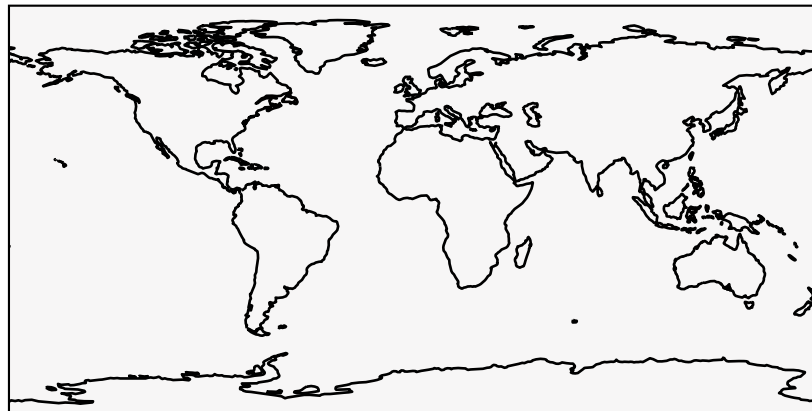
GCHP\_dev (Dev)  
c24



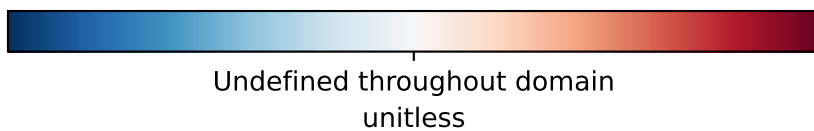
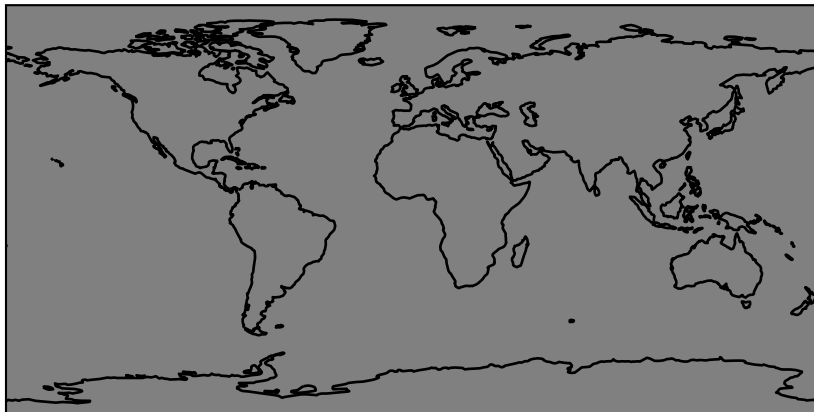
Difference (1x1.25)  
Dev - Ref, Dynamic Range



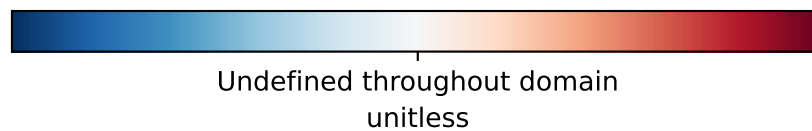
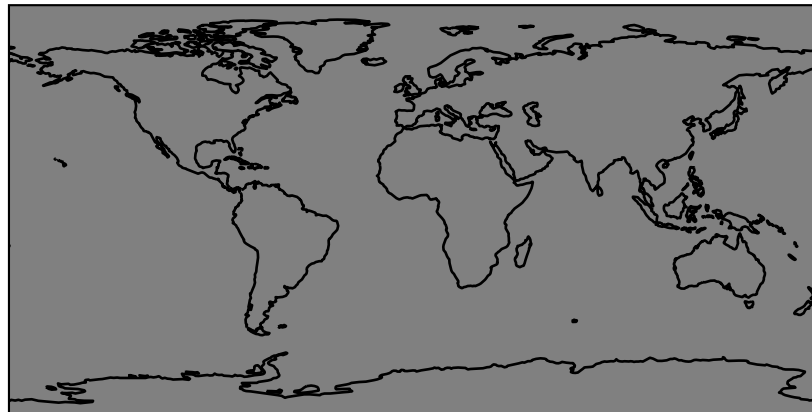
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

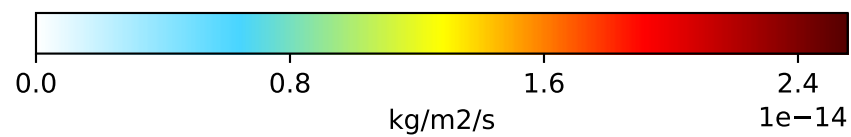
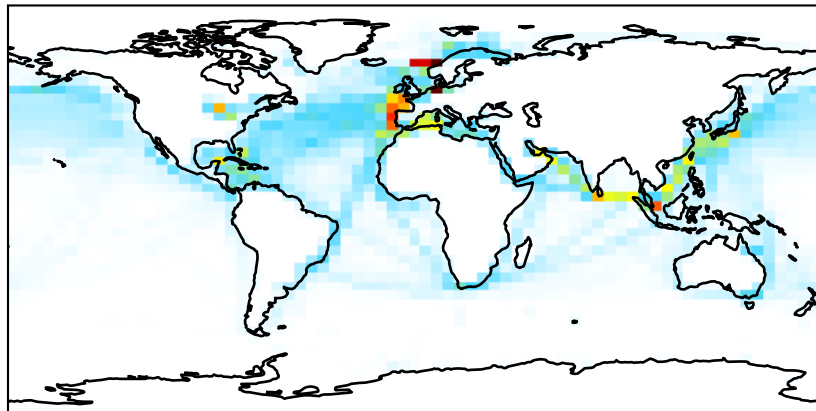


Ratio (1x1.25)  
Dev/Ref, Fixed Range

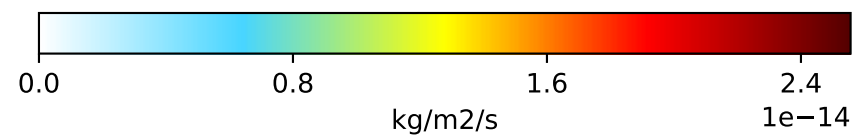
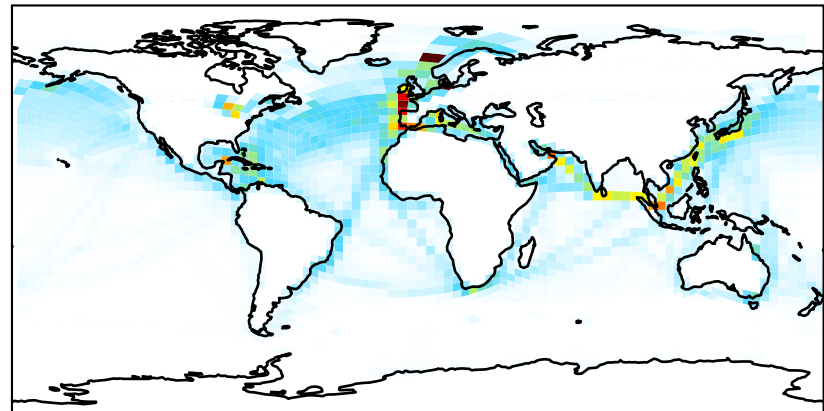


# EmisNH3\_Ship

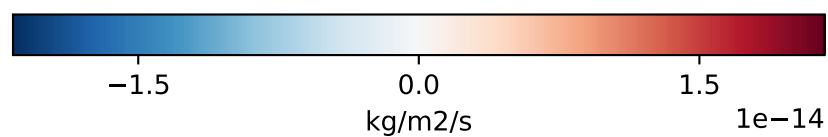
GCC\_dev (Ref)  
4.0x5.0



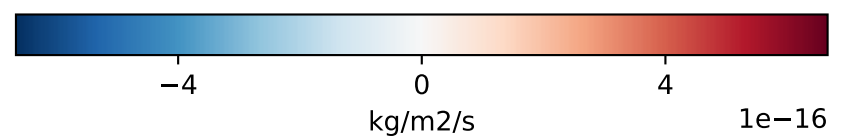
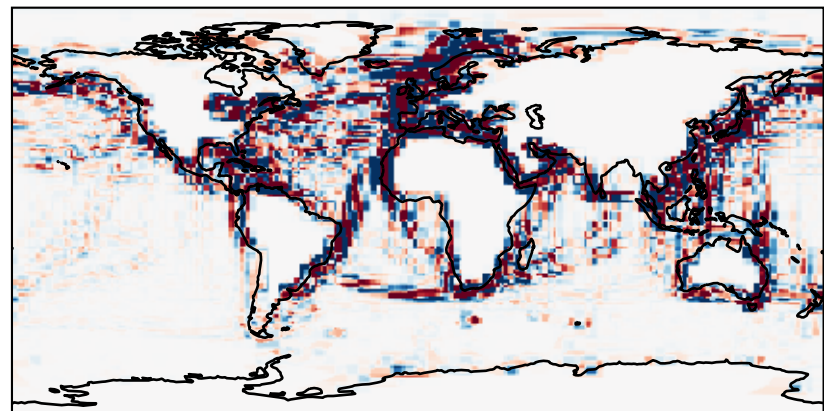
GCHP\_dev (Dev)  
c24



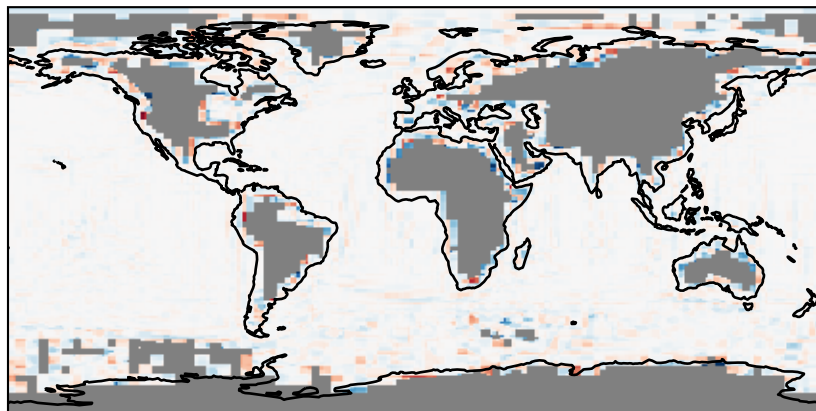
Difference (1x1.25)  
Dev - Ref, Dynamic Range



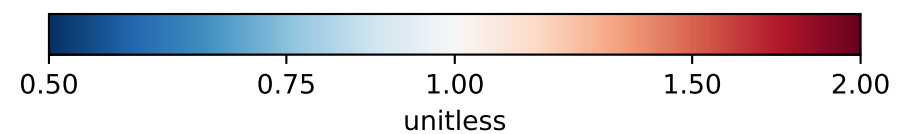
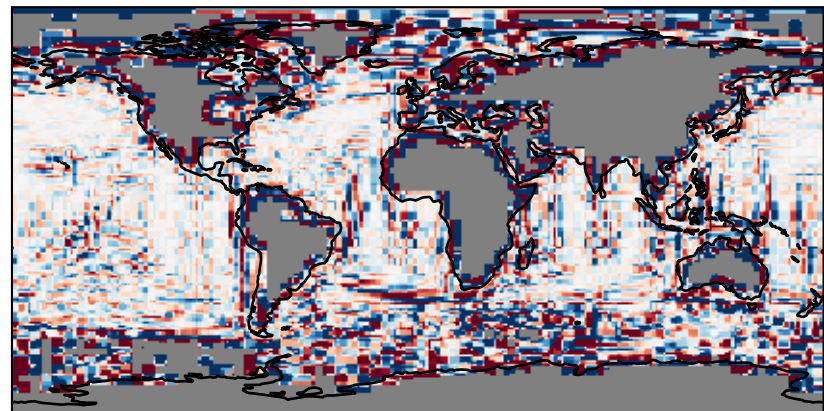
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

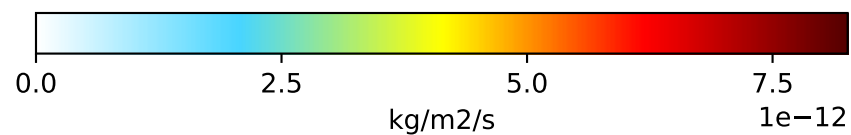
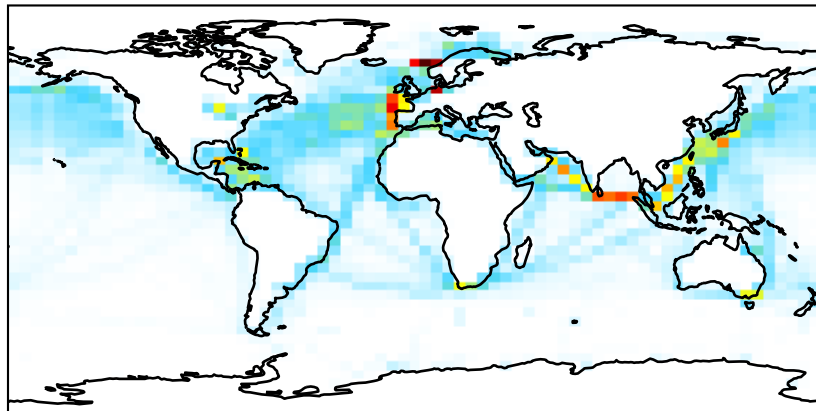


Ratio (1x1.25)  
Dev/Ref, Fixed Range

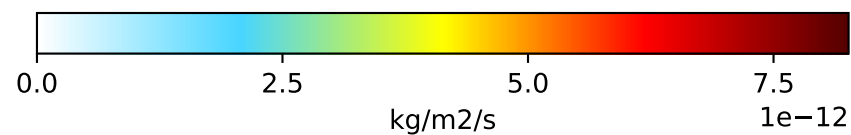
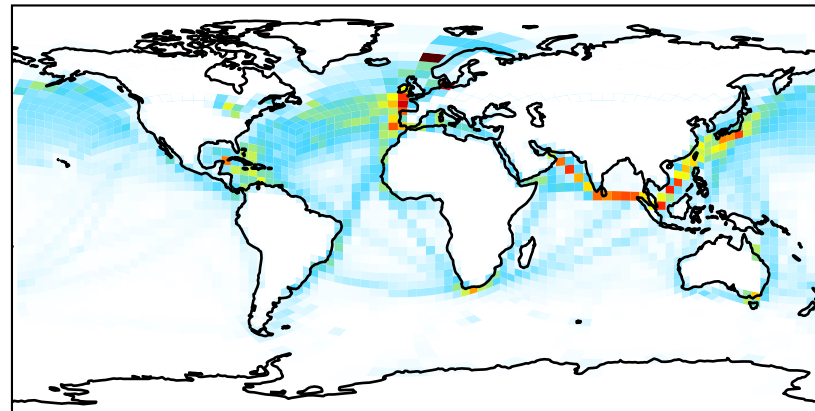


# EmisNO2\_Ship

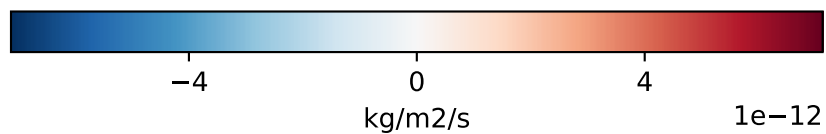
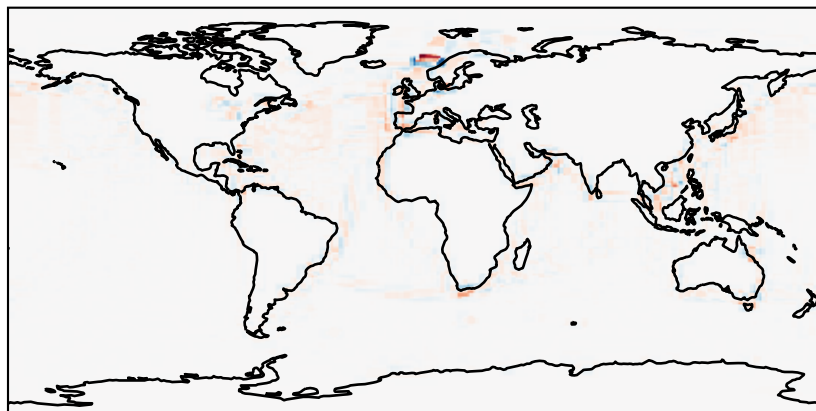
GCC\_dev (Ref)  
4.0x5.0



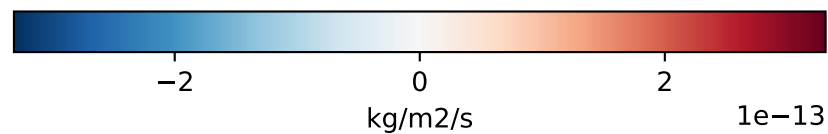
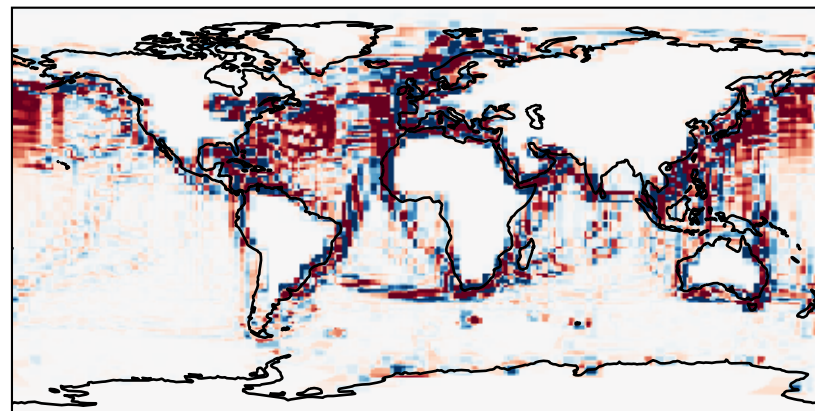
GCHP\_dev (Dev)  
c24



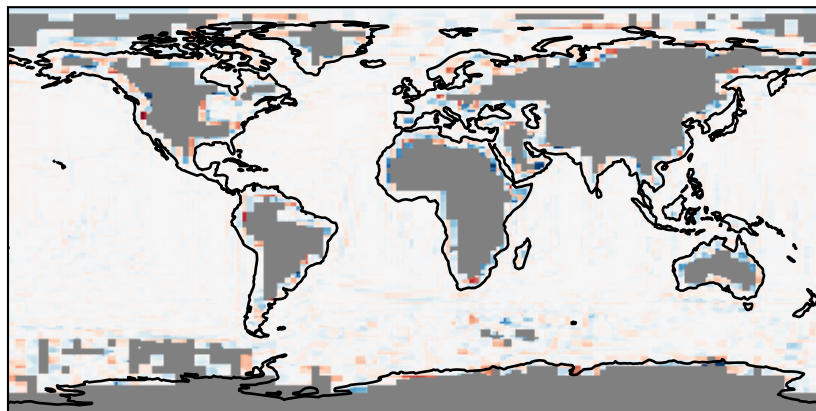
Difference (1x1.25)  
Dev - Ref, Dynamic Range



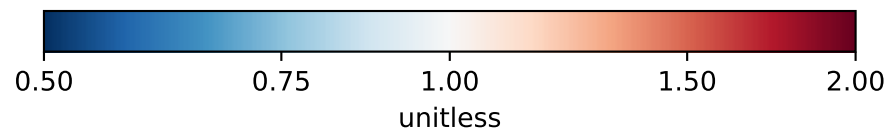
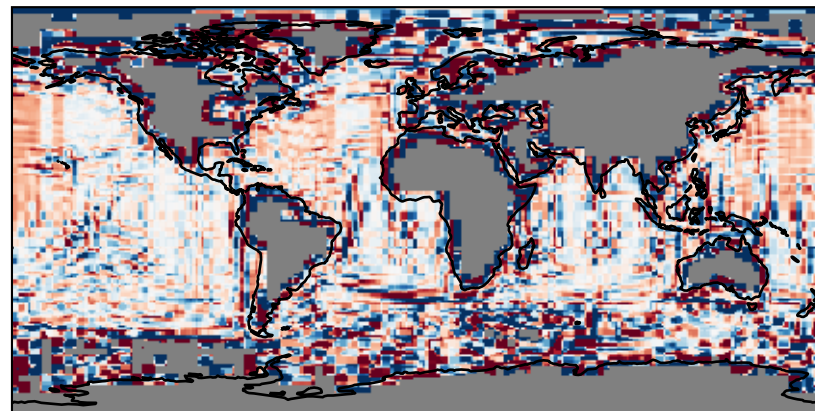
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

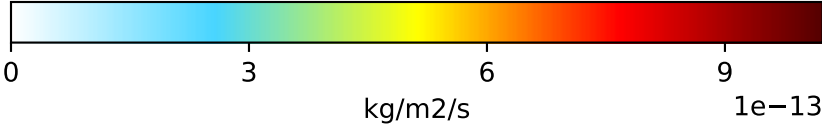
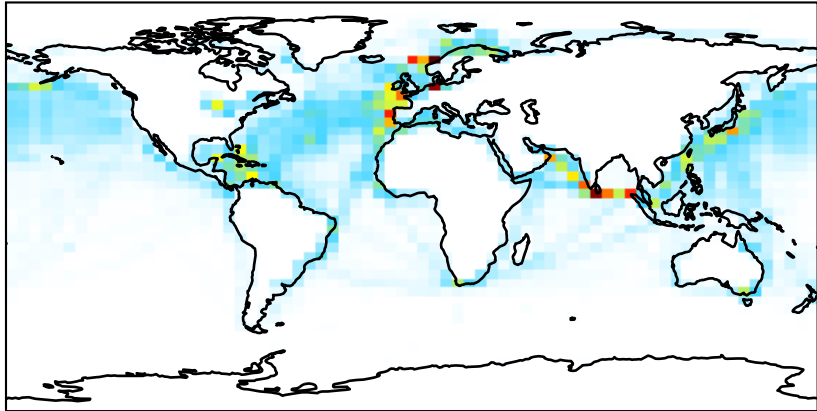


Ratio (1x1.25)  
Dev/Ref, Fixed Range

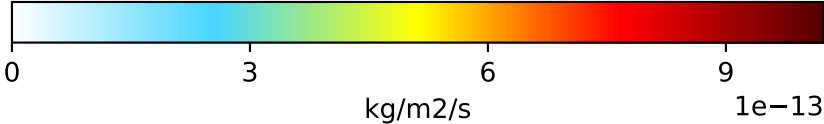
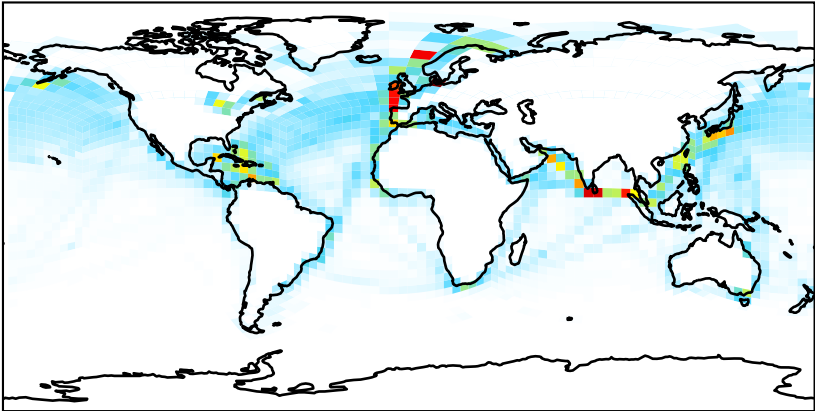


# EmisNO\_Ship

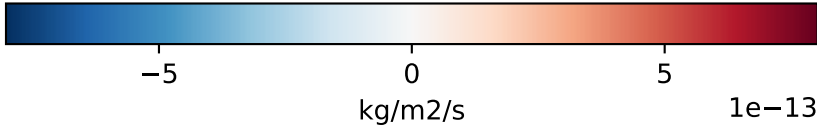
GCC\_dev (Ref)  
4.0x5.0



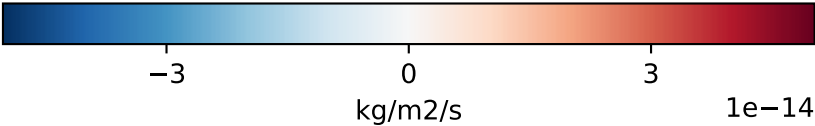
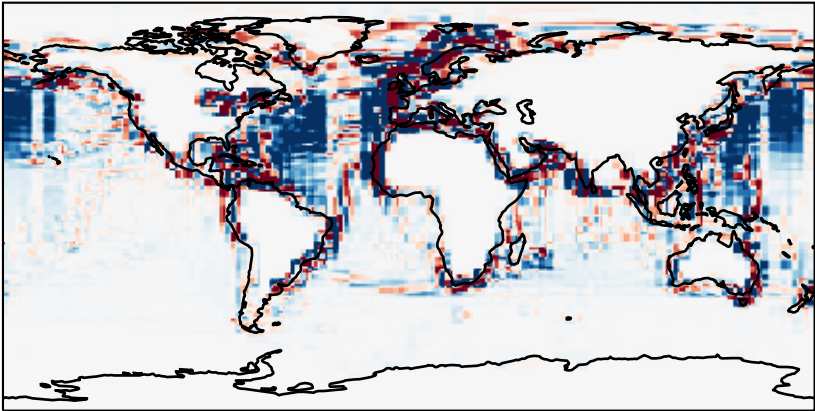
GCHP\_dev (Dev)  
c24



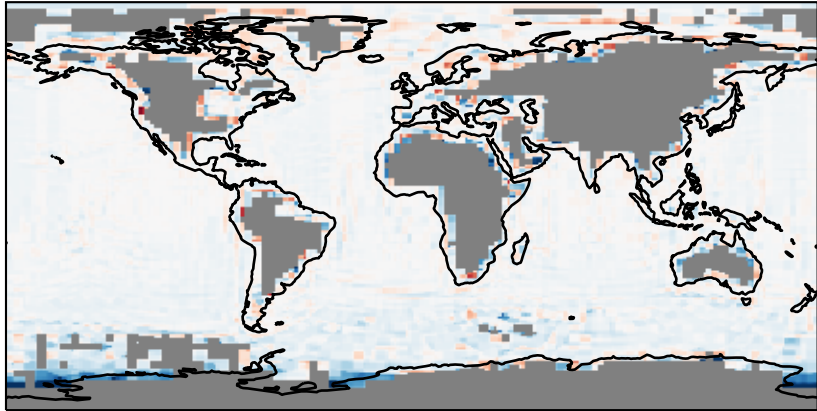
Difference (1x1.25)  
Dev - Ref, Dynamic Range



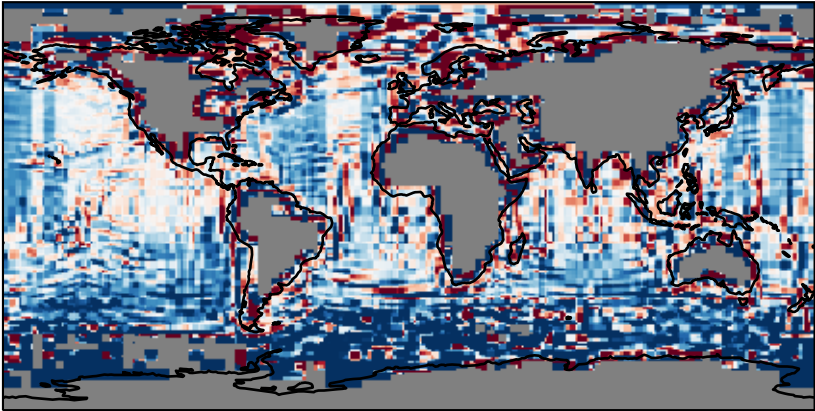
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

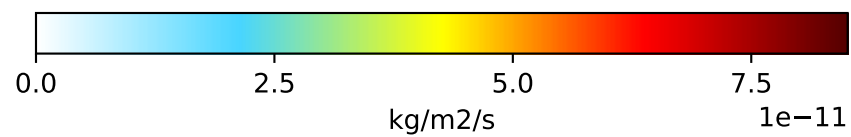
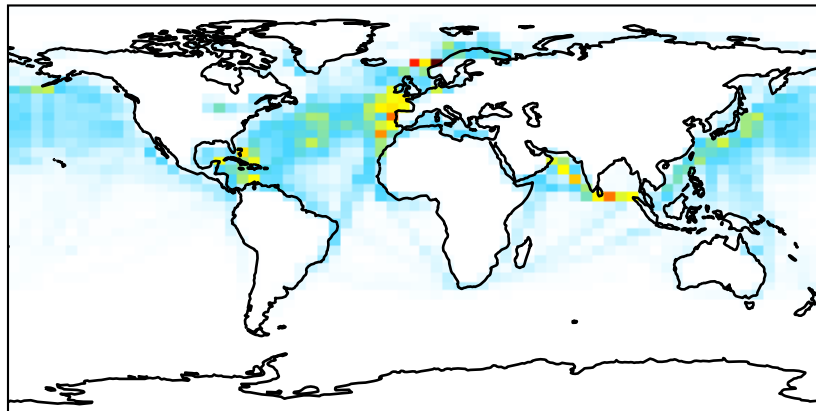


Ratio (1x1.25)  
Dev/Ref, Fixed Range

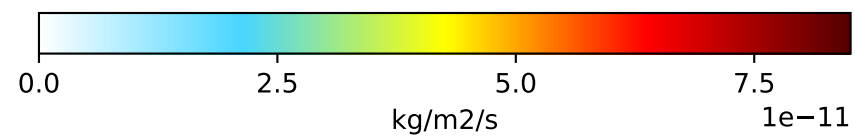
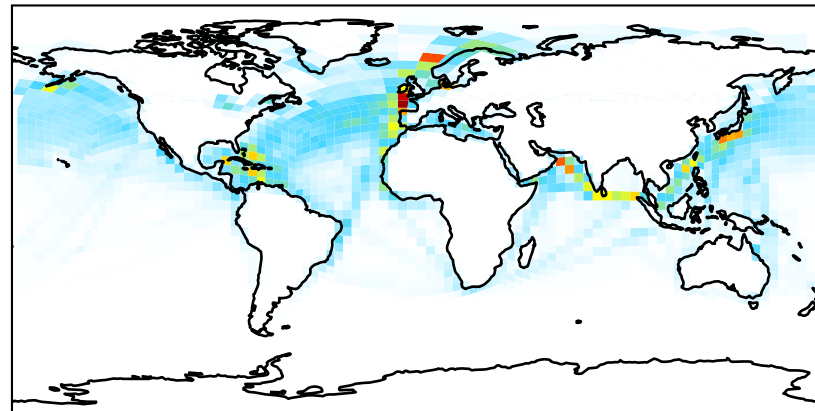


# Emis03\_Ship

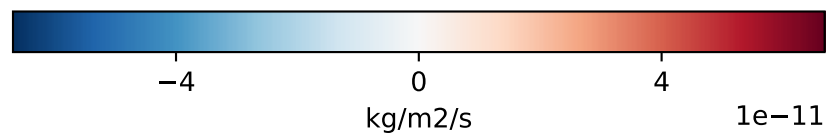
GCC\_dev (Ref)  
4.0x5.0



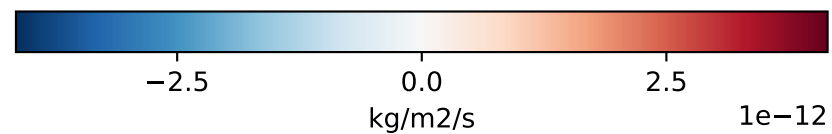
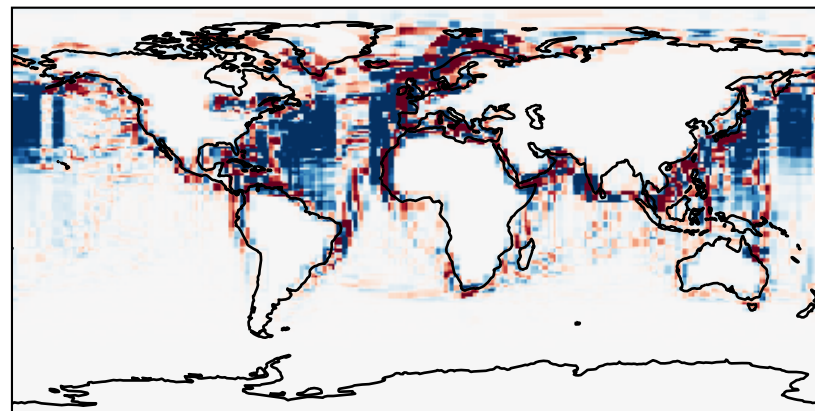
GCHP\_dev (Dev)  
c24



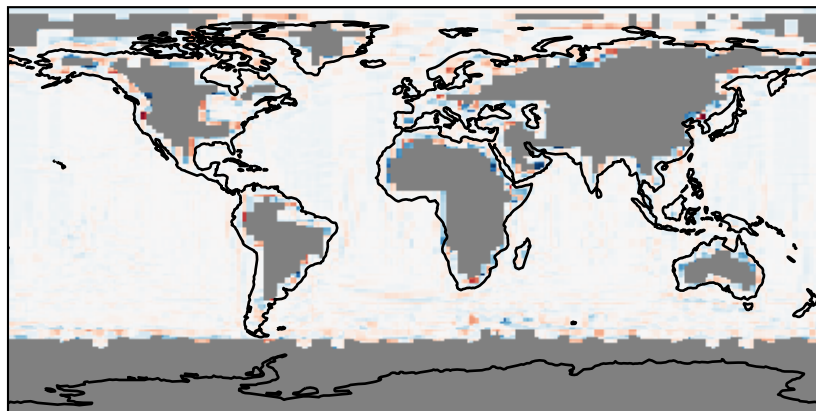
Difference (1x1.25)  
Dev - Ref, Dynamic Range



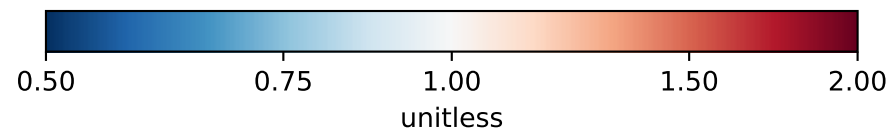
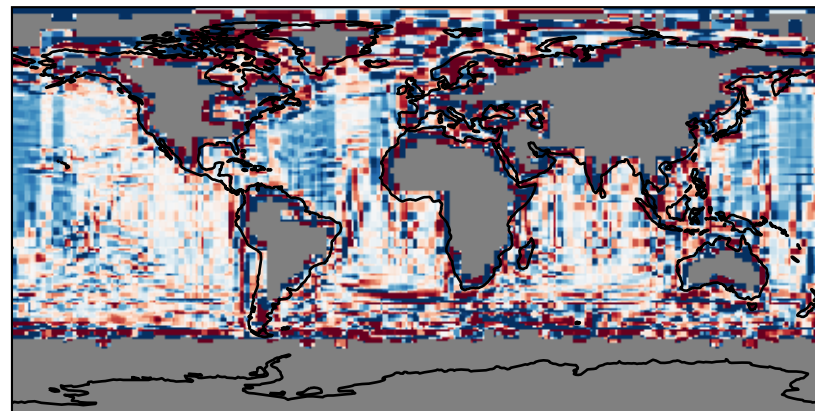
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

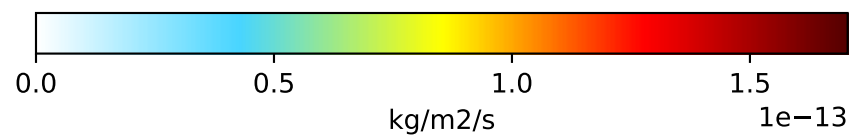
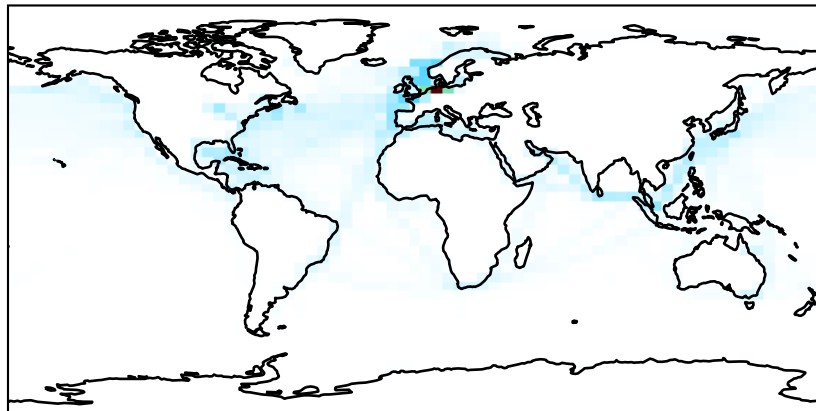


Ratio (1x1.25)  
Dev/Ref, Fixed Range

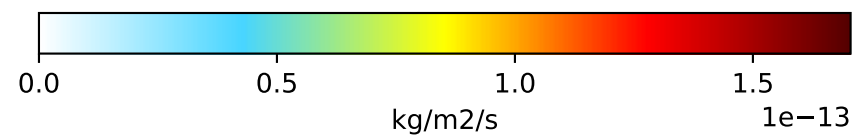
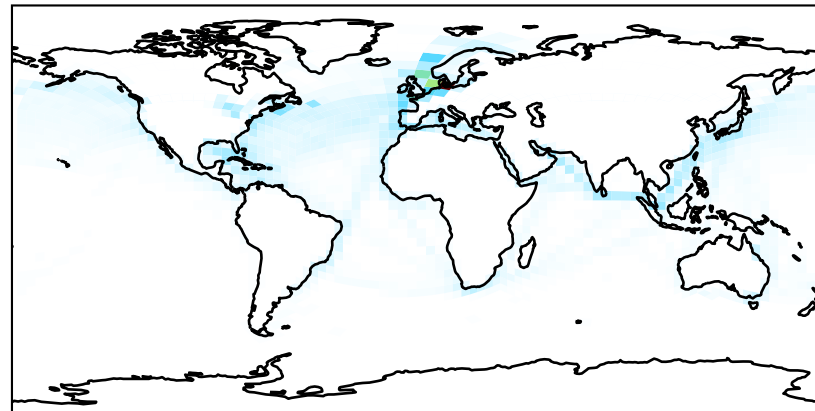


# EmisOCPI\_Ship

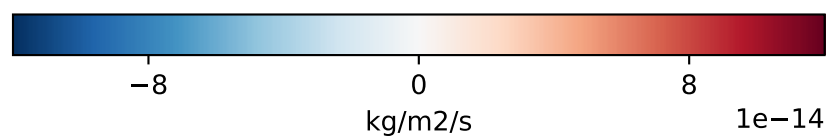
GCC\_dev (Ref)  
4.0x5.0



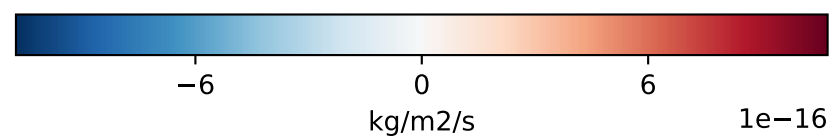
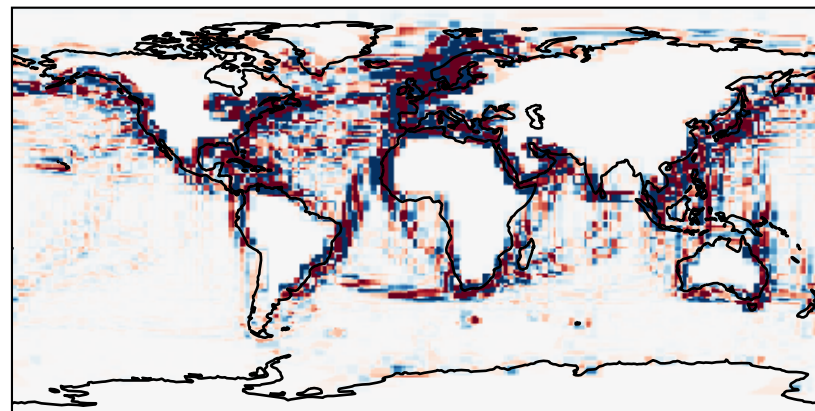
GCHP\_dev (Dev)  
c24



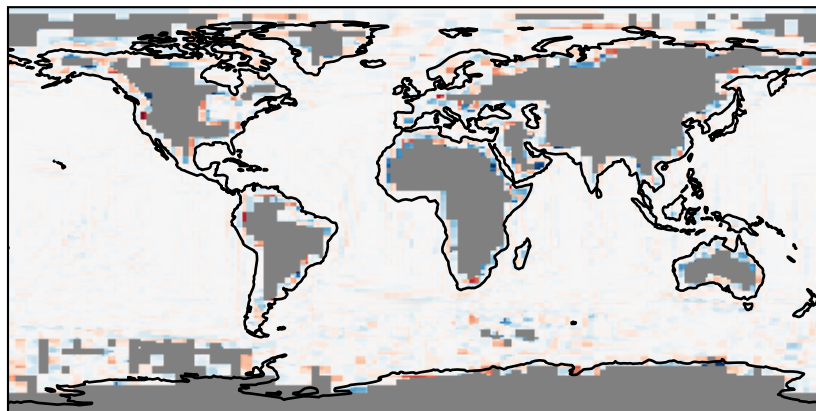
Difference (1x1.25)  
Dev - Ref, Dynamic Range



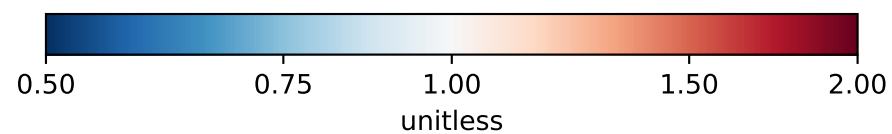
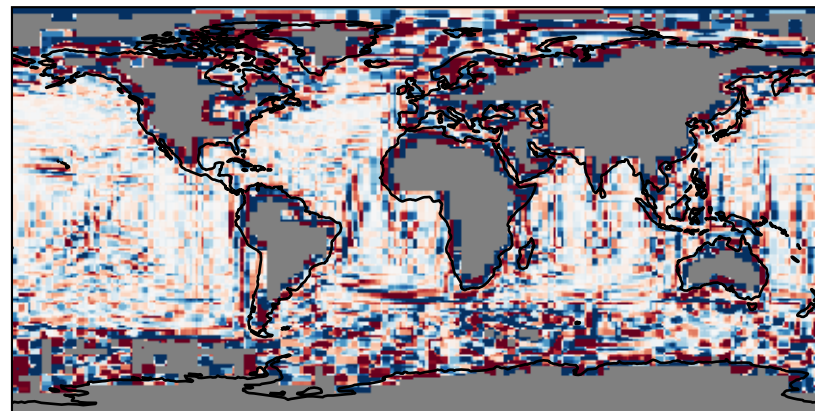
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

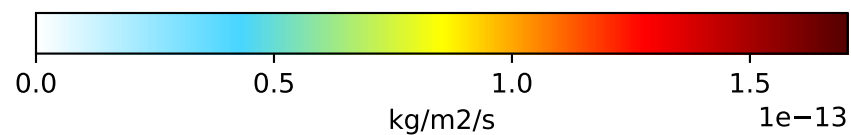
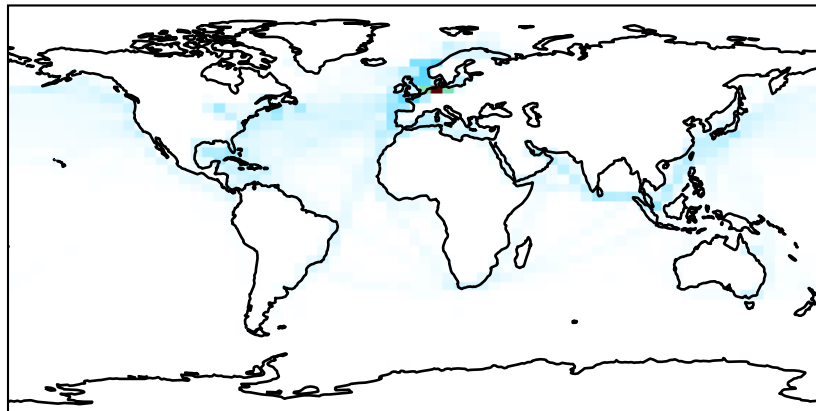


Ratio (1x1.25)  
Dev/Ref, Fixed Range

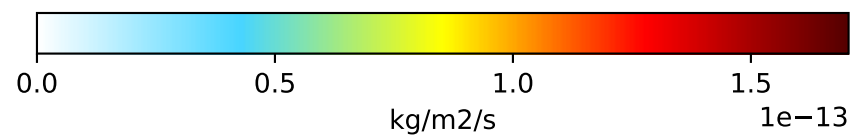
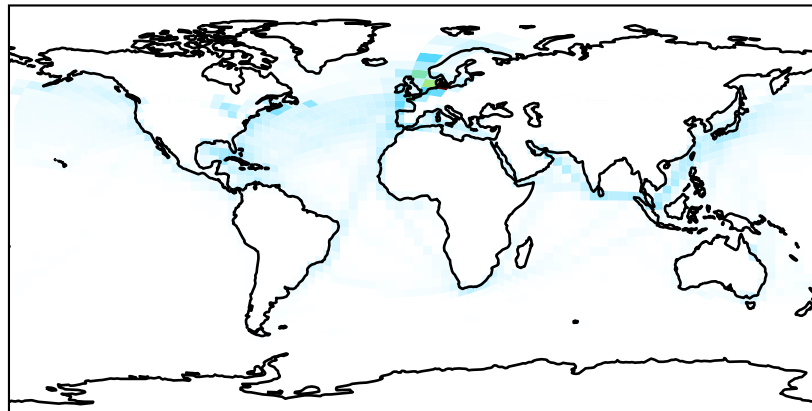


# EmisOCPO\_Ship

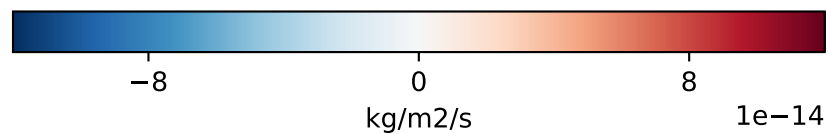
GCC\_dev (Ref)  
4.0x5.0



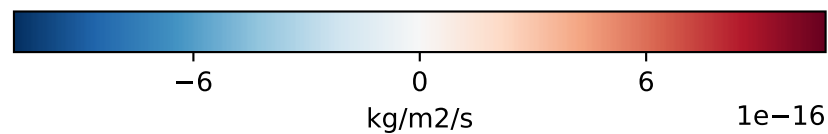
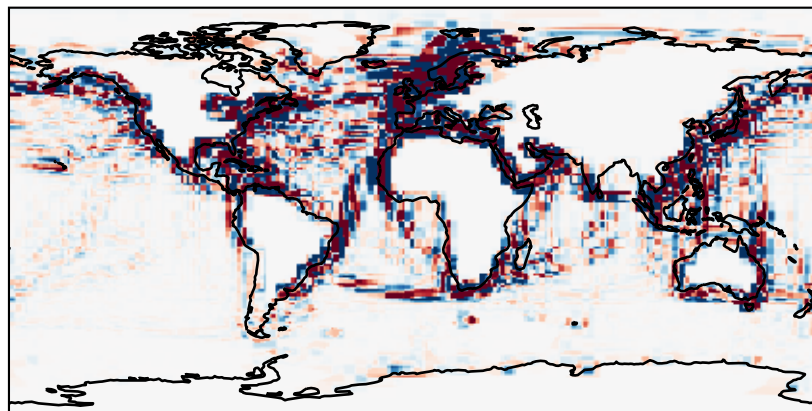
GCHP\_dev (Dev)  
c24



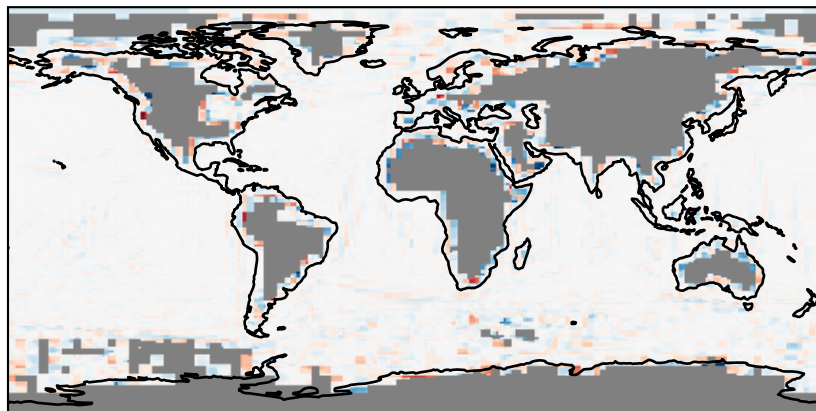
Difference (1x1.25)  
Dev - Ref, Dynamic Range



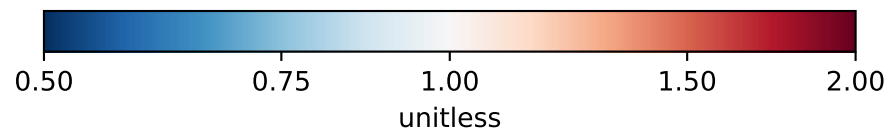
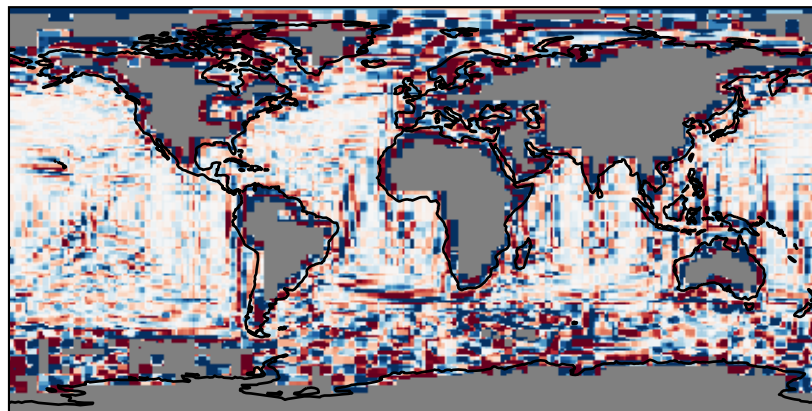
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

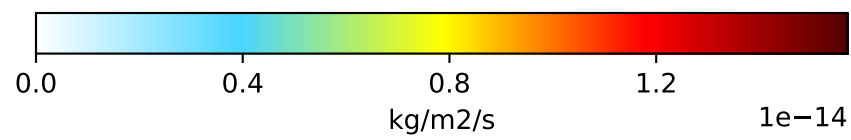
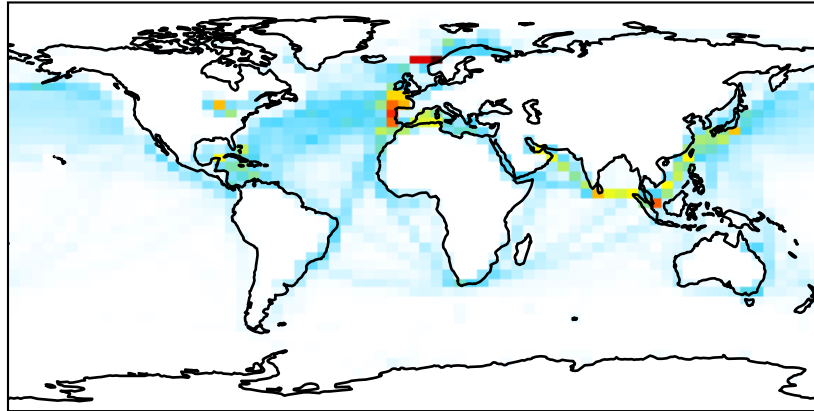


Ratio (1x1.25)  
Dev/Ref, Fixed Range

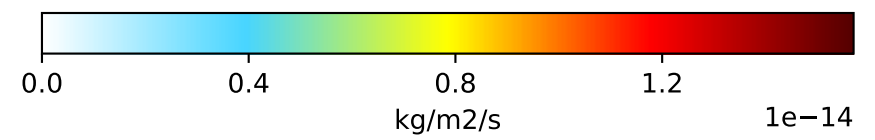
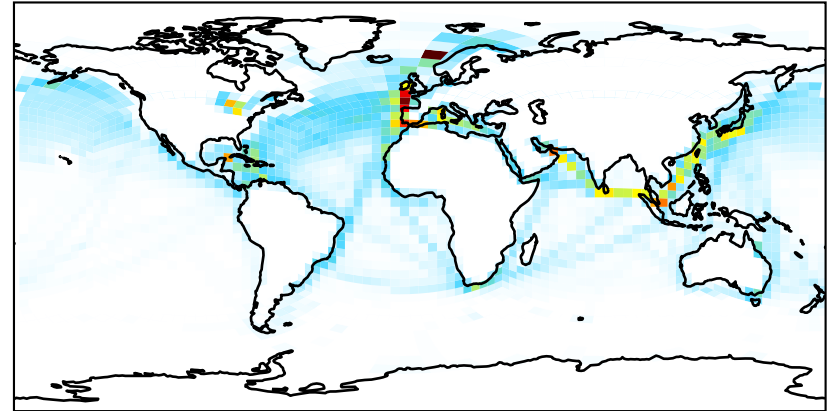


# EmispFe\_Ship

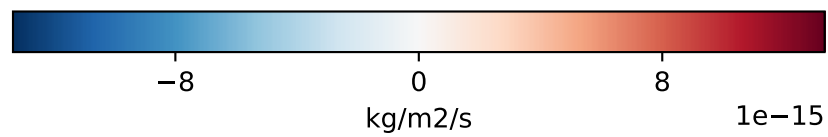
GCC\_dev (Ref)  
4.0x5.0



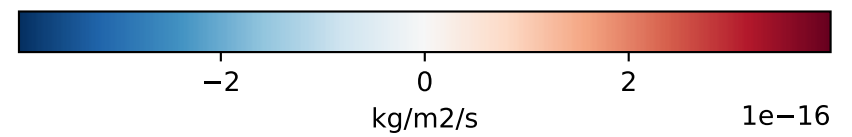
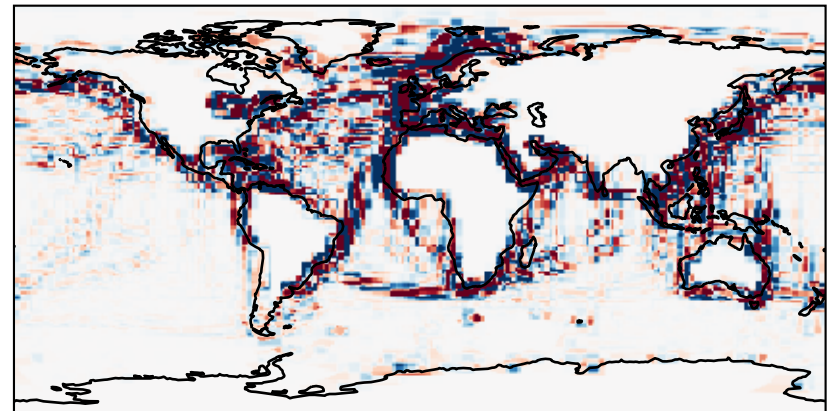
GCHP\_dev (Dev)  
c24



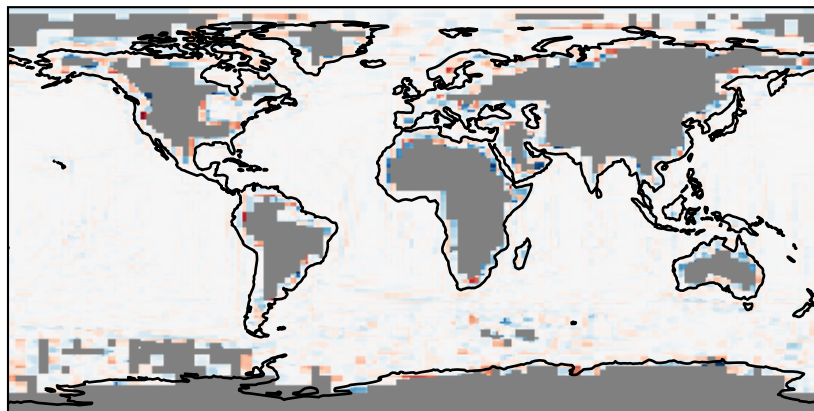
Difference (1x1.25)  
Dev - Ref, Dynamic Range



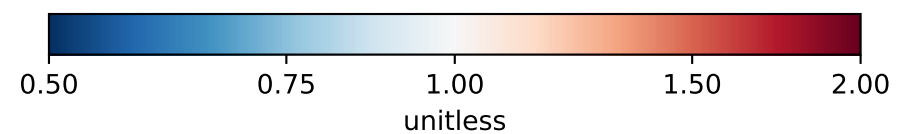
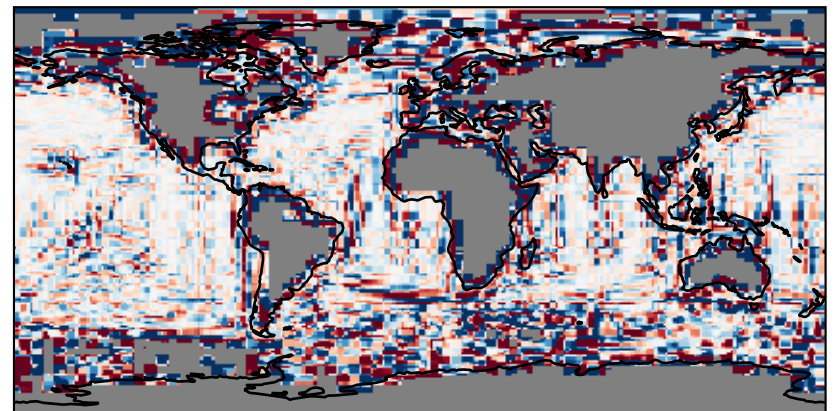
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

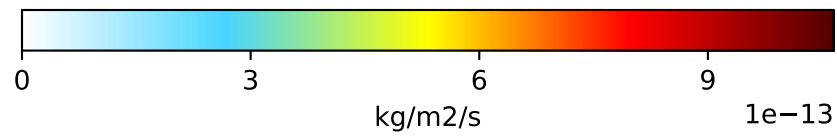
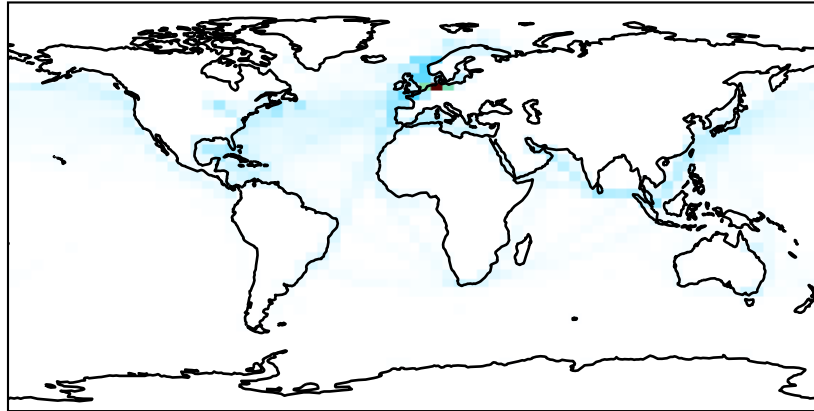


Ratio (1x1.25)  
Dev/Ref, Fixed Range

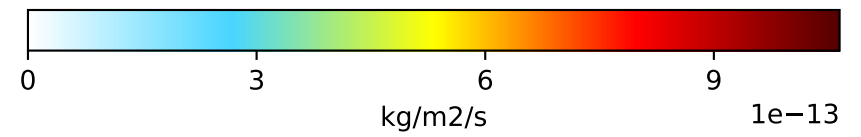
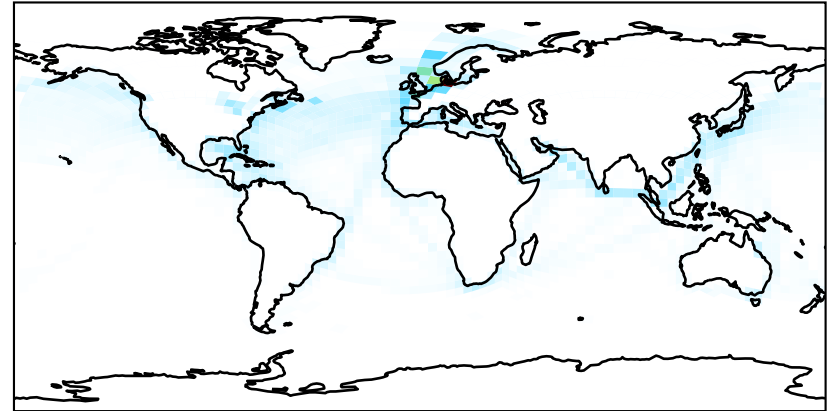


# EmisPRPE\_Ship

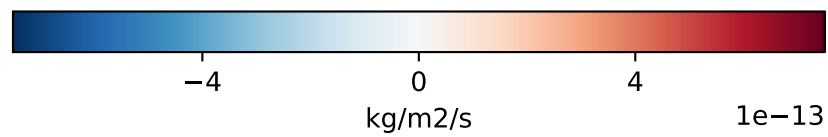
GCC\_dev (Ref)  
4.0x5.0



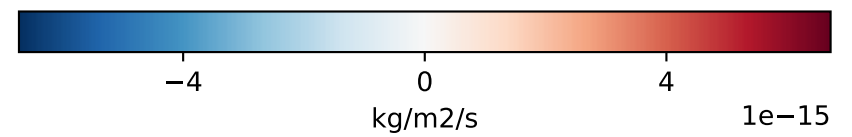
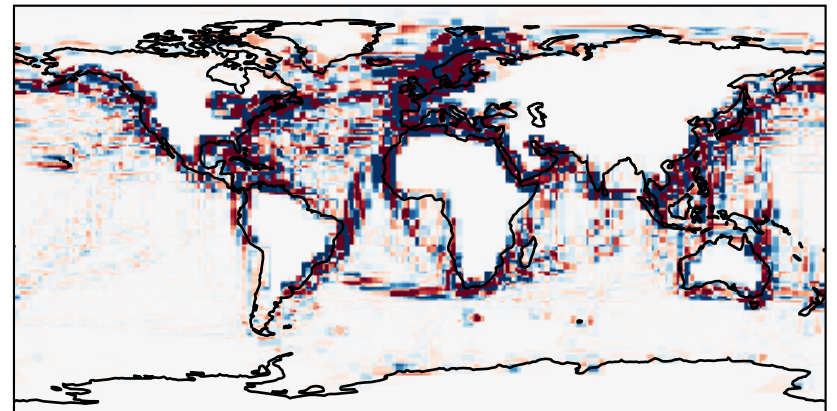
GCHP\_dev (Dev)  
c24



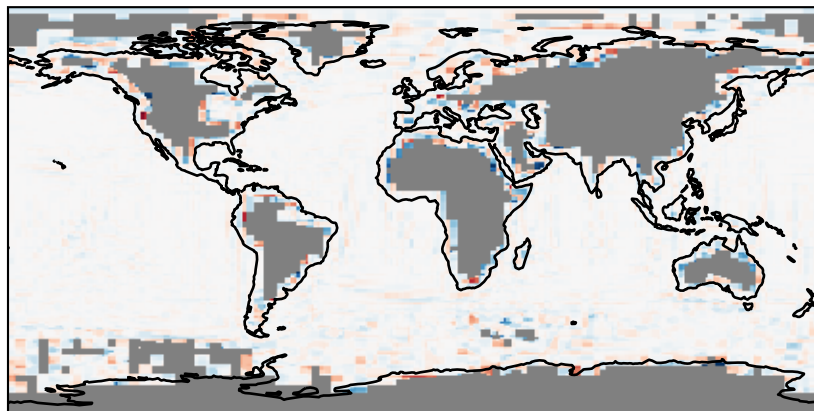
Difference (1x1.25)  
Dev - Ref, Dynamic Range



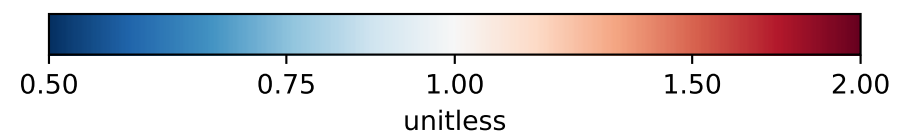
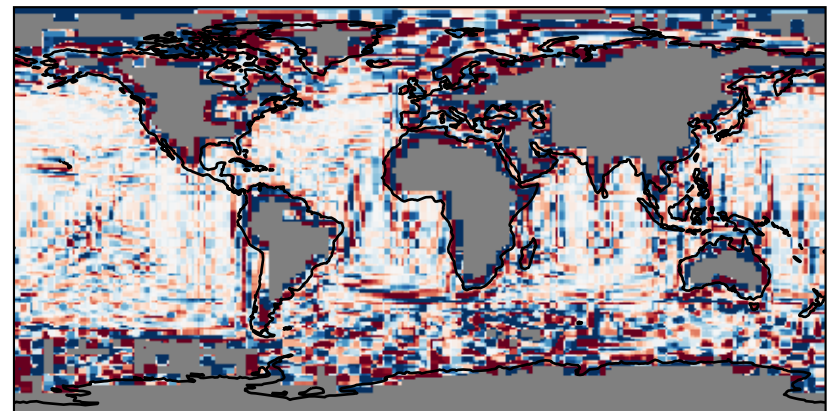
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

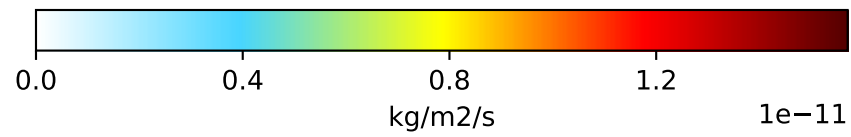
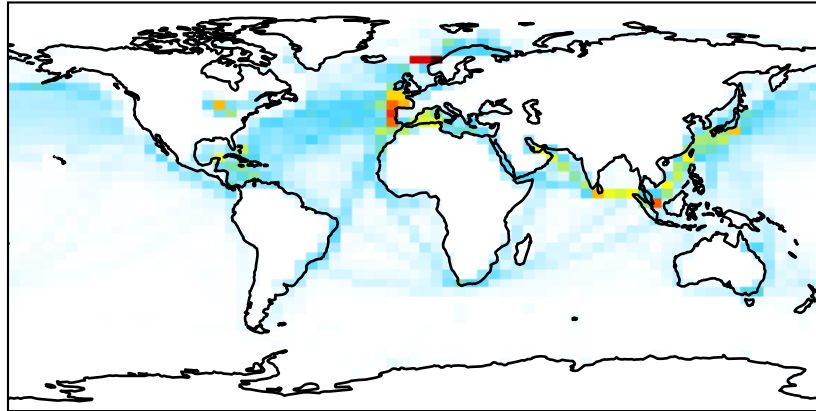


Ratio (1x1.25)  
Dev/Ref, Fixed Range

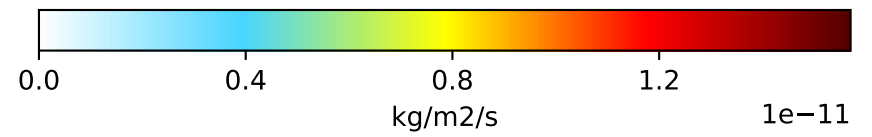
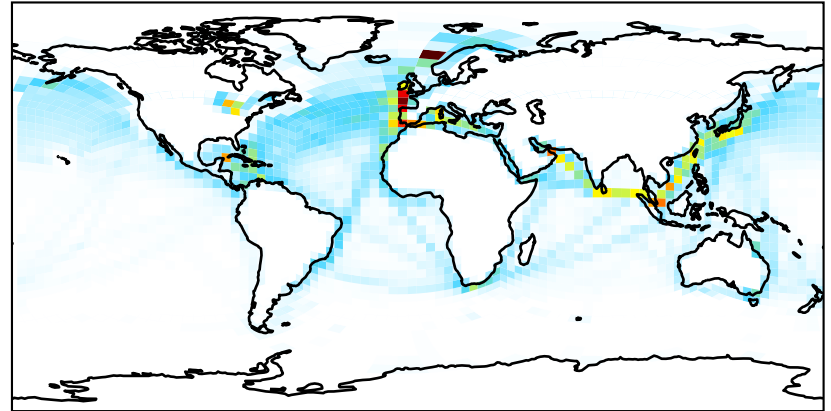


# EmisSO2\_Ship

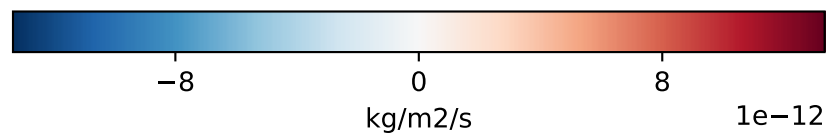
GCC\_dev (Ref)  
4.0x5.0



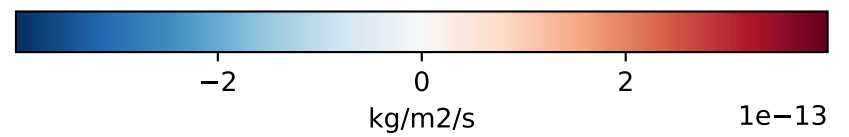
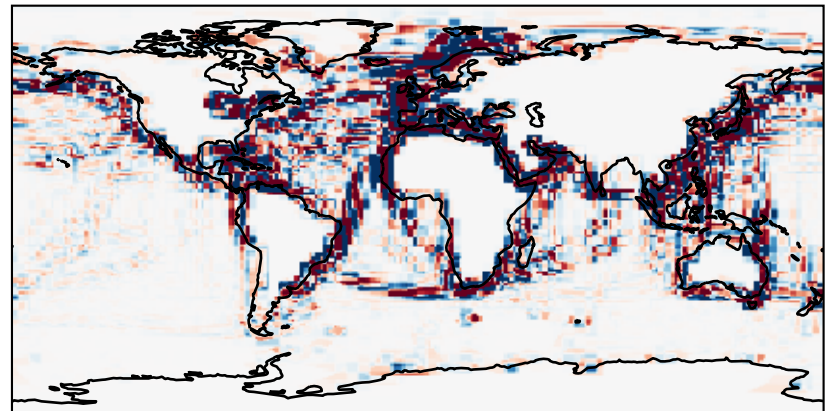
GCHP\_dev (Dev)  
c24



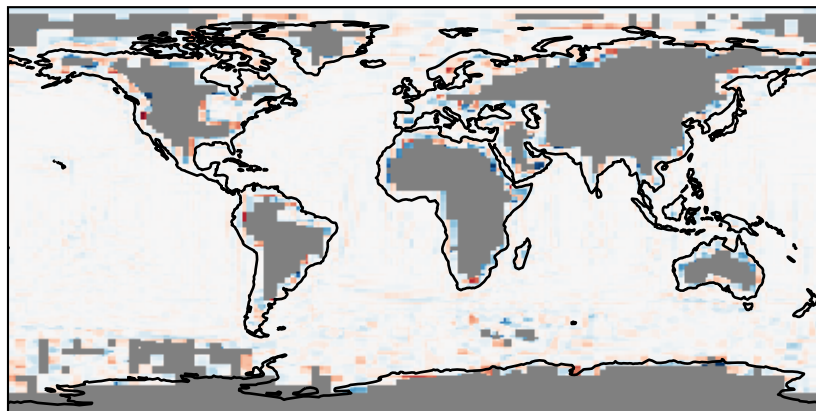
Difference (1x1.25)  
Dev - Ref, Dynamic Range



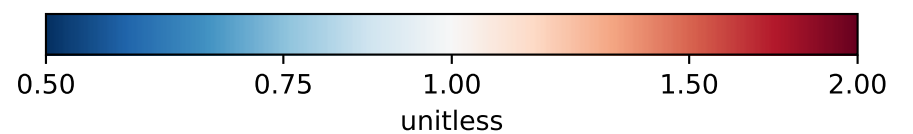
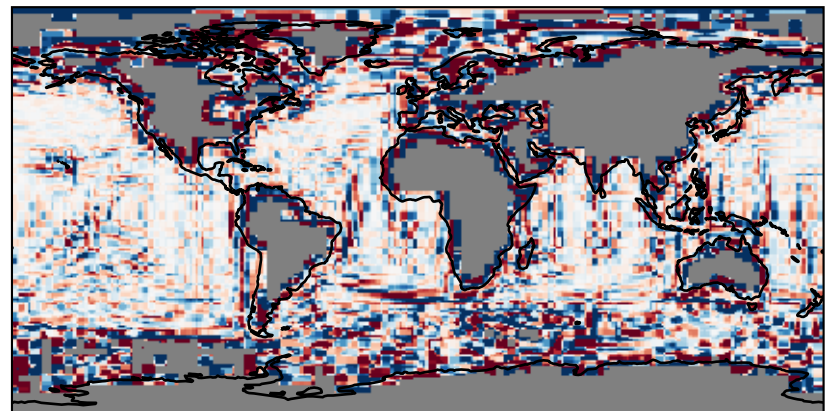
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

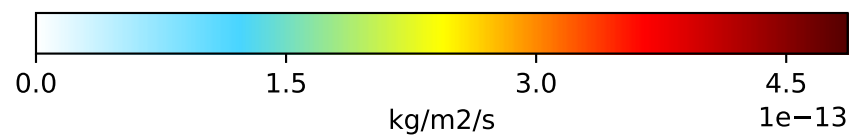
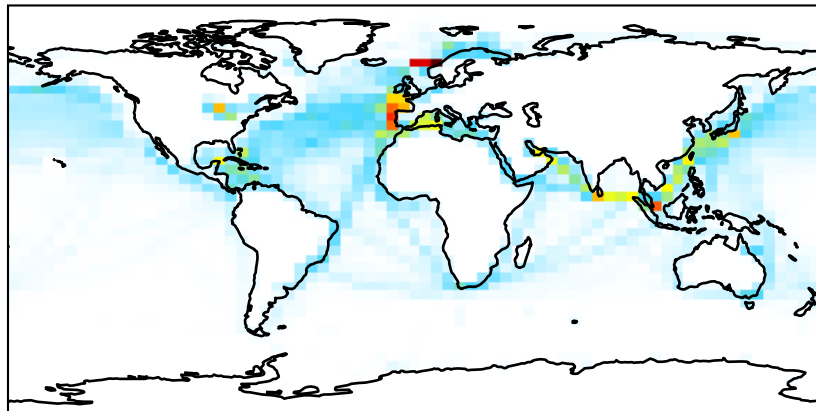


Ratio (1x1.25)  
Dev/Ref, Fixed Range

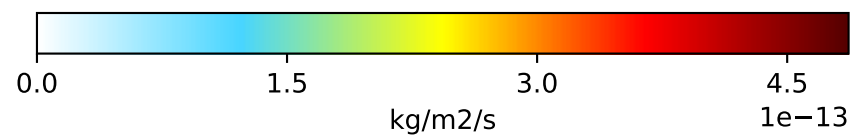
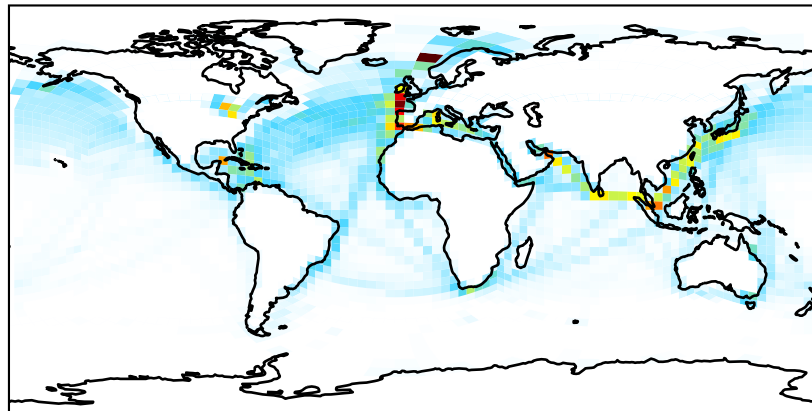


# EmisSO4\_Ship

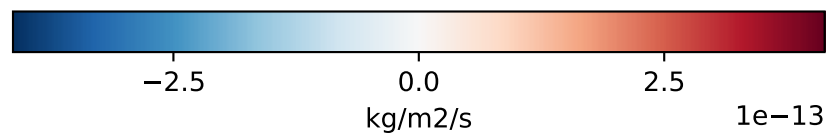
GCC\_dev (Ref)  
4.0x5.0



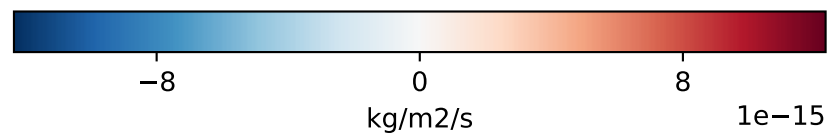
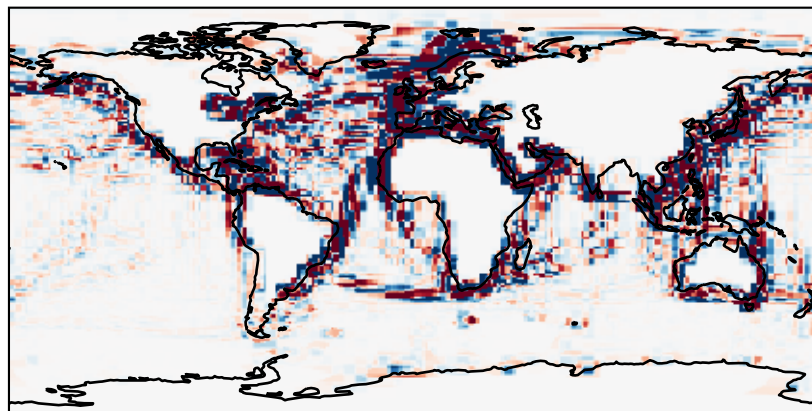
GCHP\_dev (Dev)  
c24



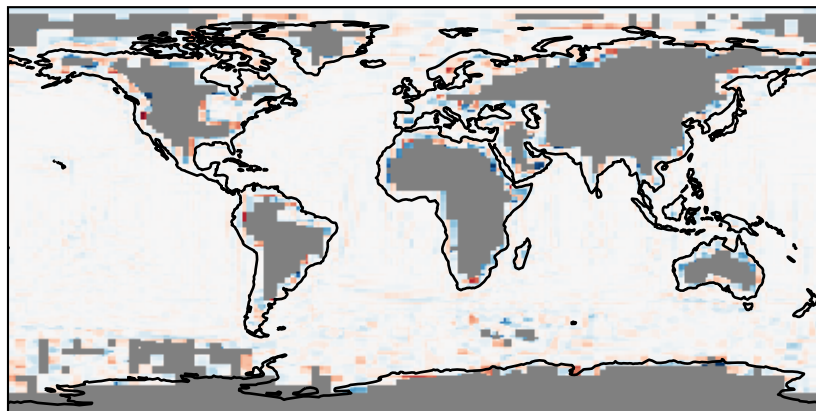
Difference (1x1.25)  
Dev - Ref, Dynamic Range



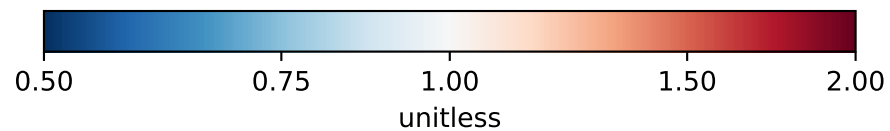
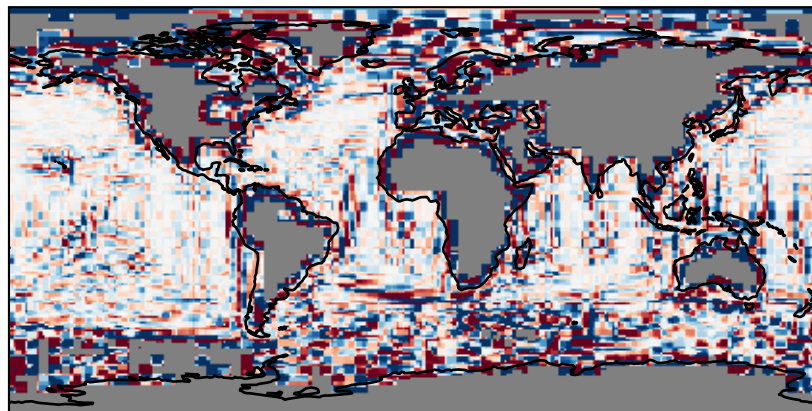
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

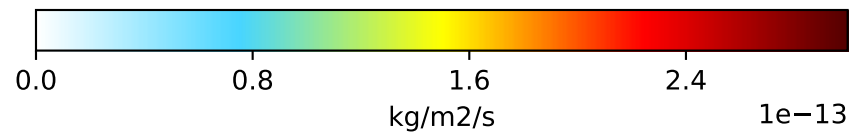
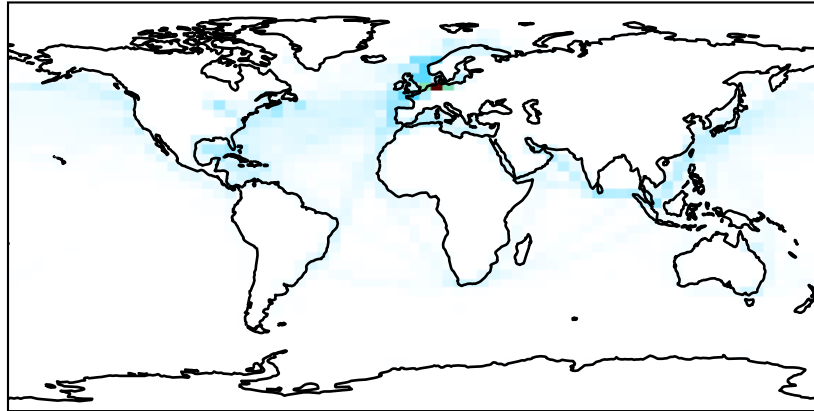


Ratio (1x1.25)  
Dev/Ref, Fixed Range

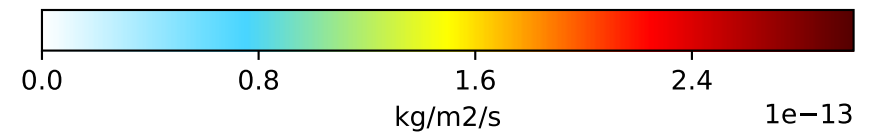
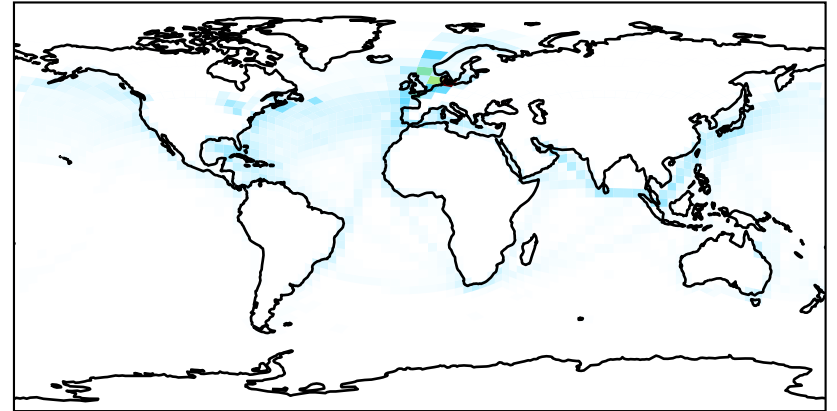


# EmisSOAP\_Ship

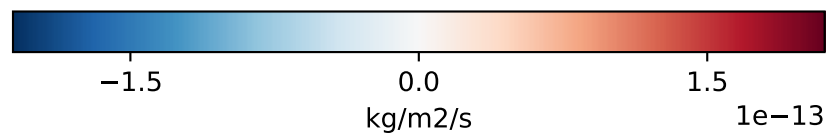
GCC\_dev (Ref)  
4.0x5.0



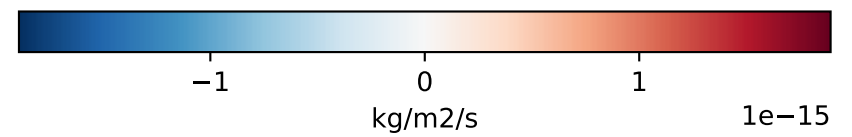
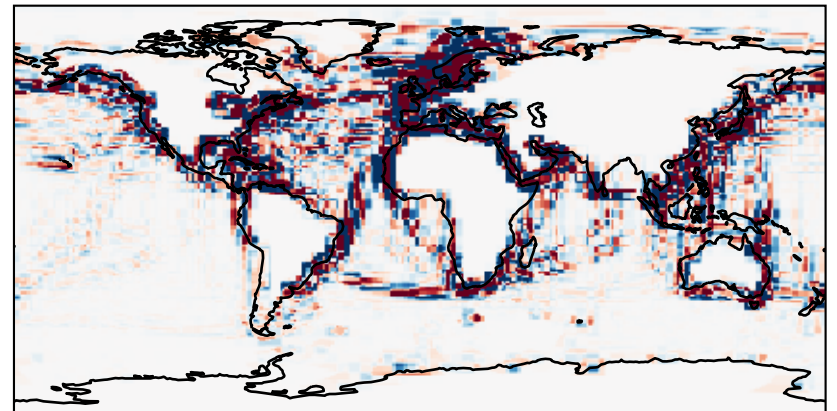
GCHP\_dev (Dev)  
c24



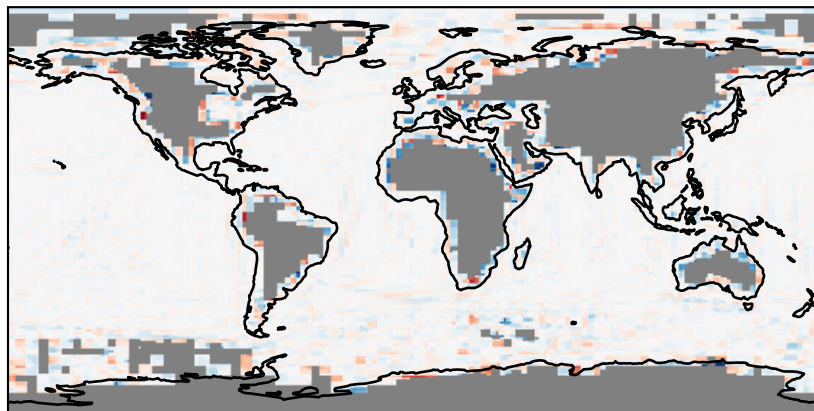
Difference (1x1.25)  
Dev - Ref, Dynamic Range



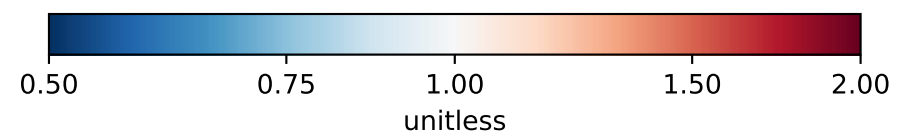
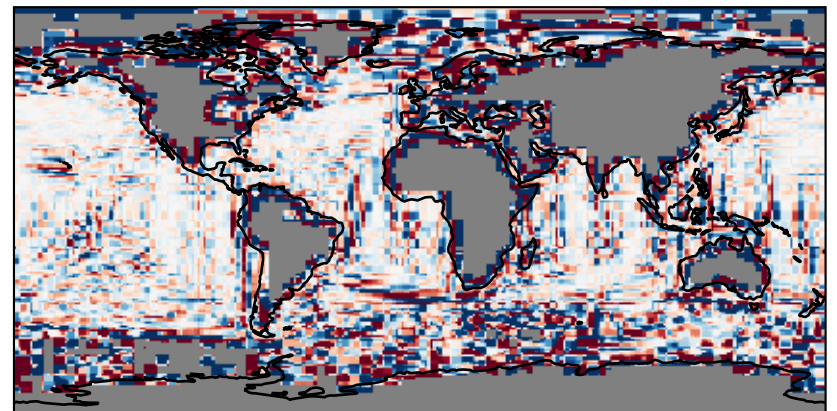
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

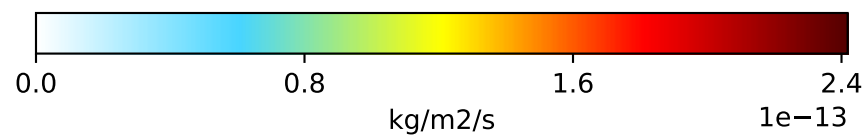
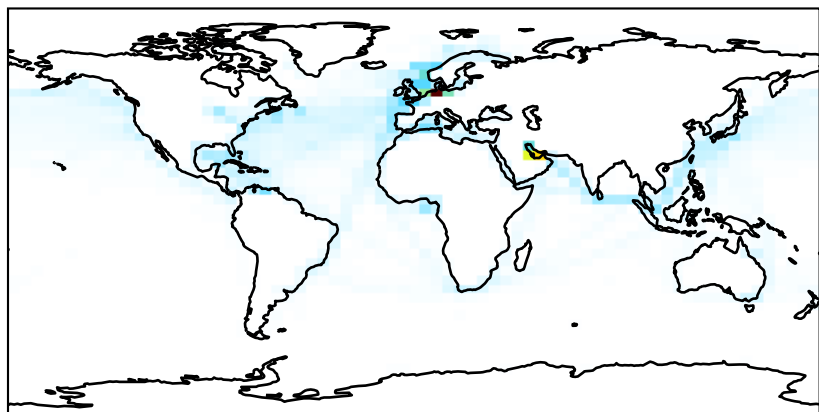


Ratio (1x1.25)  
Dev/Ref, Fixed Range

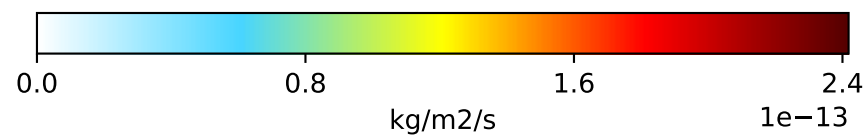
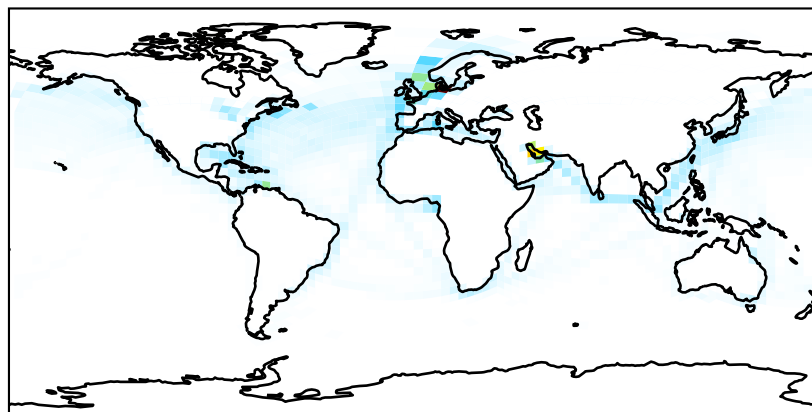


# EmisTOLU\_Ship

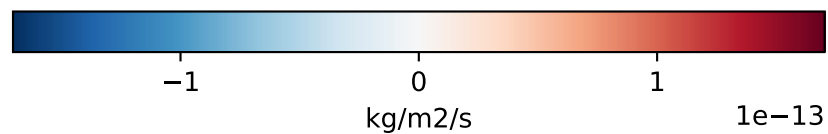
GCC\_dev (Ref)  
4.0x5.0



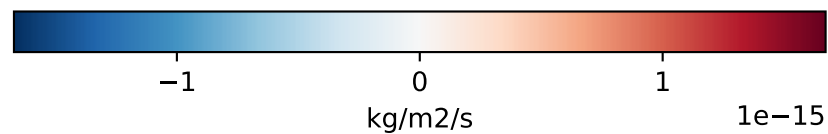
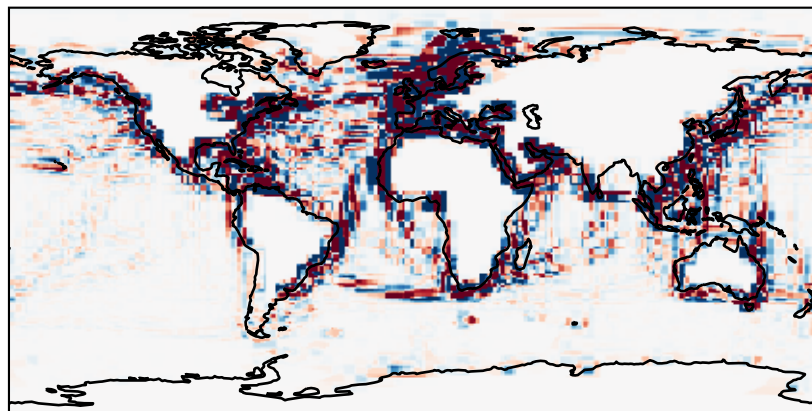
GCHP\_dev (Dev)  
c24



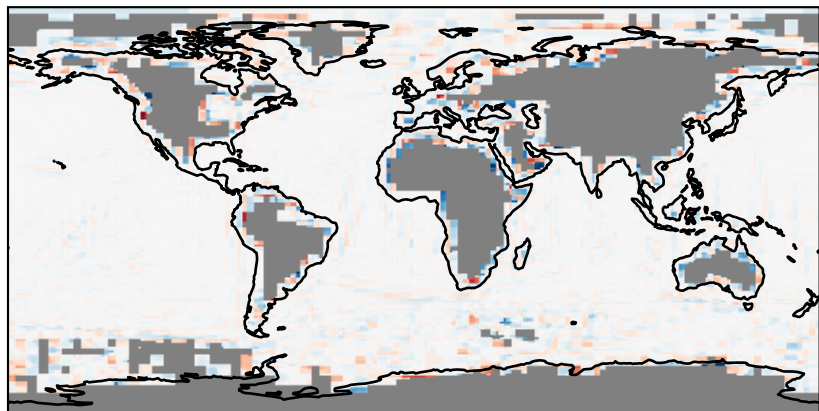
Difference (1x1.25)  
Dev - Ref, Dynamic Range



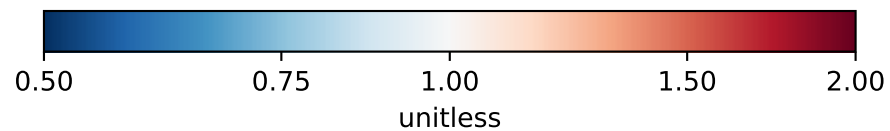
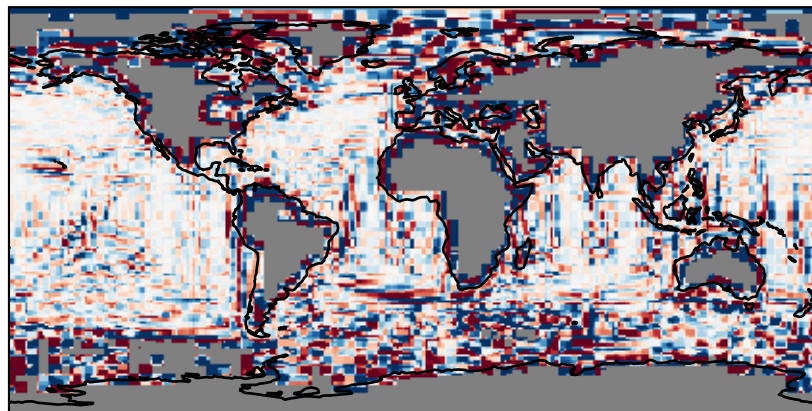
Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range

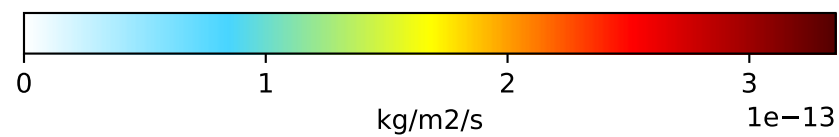
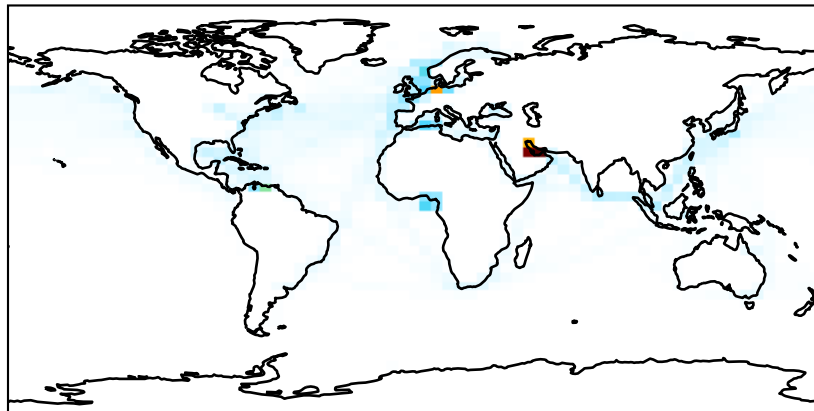


Ratio (1x1.25)  
Dev/Ref, Fixed Range

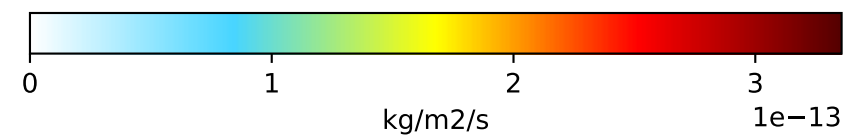
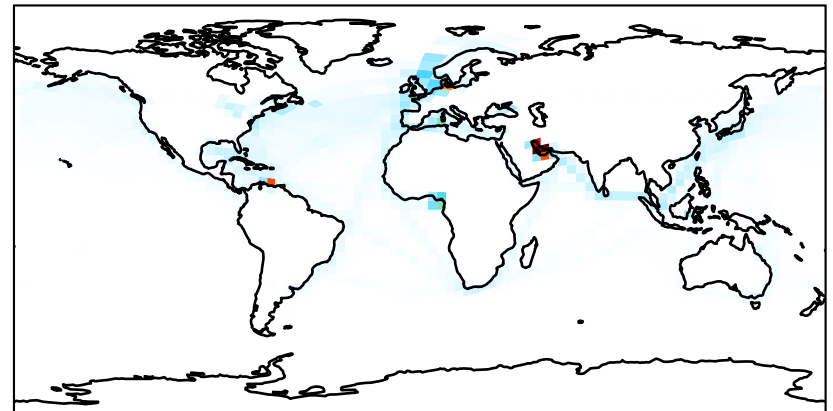


# EmisXYLE\_Ship

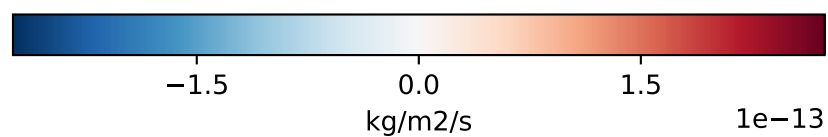
GCC\_dev (Ref)  
4.0x5.0



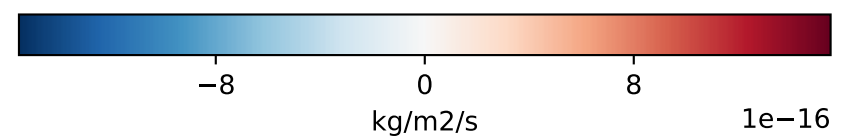
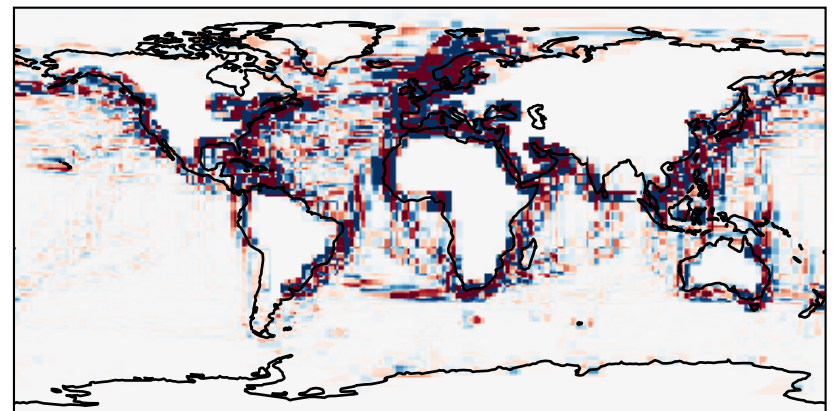
GCHP\_dev (Dev)  
c24



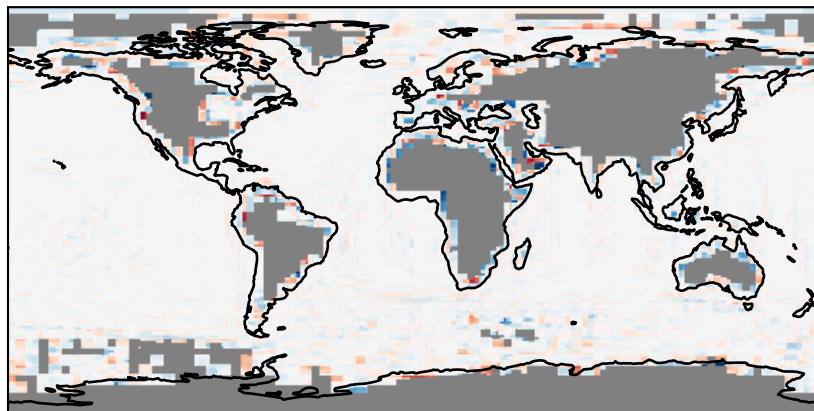
Difference (1x1.25)  
Dev - Ref, Dynamic Range



Difference (1x1.25)  
Dev - Ref, Restricted Range [5%,95%]



Ratio (1x1.25)  
Dev/Ref, Dynamic Range



Ratio (1x1.25)  
Dev/Ref, Fixed Range

