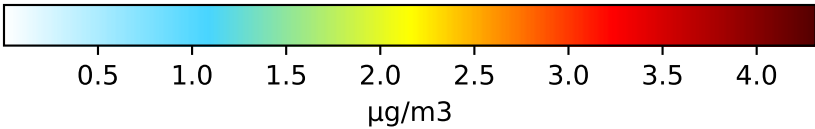
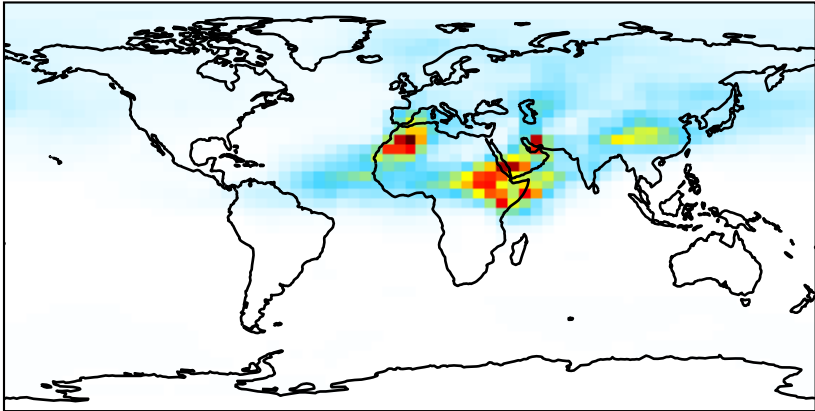
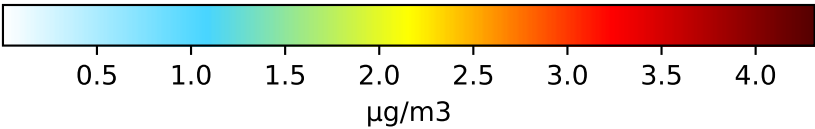
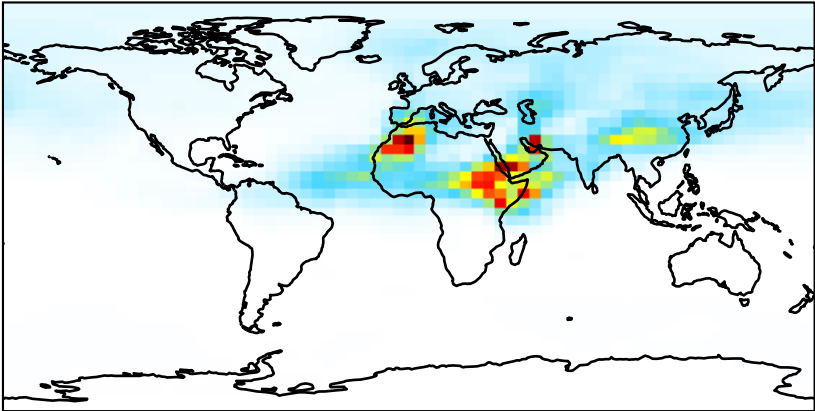


# SpeciesConcVV\_DST1

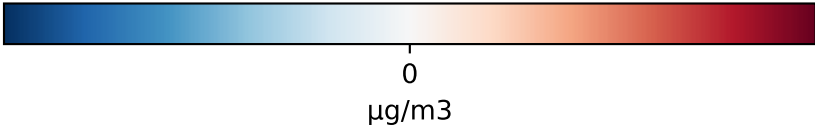
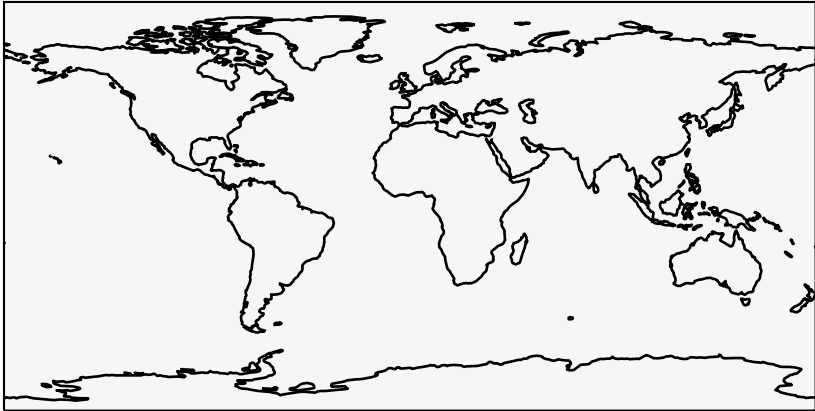
GCC\_ref (Ref)  
4.0x5.0



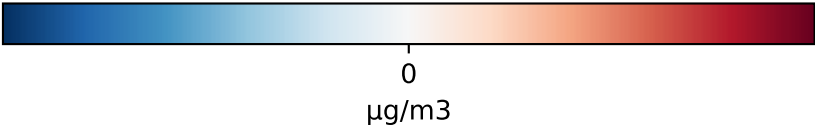
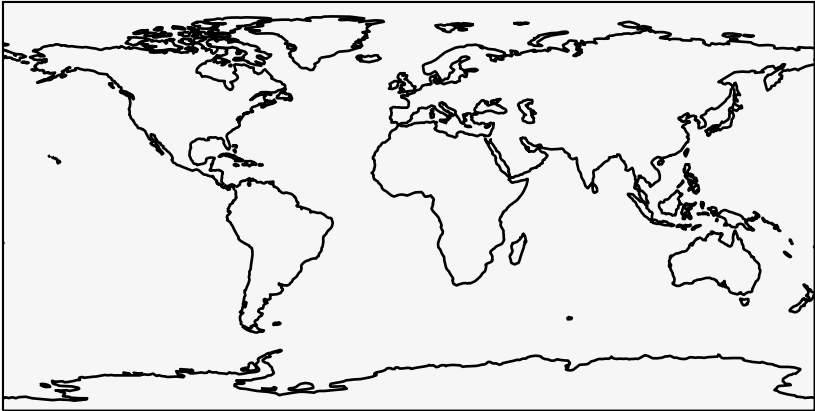
GCC\_dev (Dev)  
4.0x5.0



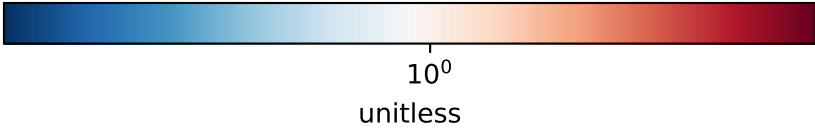
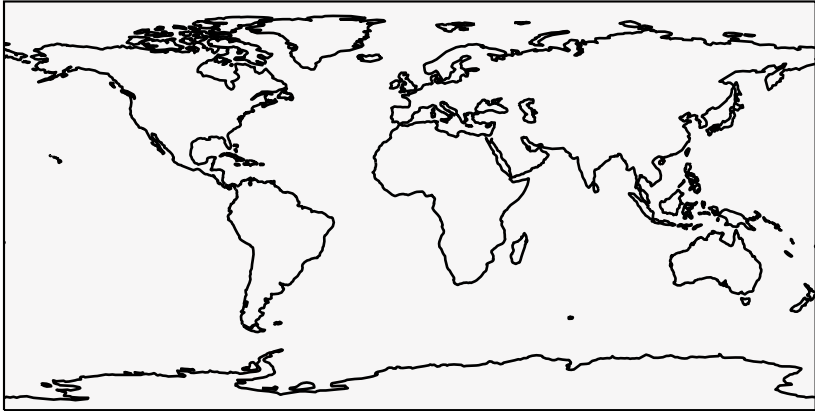
Difference  
Dev - Ref, Dynamic Range



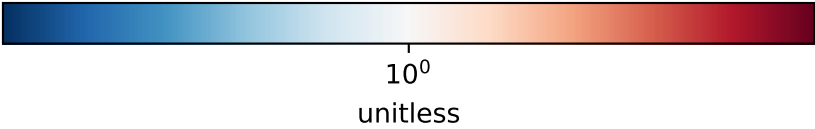
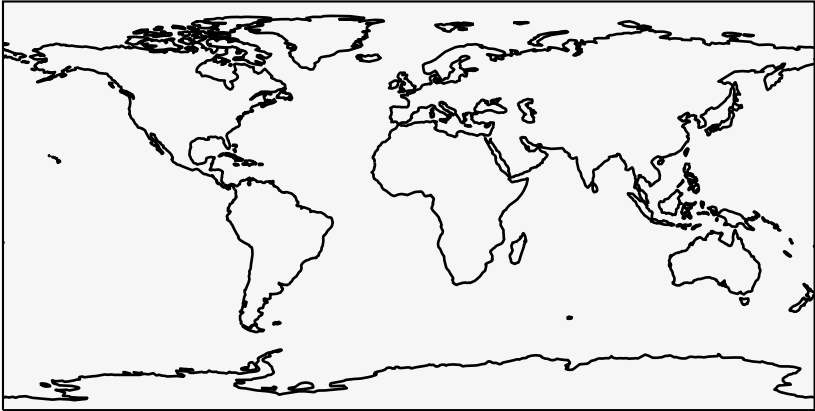
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

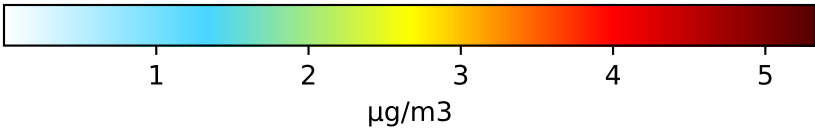
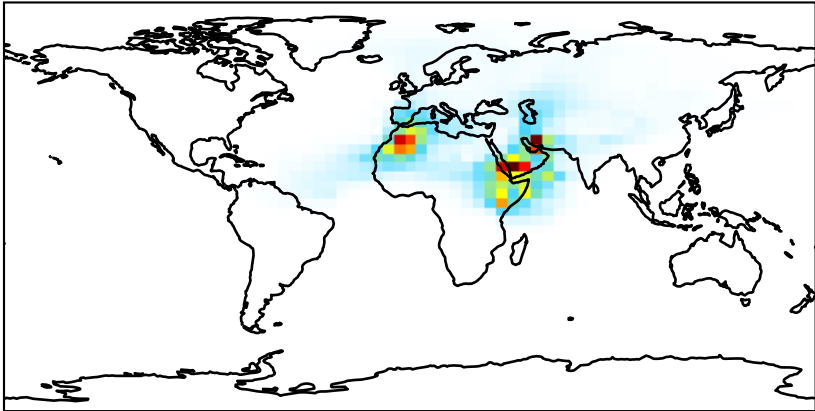


Ratio  
Dev/Ref, Fixed Range

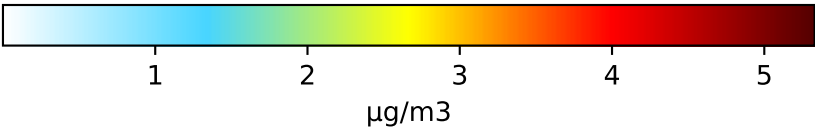
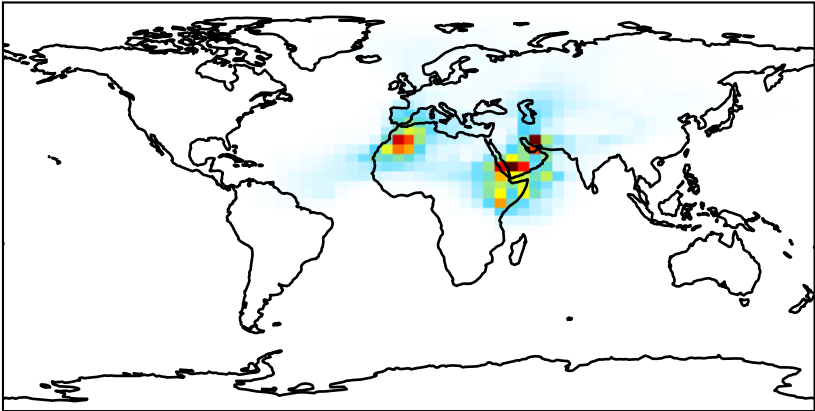


# SpeciesConcVV\_DST2

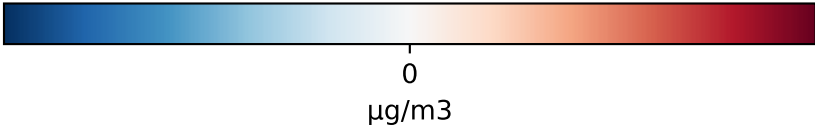
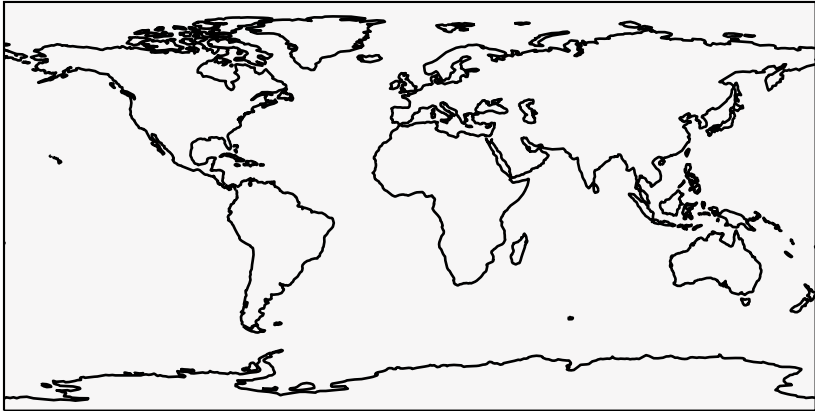
GCC\_ref (Ref)  
4.0x5.0



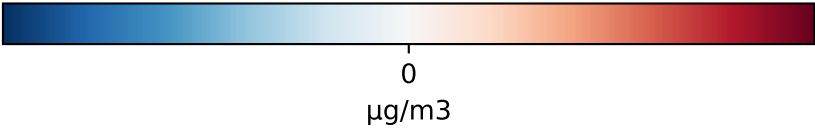
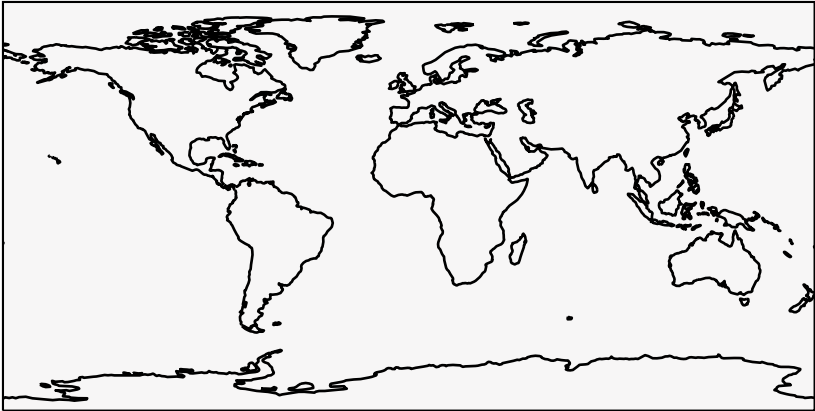
GCC\_dev (Dev)  
4.0x5.0



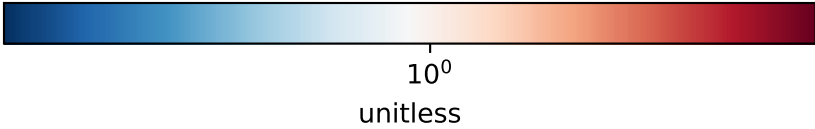
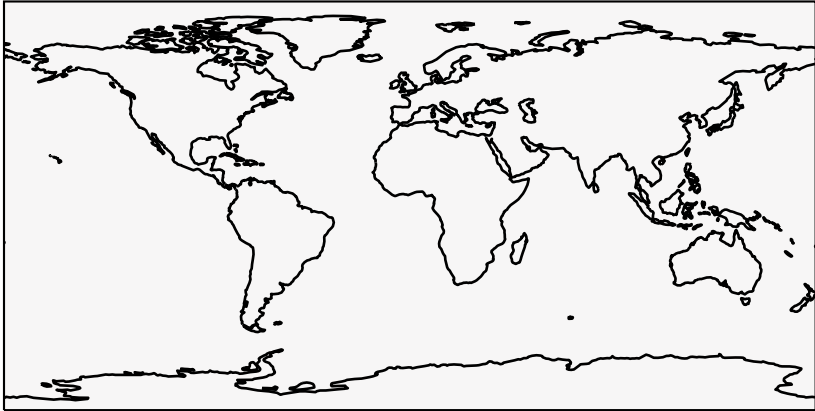
Difference  
Dev - Ref, Dynamic Range



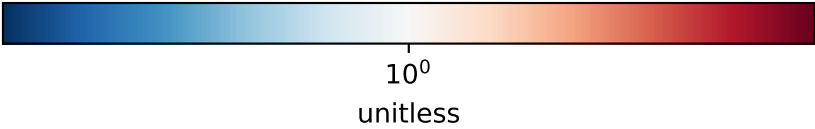
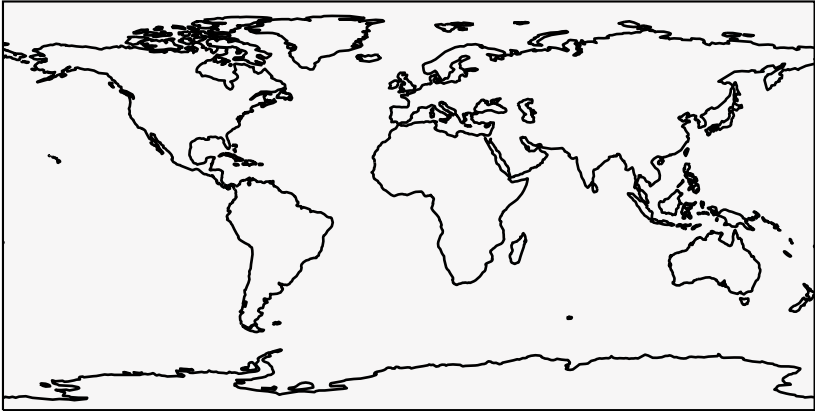
Difference  
Dev - Ref, Restricted Range [5%,95%]



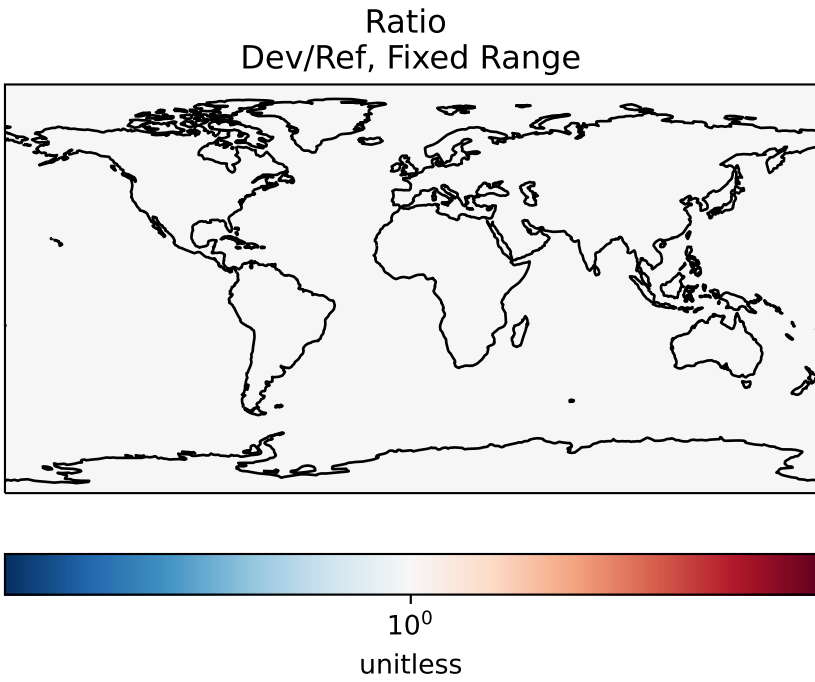
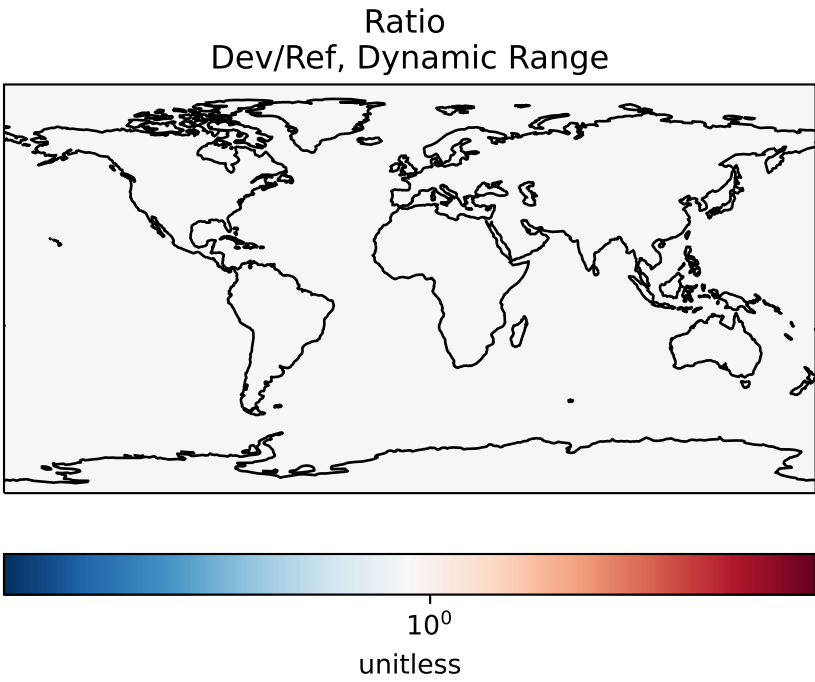
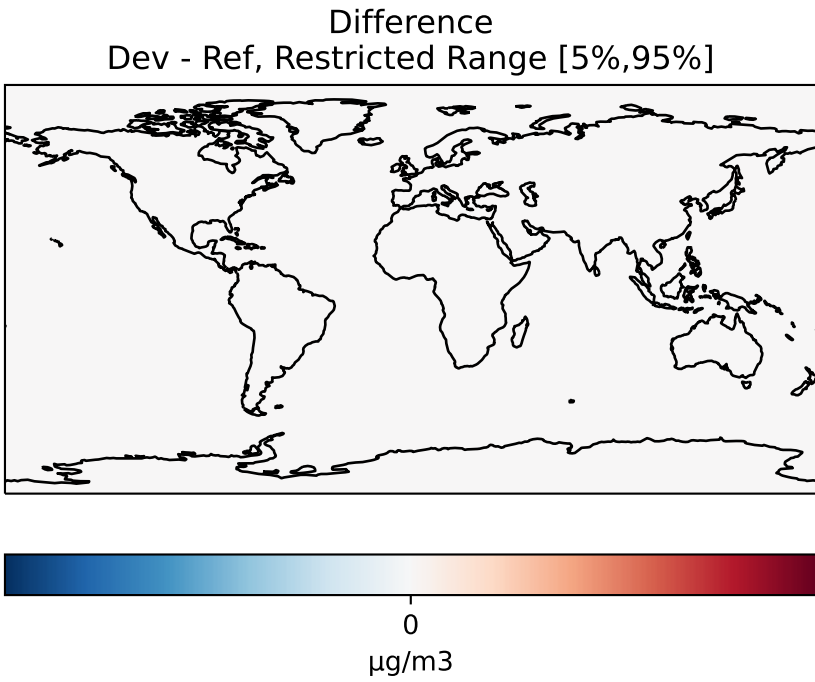
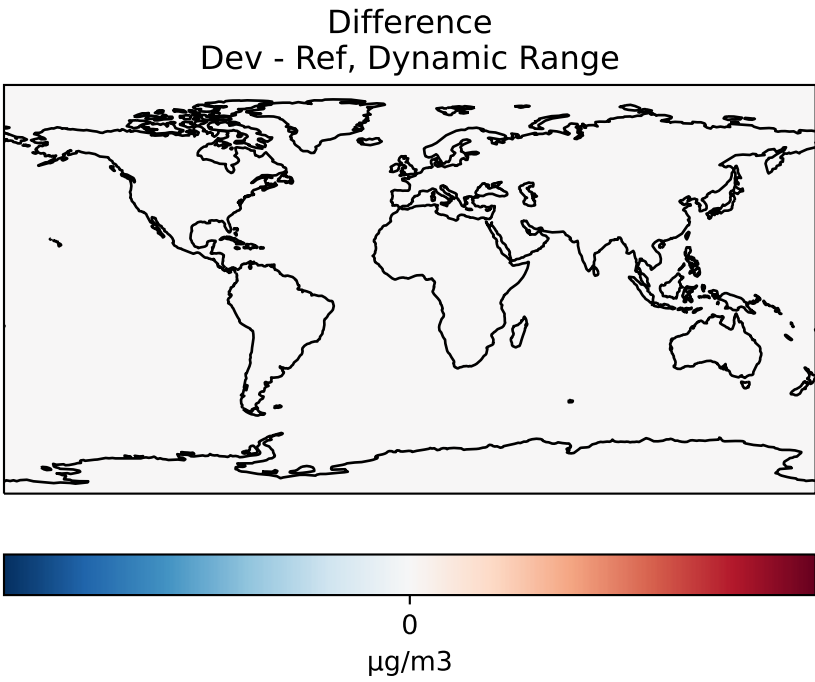
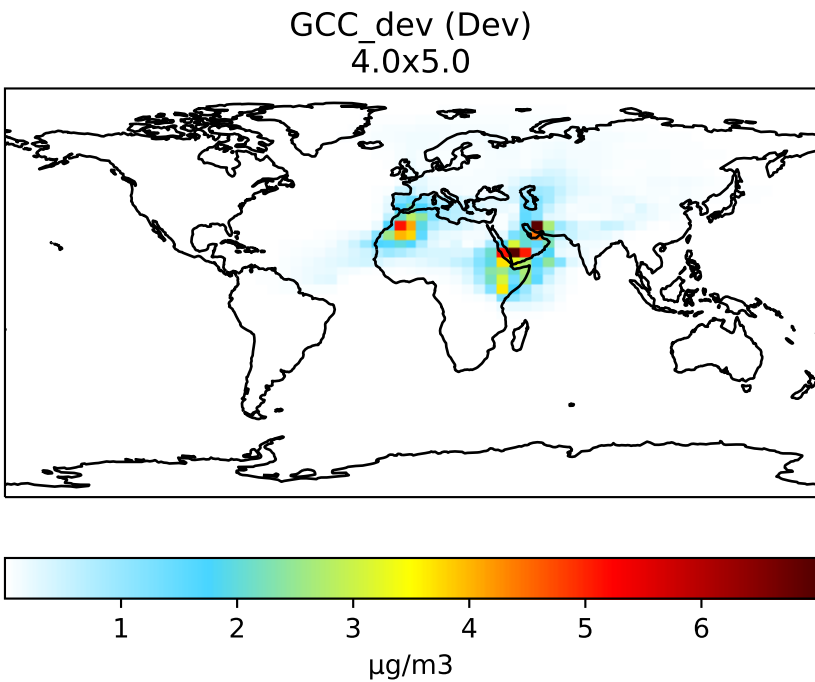
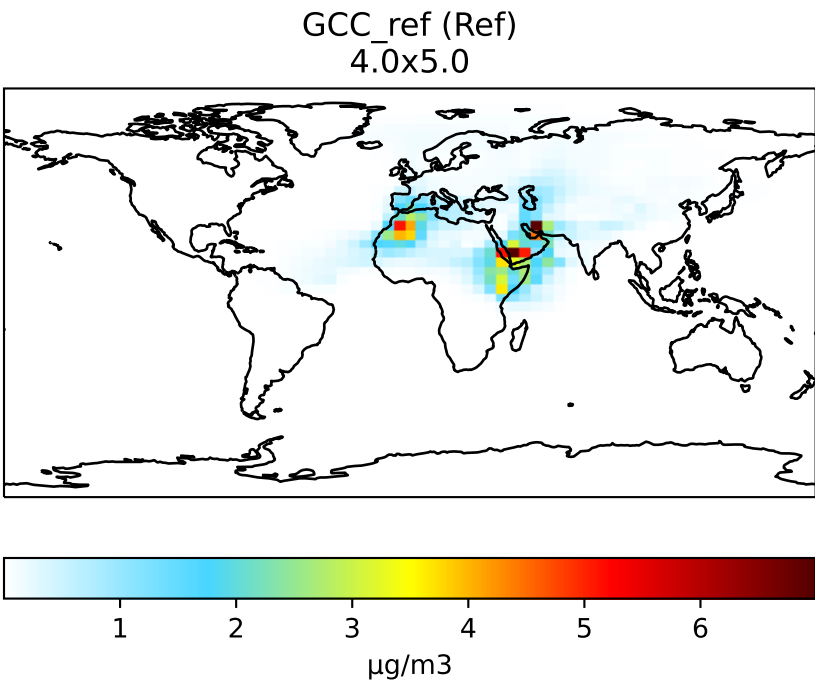
Ratio  
Dev/Ref, Dynamic Range



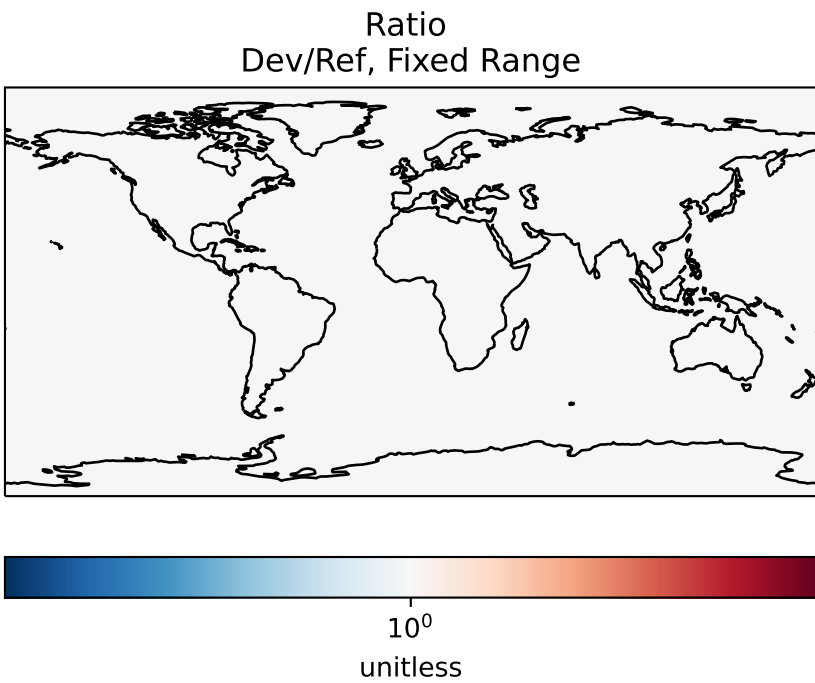
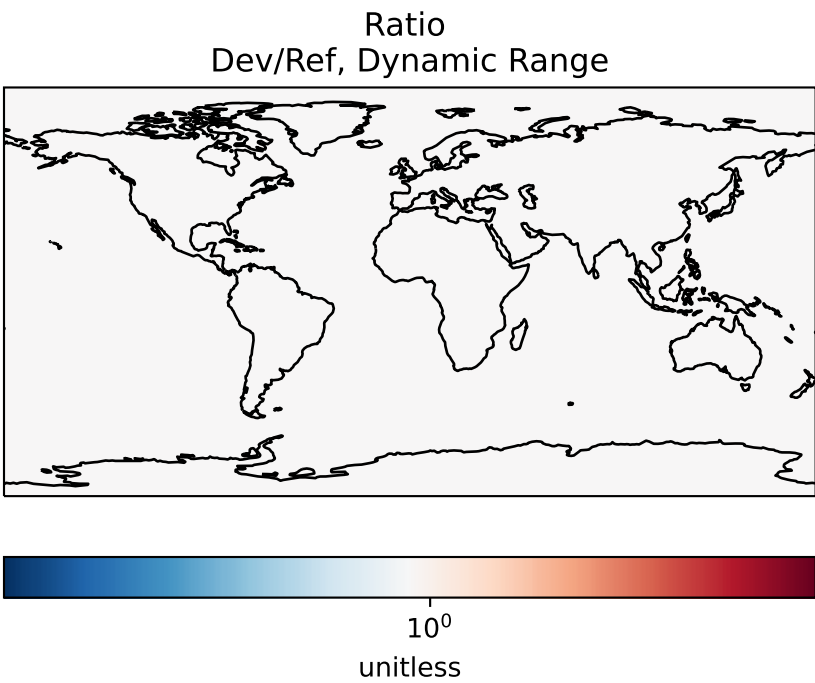
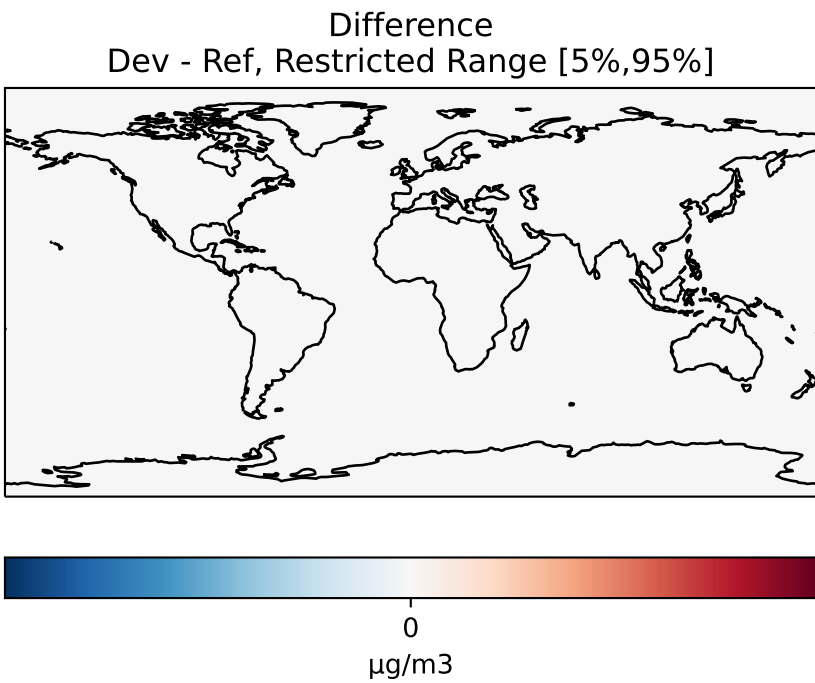
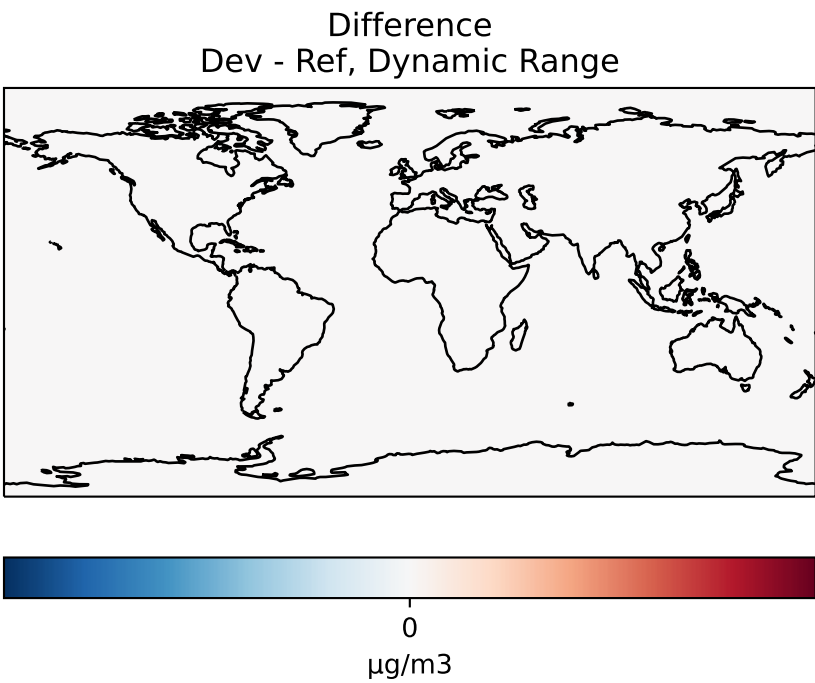
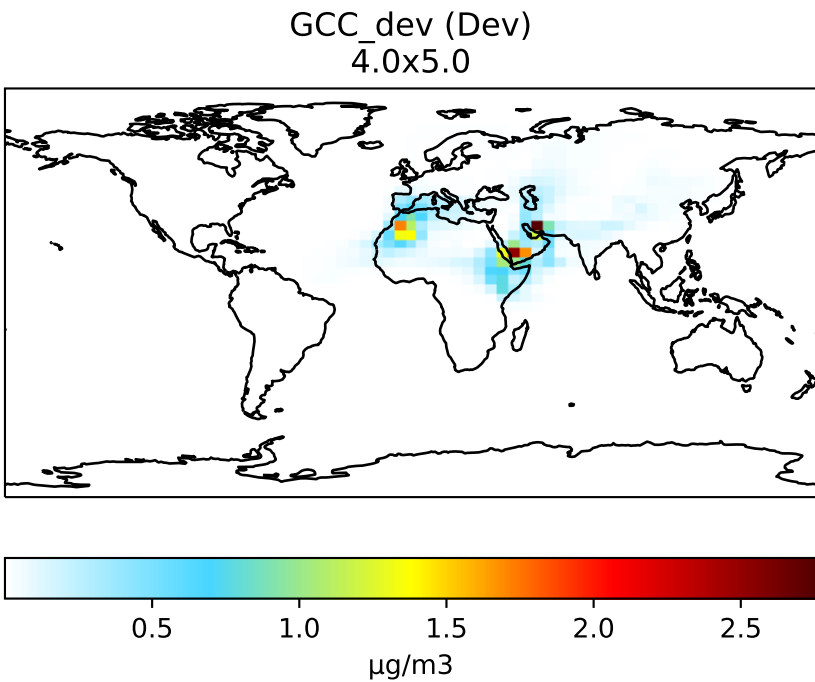
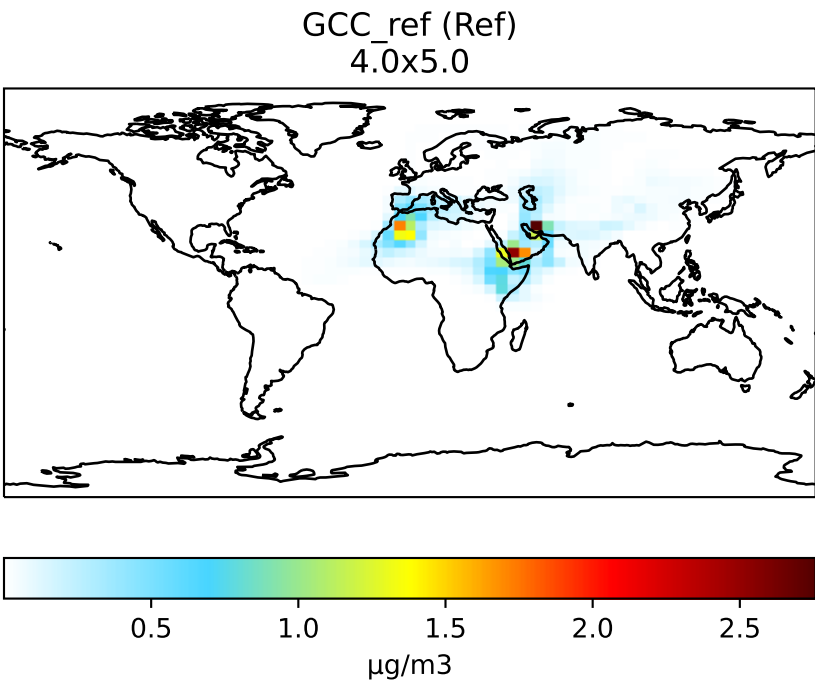
Ratio  
Dev/Ref, Fixed Range



# SpeciesConcVV\_DST3

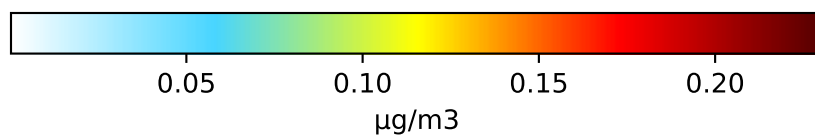
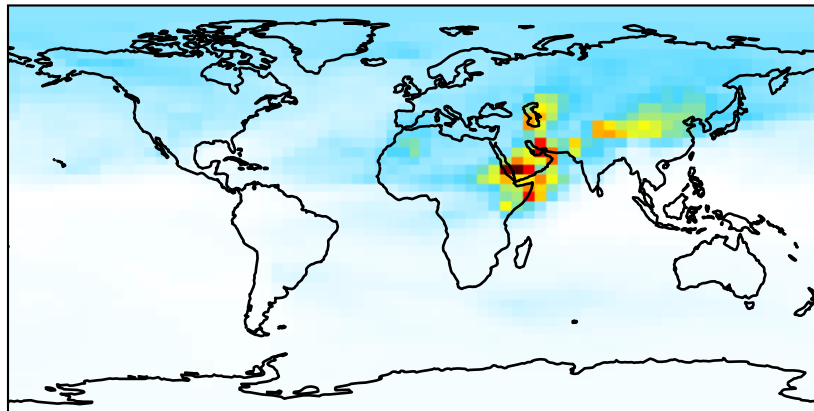


# SpeciesConcVV\_DST4

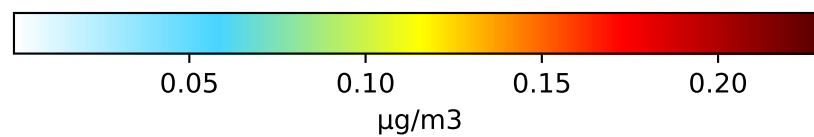
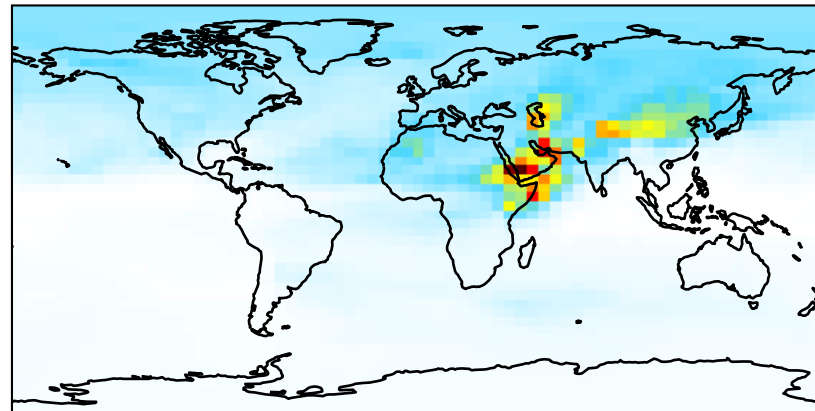


# SpeciesConcVV\_NH4

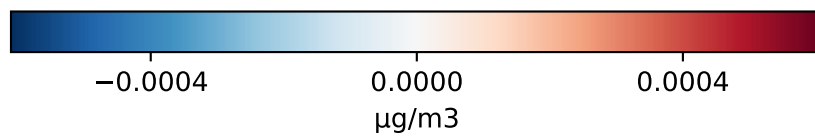
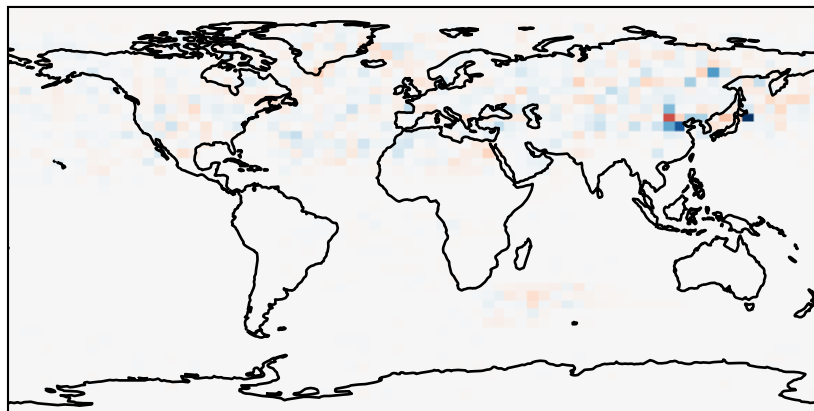
GCC\_ref (Ref)  
4.0x5.0



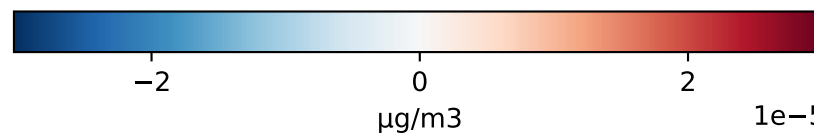
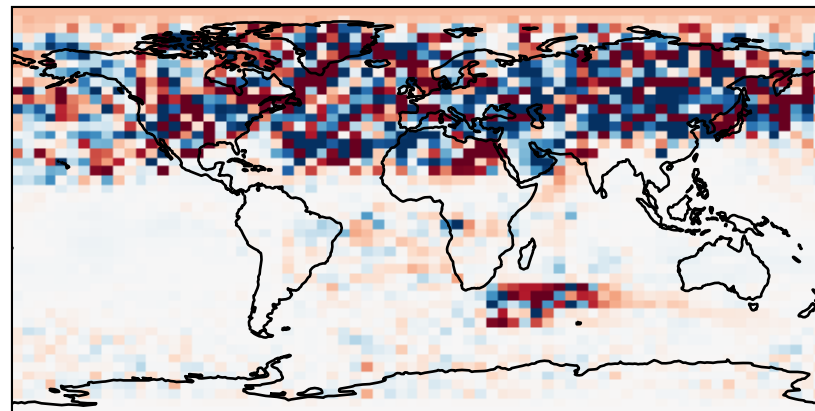
GCC\_dev (Dev)  
4.0x5.0



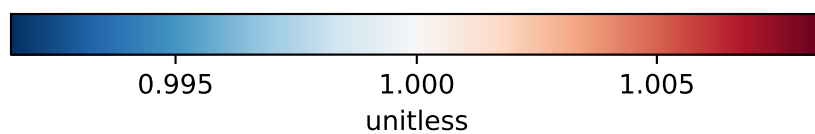
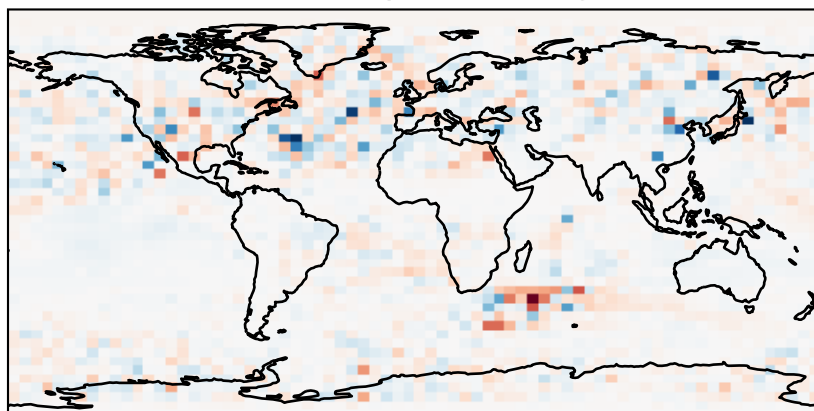
Difference  
Dev - Ref, Dynamic Range



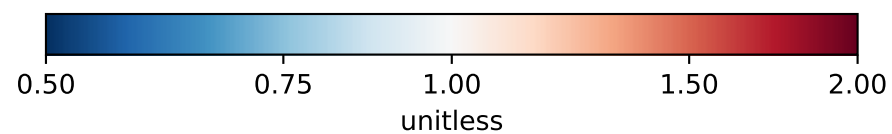
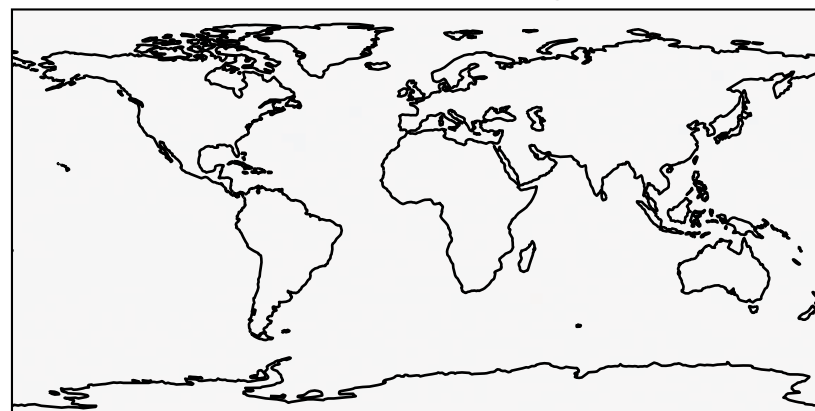
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

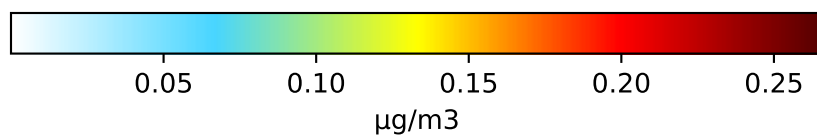
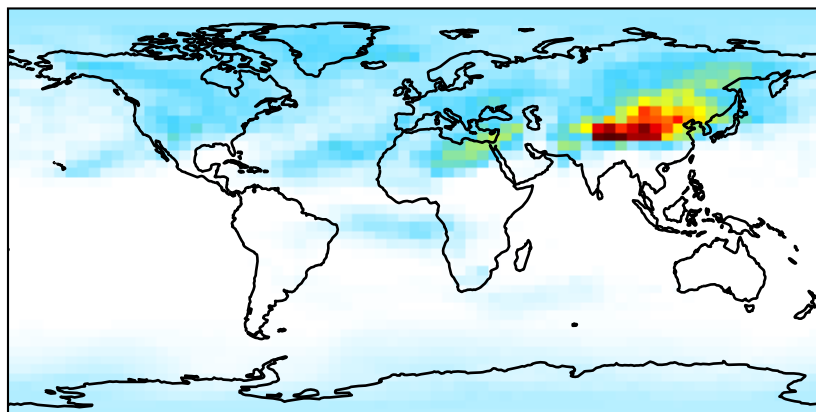


Ratio  
Dev/Ref, Fixed Range

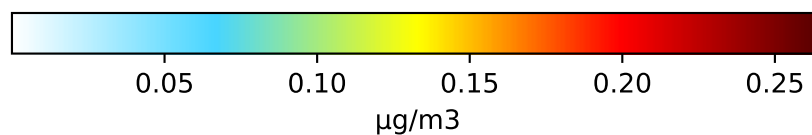
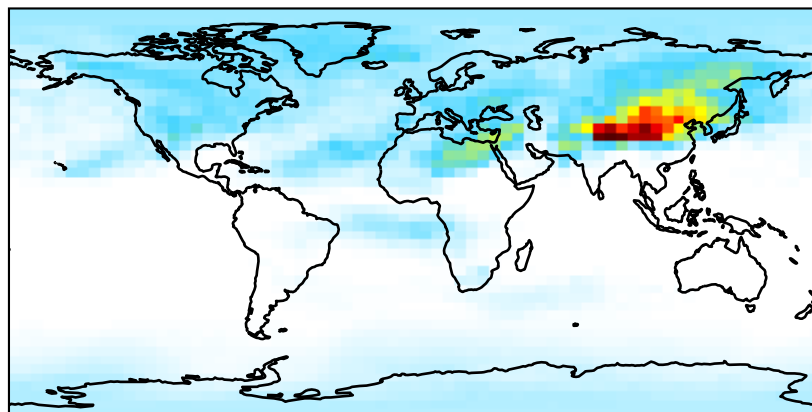


# SpeciesConcVV\_NIT

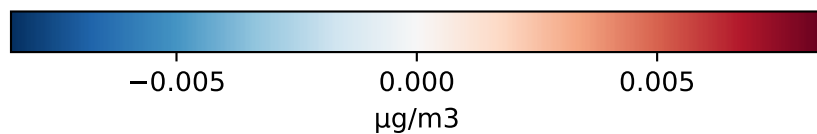
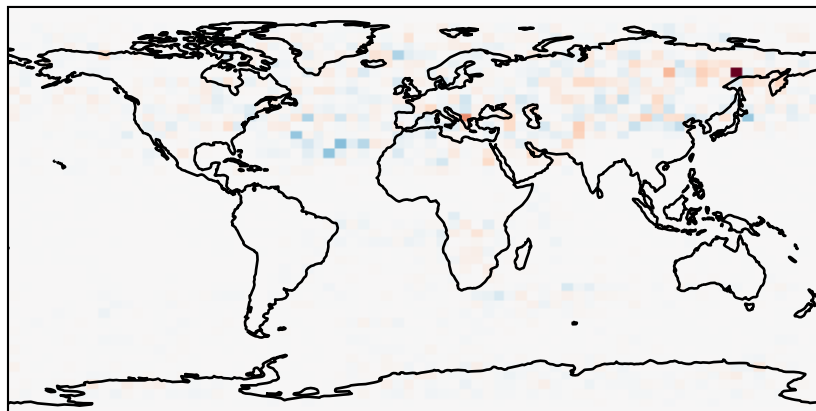
GCC\_ref (Ref)  
4.0x5.0



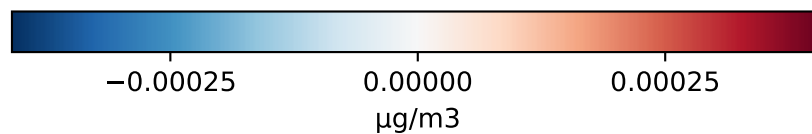
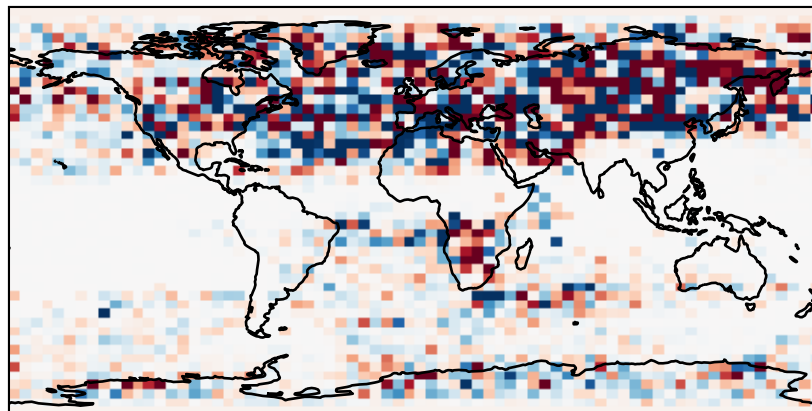
GCC\_dev (Dev)  
4.0x5.0



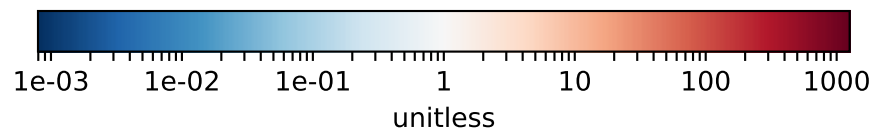
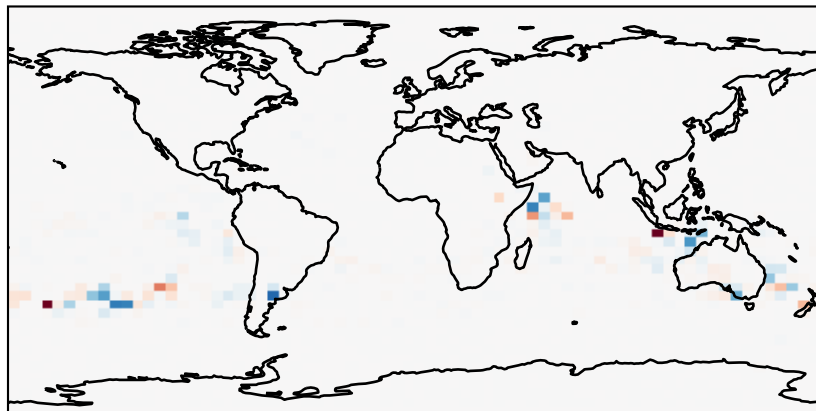
Difference  
Dev - Ref, Dynamic Range



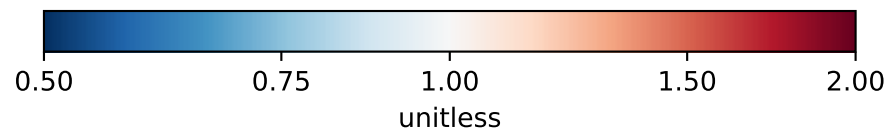
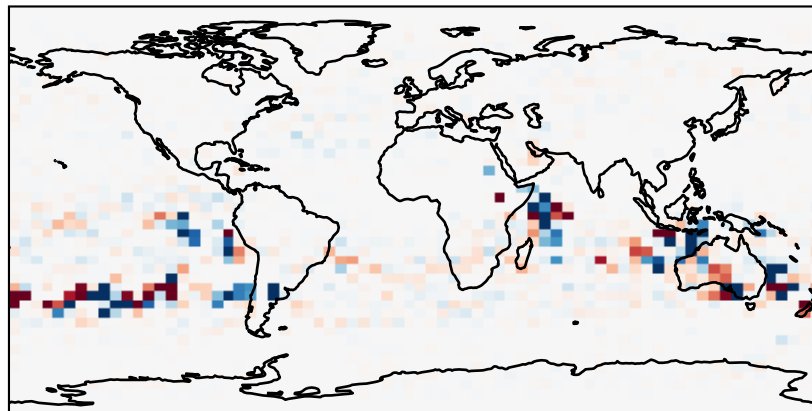
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

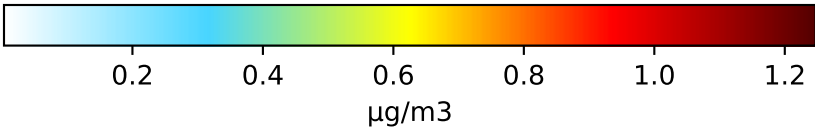
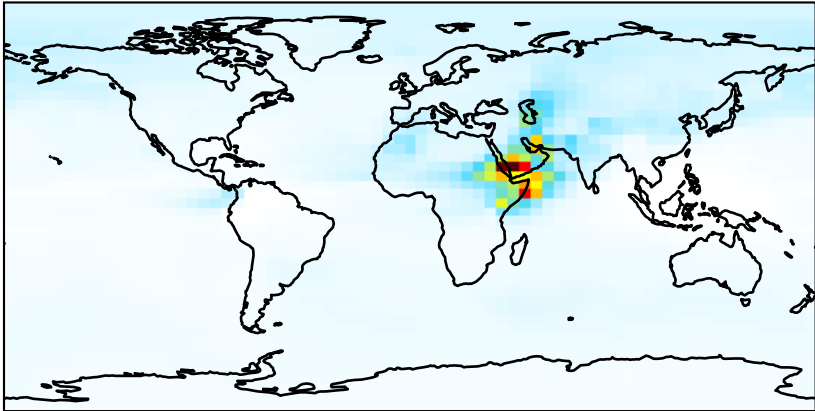


Ratio  
Dev/Ref, Fixed Range

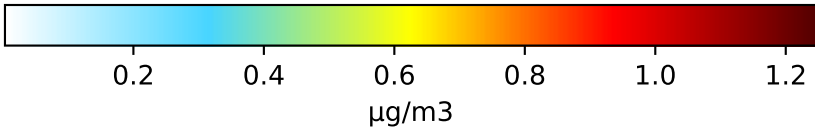
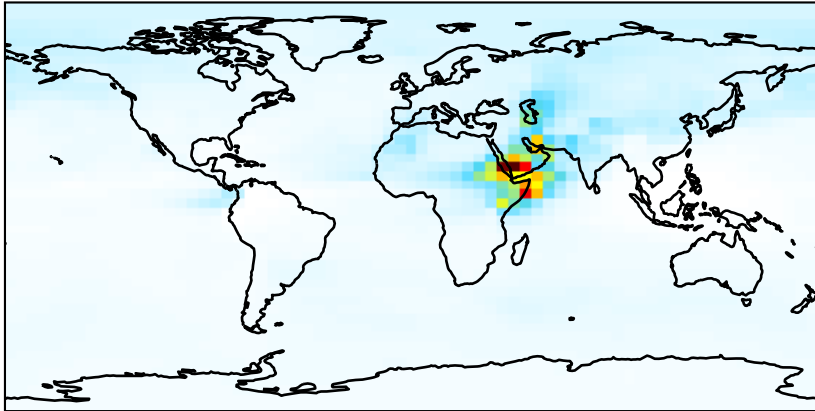


# SpeciesConcVV\_S04

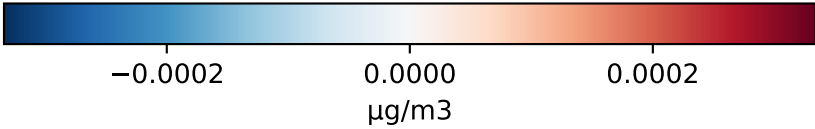
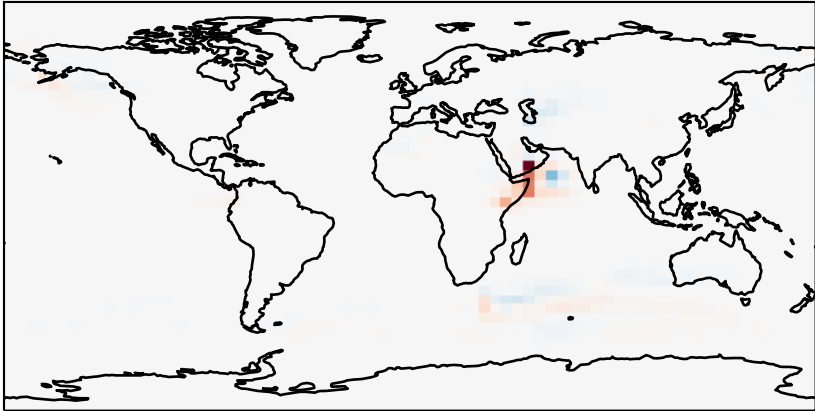
GCC\_ref (Ref)  
4.0x5.0



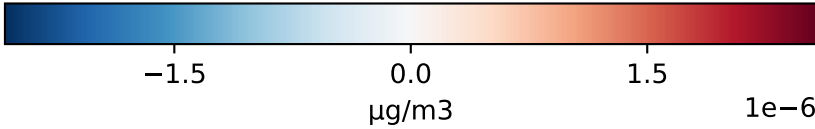
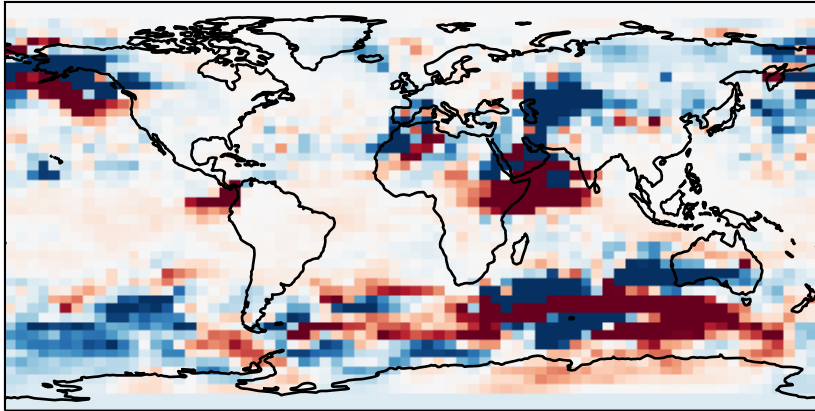
GCC\_dev (Dev)  
4.0x5.0



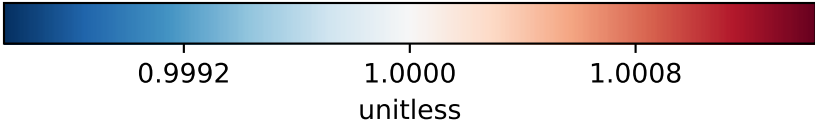
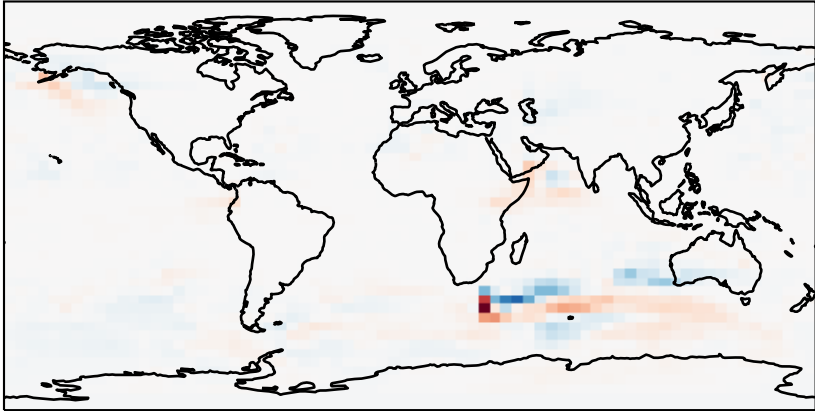
Difference  
Dev - Ref, Dynamic Range



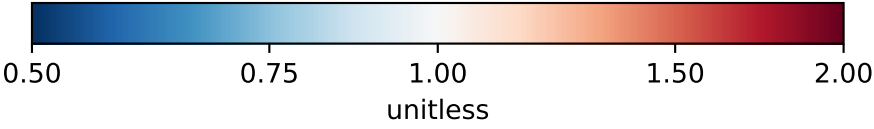
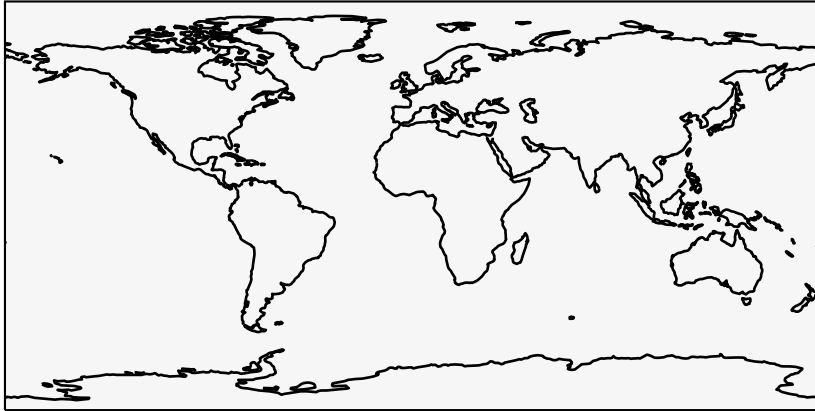
Difference  
Dev - Ref, Restricted Range [5%,95%]



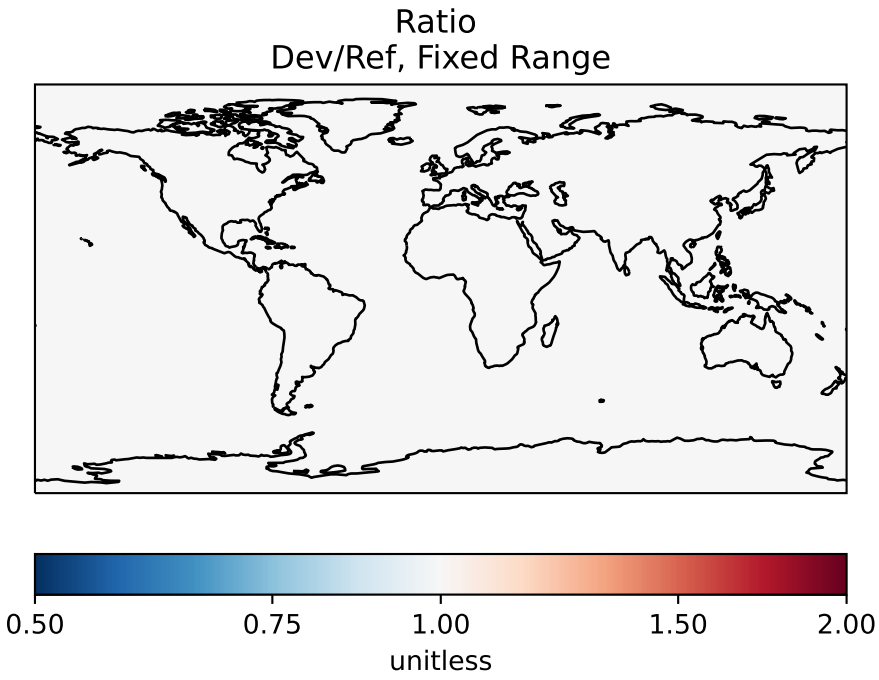
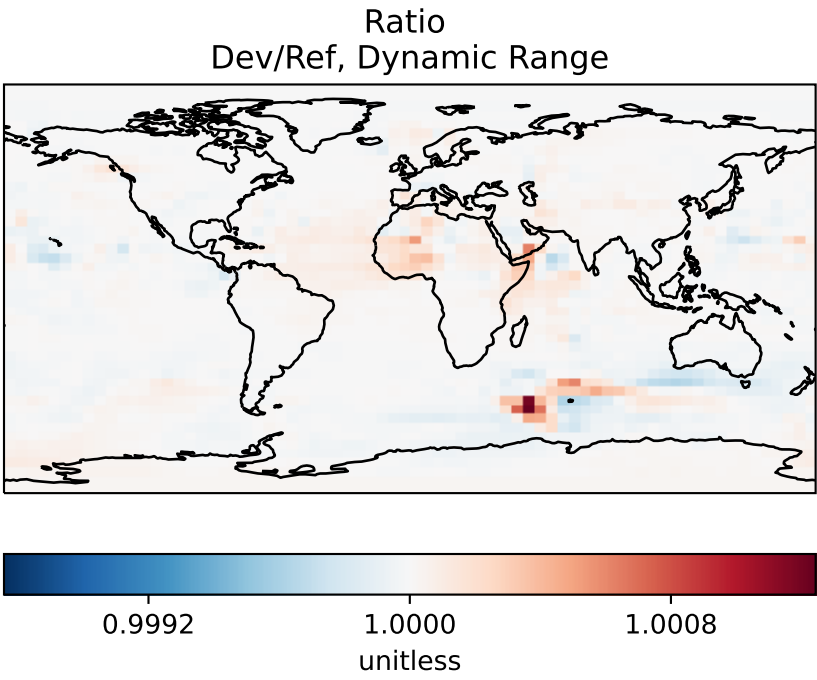
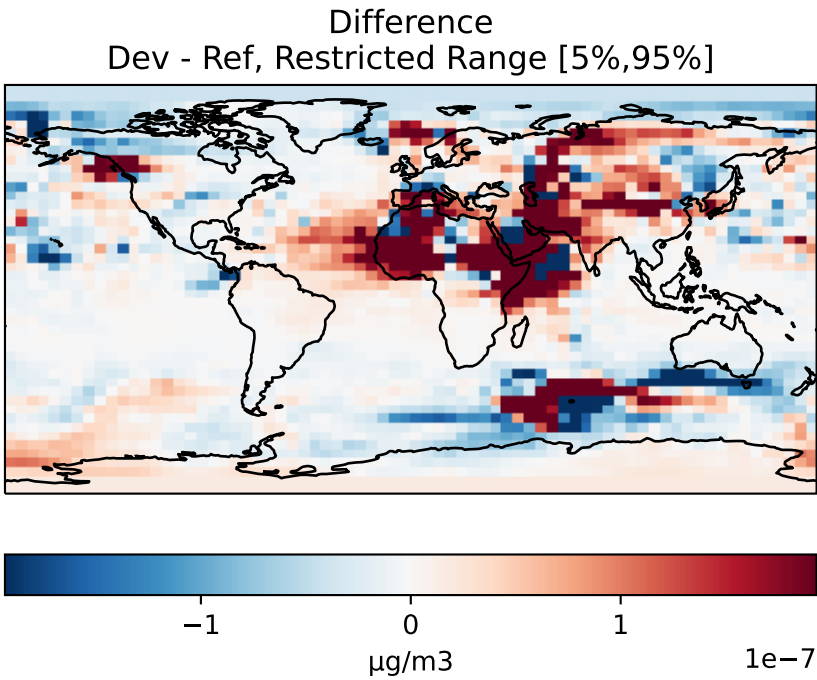
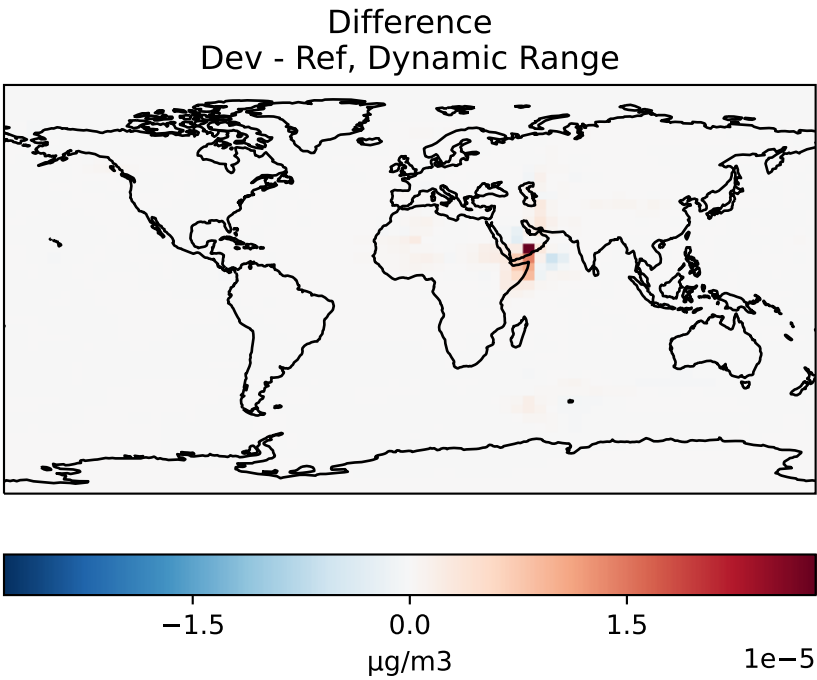
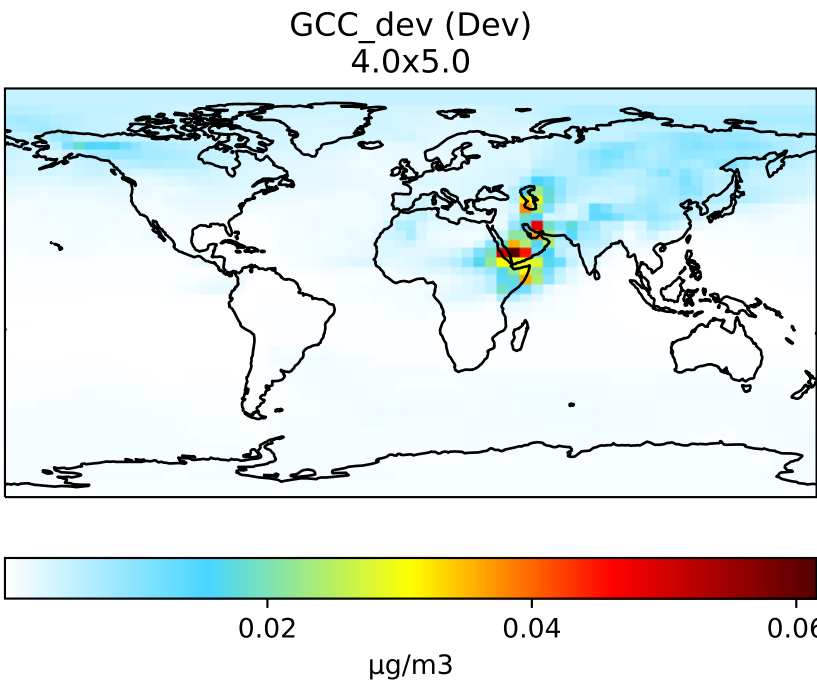
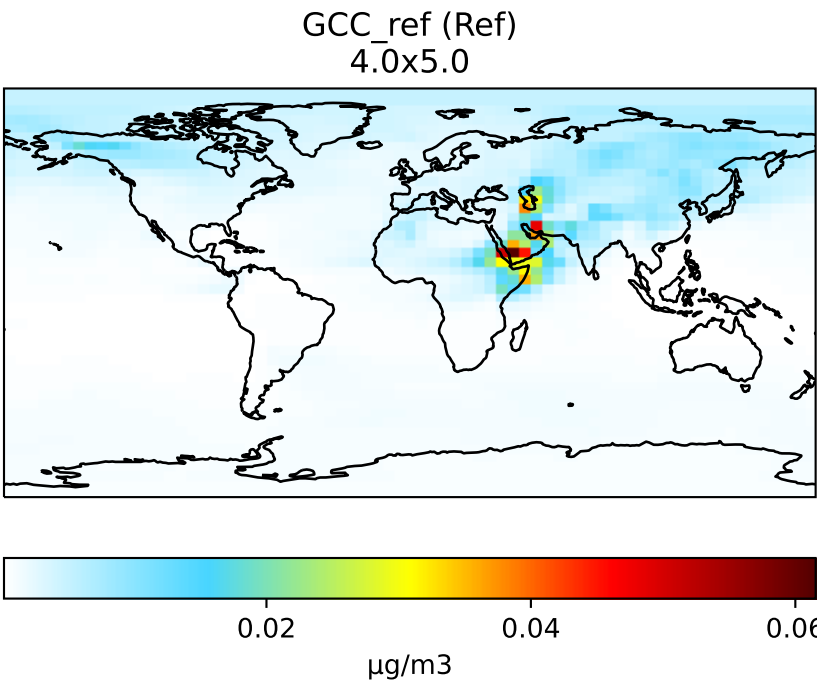
Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

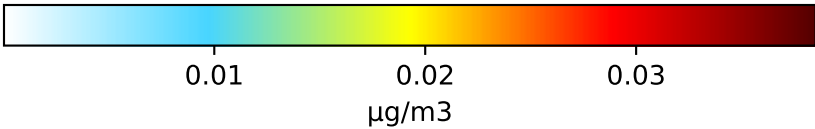
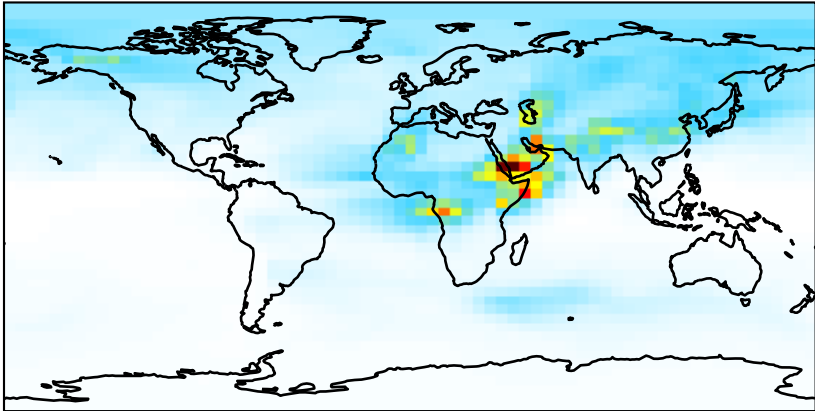


# SpeciesConcVV\_HMS

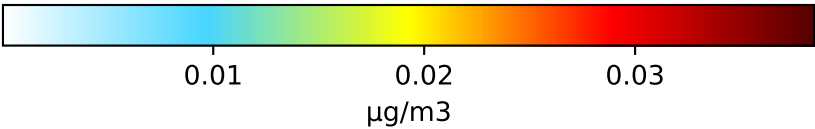
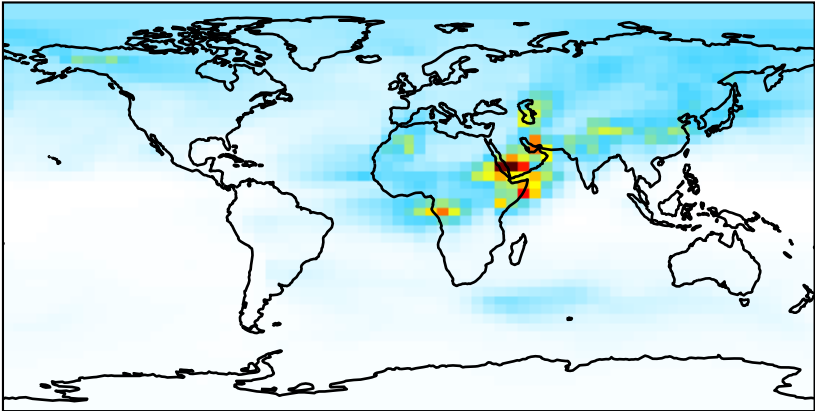


# SpeciesConcVW\_BCPI

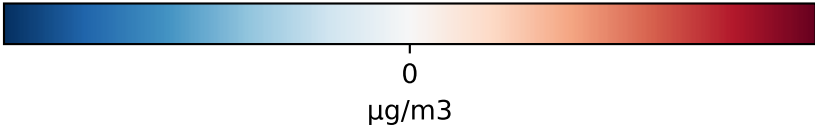
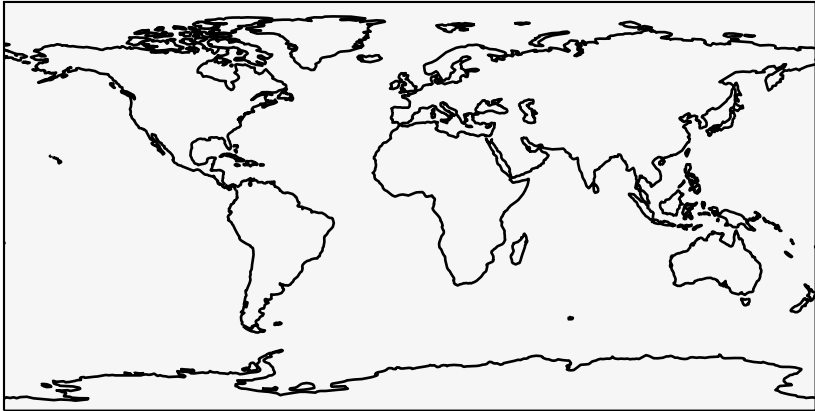
GCC\_ref (Ref)  
4.0x5.0



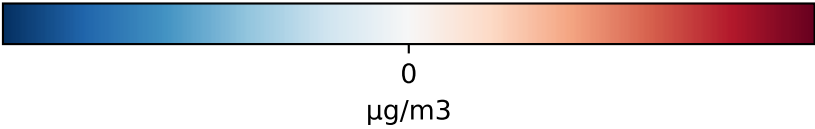
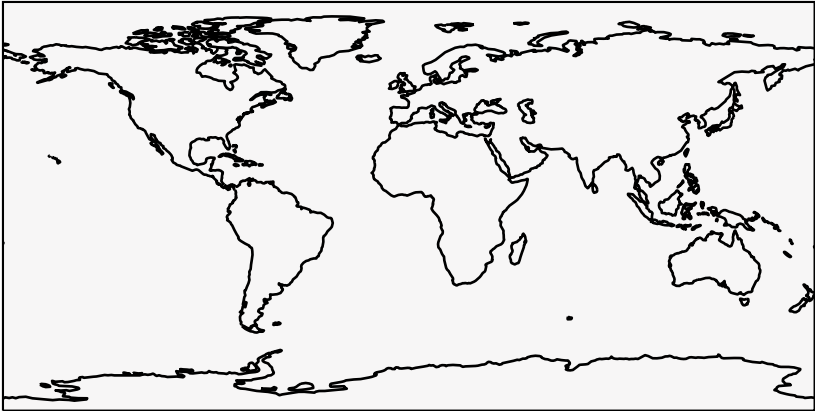
GCC\_dev (Dev)  
4.0x5.0



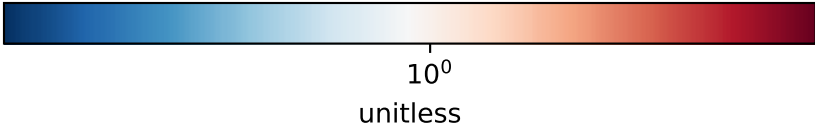
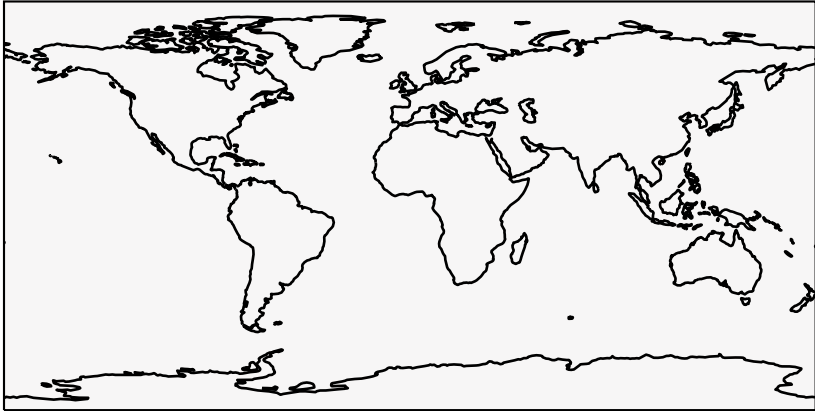
Difference  
Dev - Ref, Dynamic Range



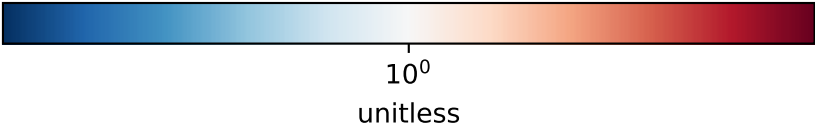
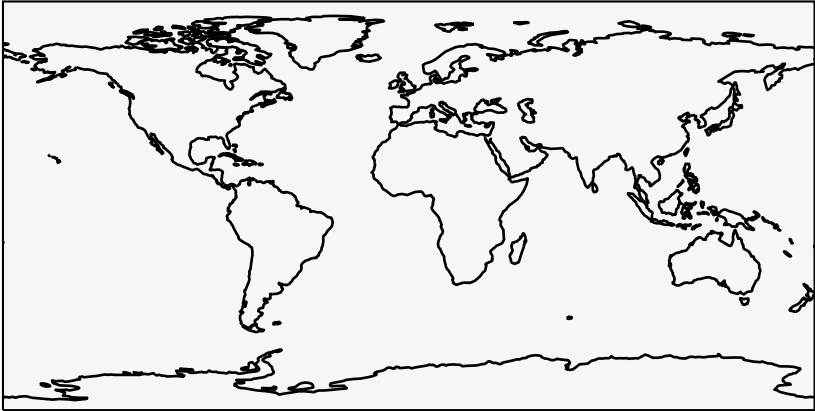
Difference  
Dev - Ref, Restricted Range [5%,95%]



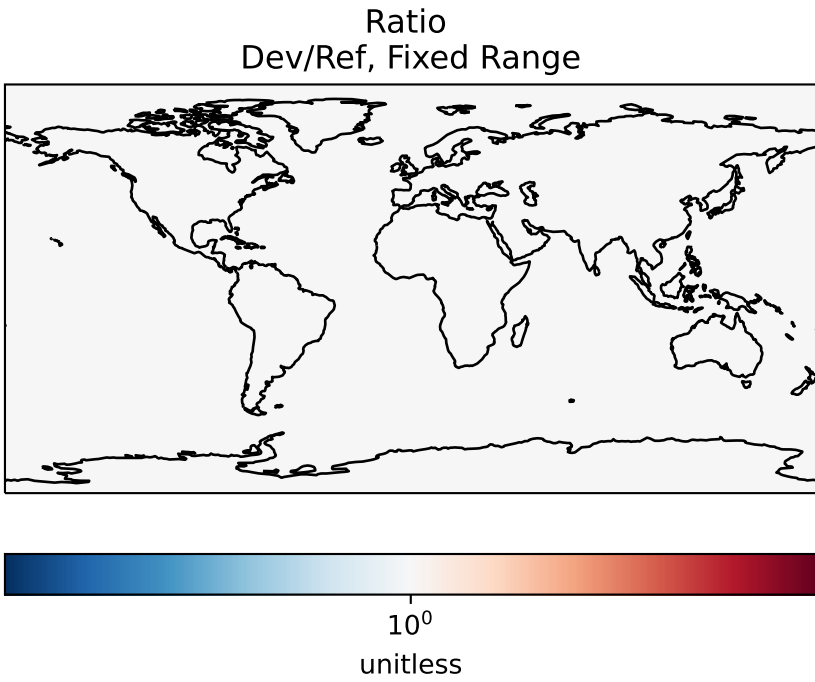
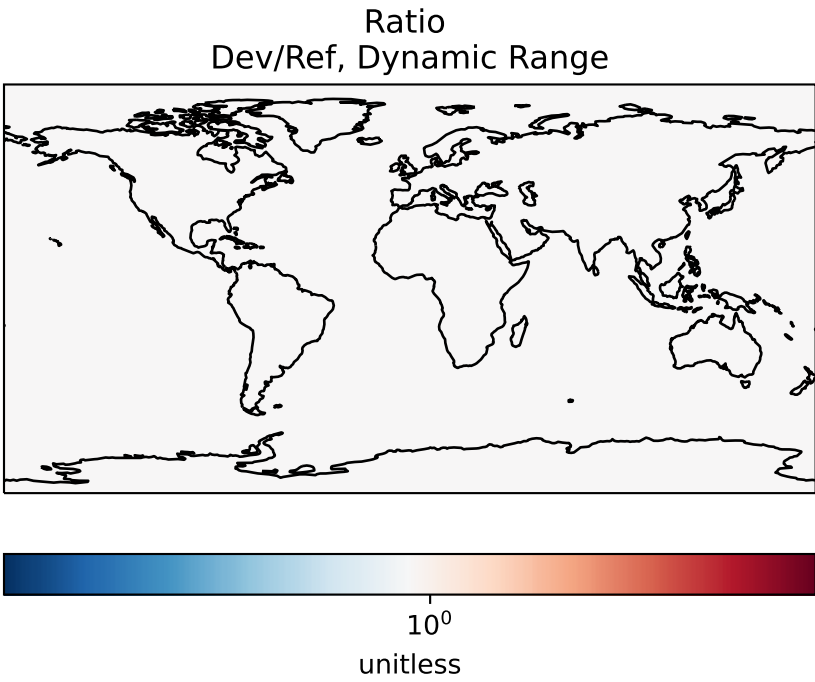
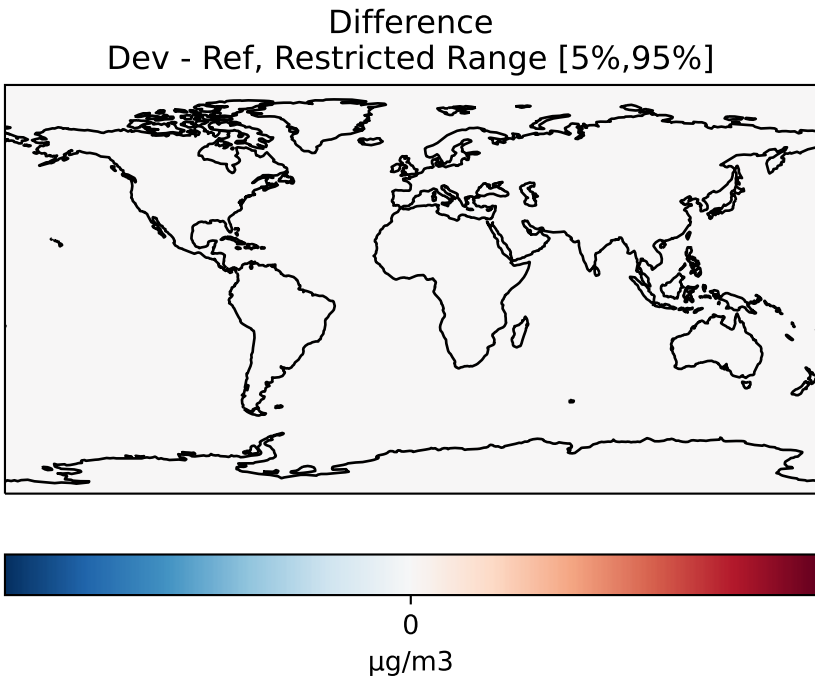
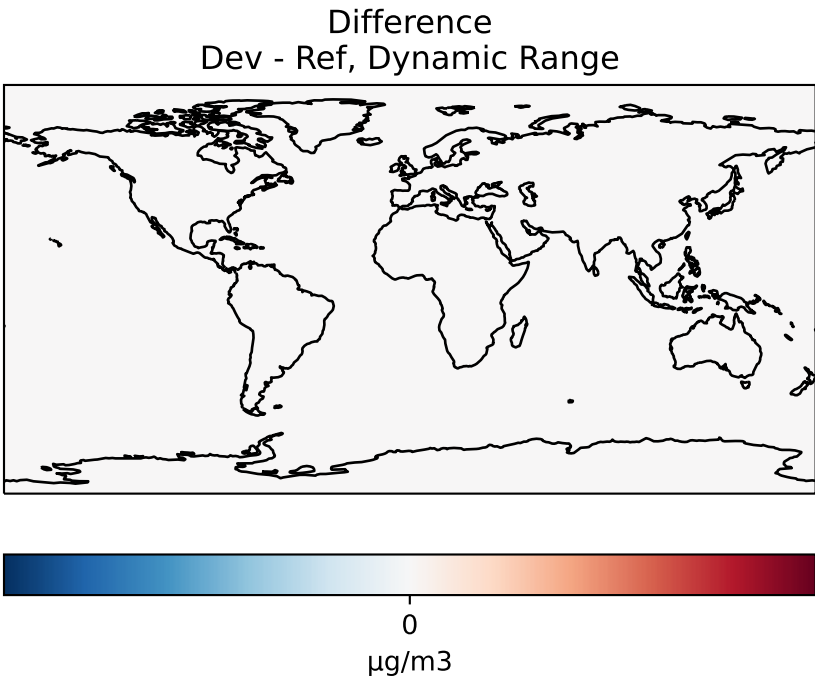
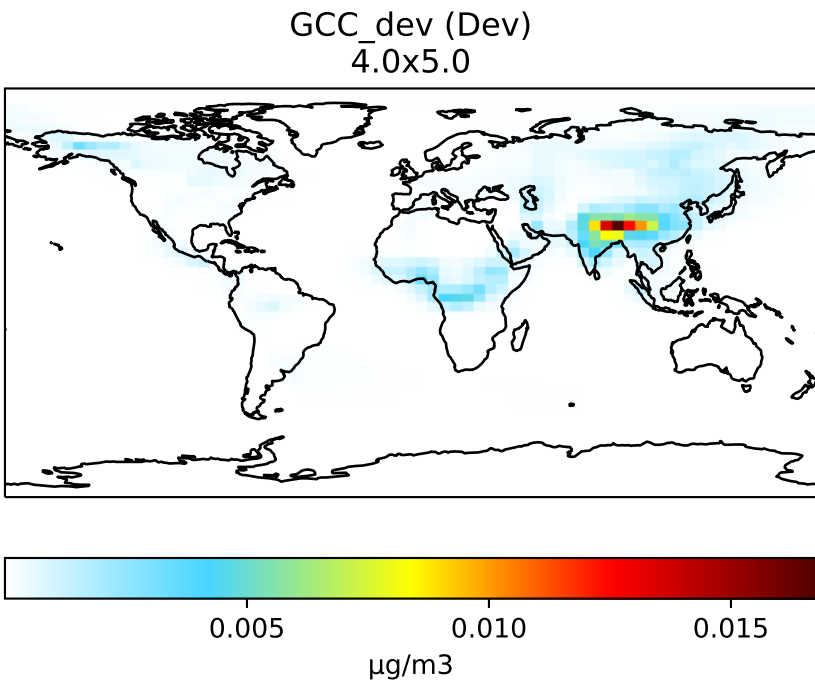
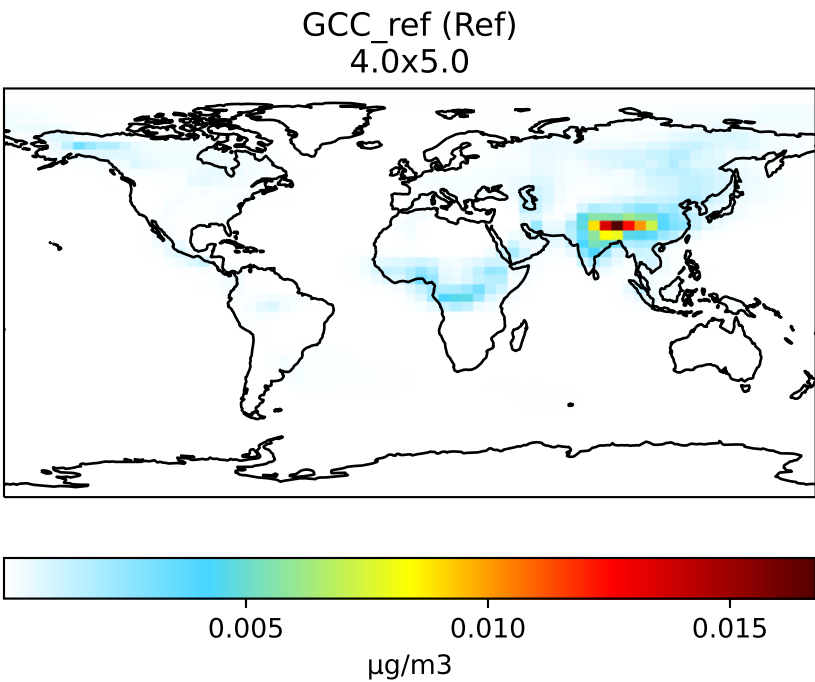
Ratio  
Dev/Ref, Dynamic Range



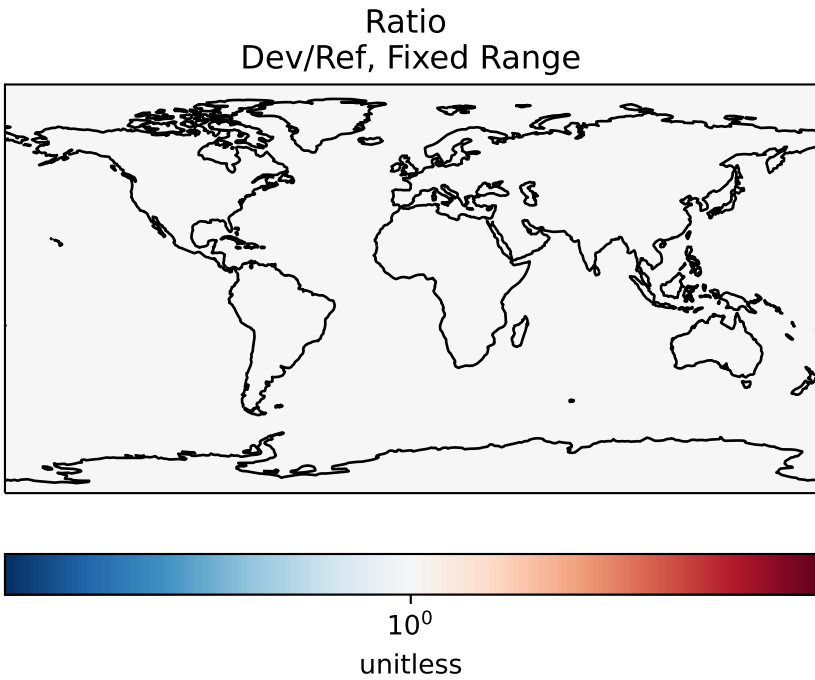
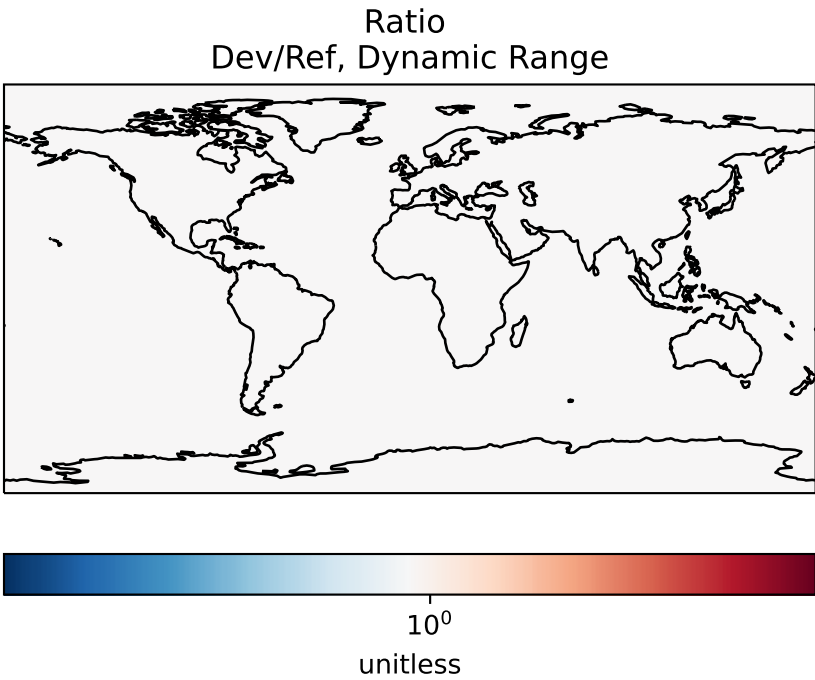
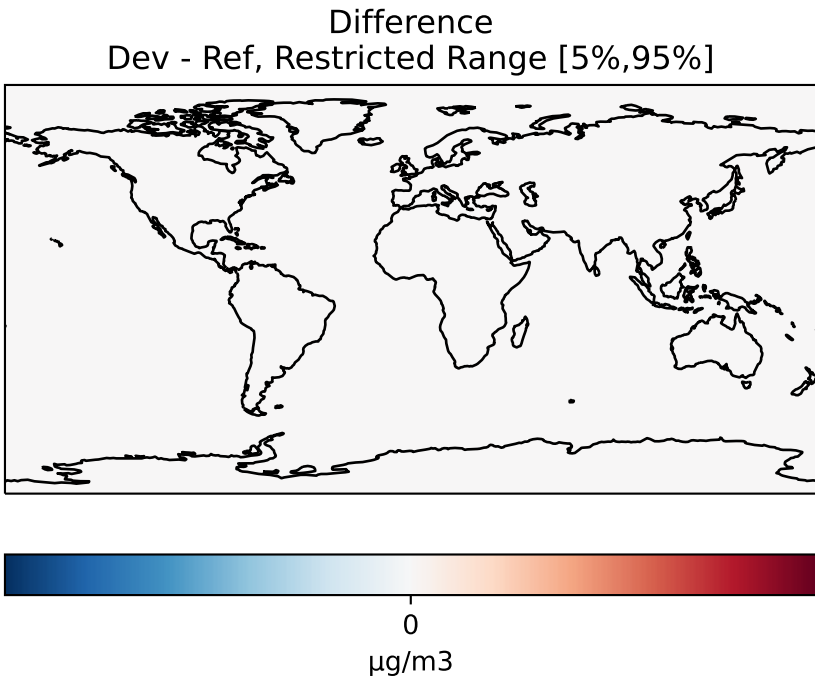
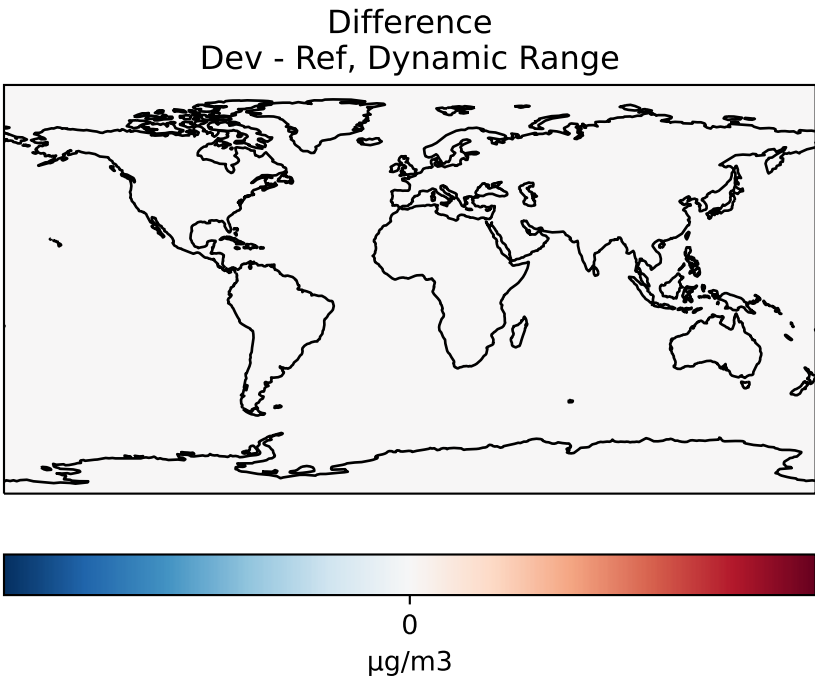
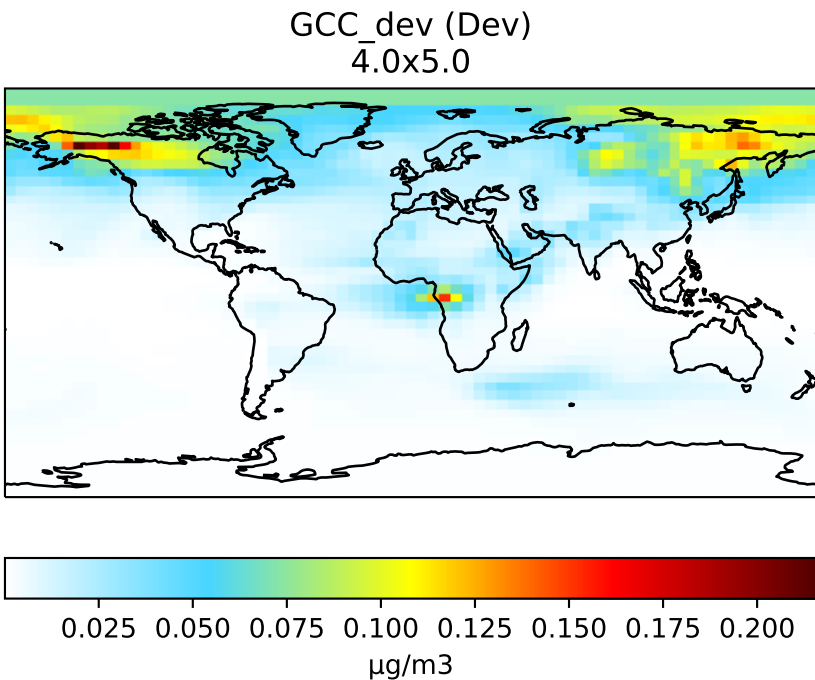
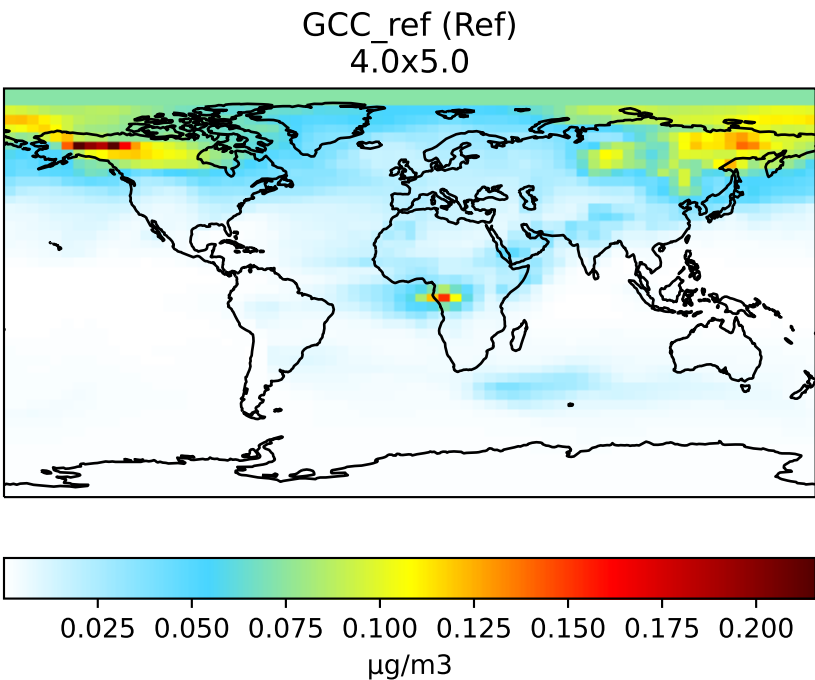
Ratio  
Dev/Ref, Fixed Range



# SpeciesConcVW\_BCPO

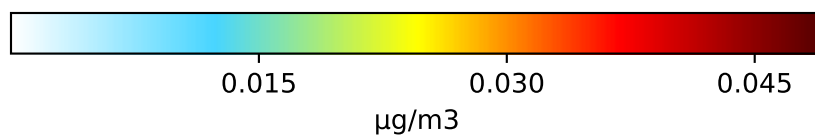
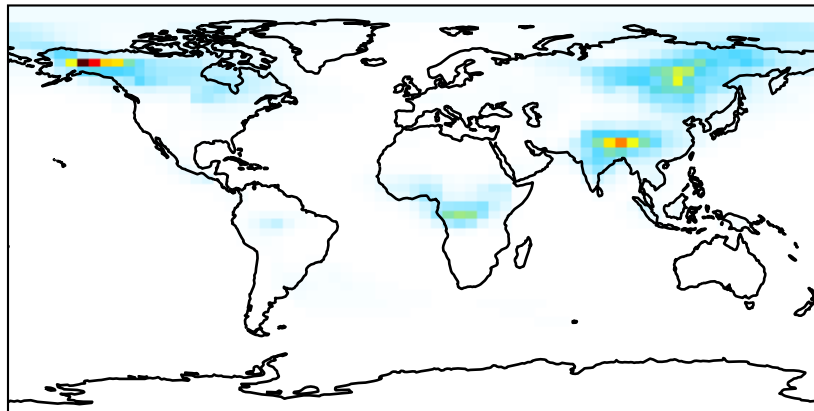


# SpeciesConcVV\_OCPI

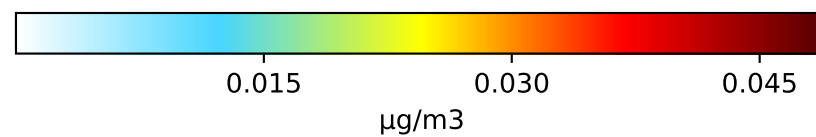
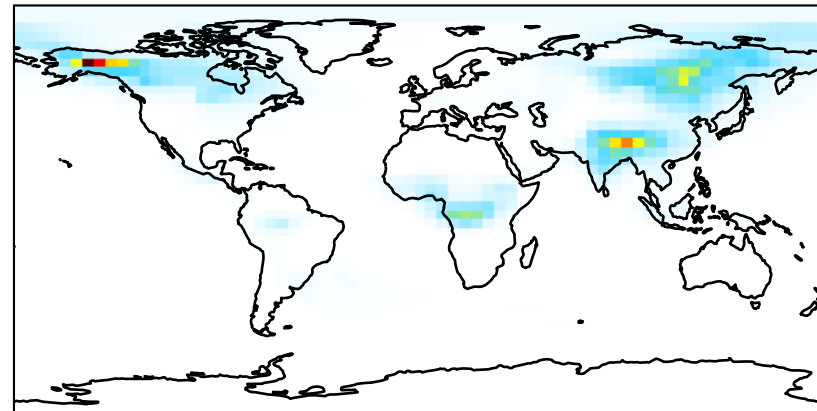


# SpeciesConcVV\_OCPO

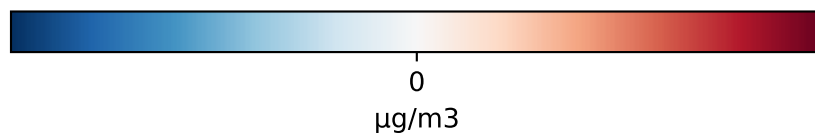
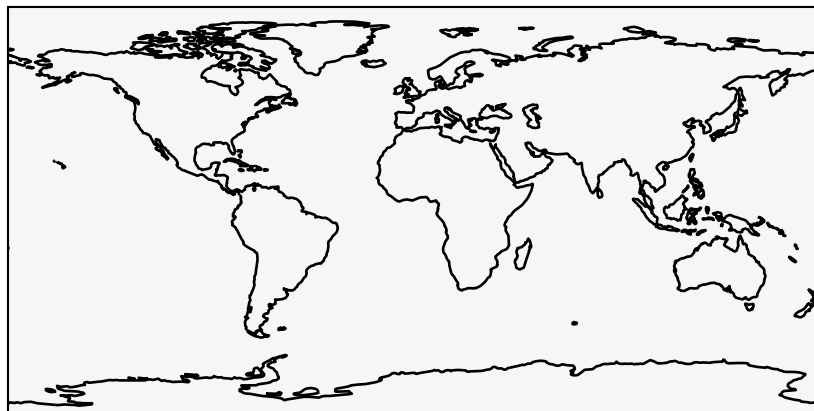
GCC\_ref (Ref)  
4.0x5.0



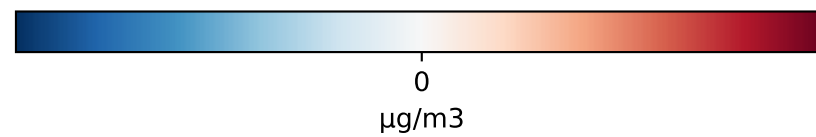
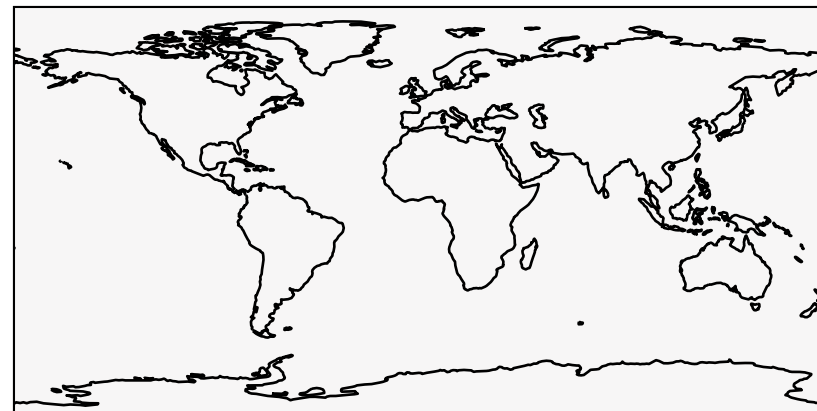
GCC\_dev (Dev)  
4.0x5.0



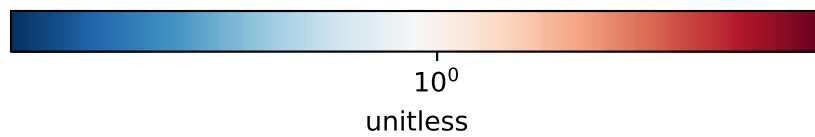
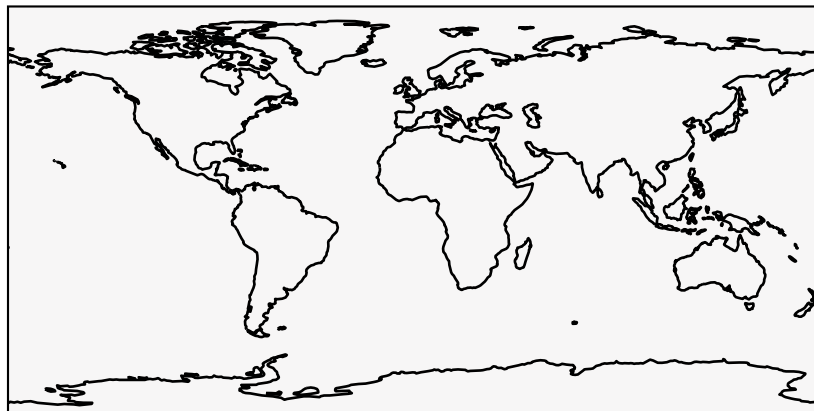
Difference  
Dev - Ref, Dynamic Range



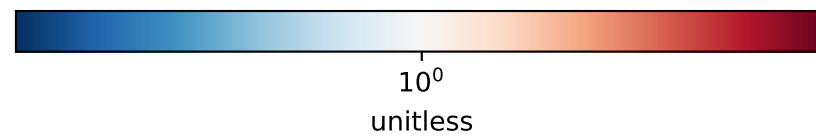
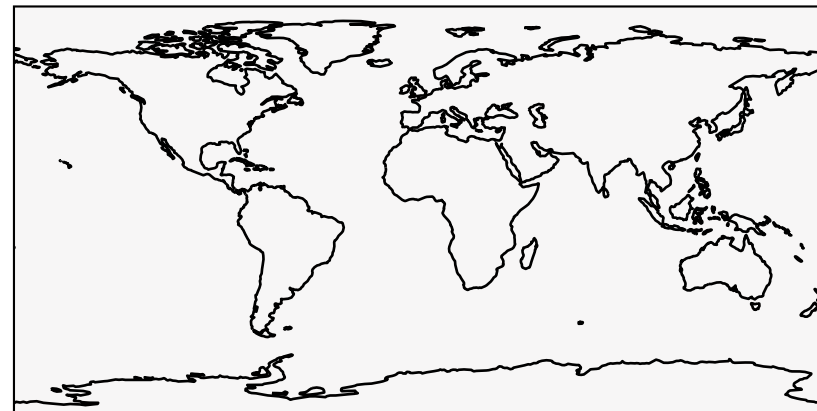
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

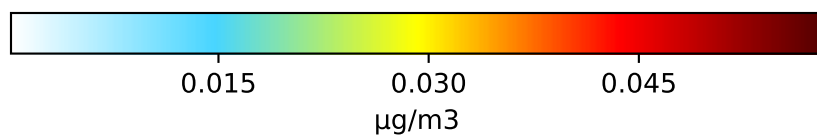
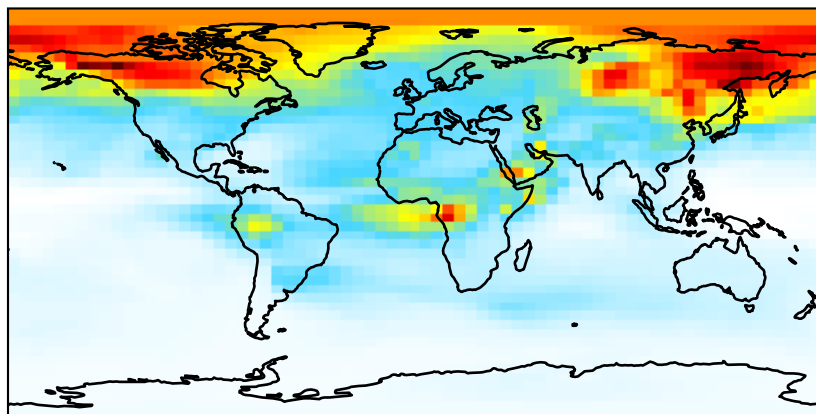


Ratio  
Dev/Ref, Fixed Range

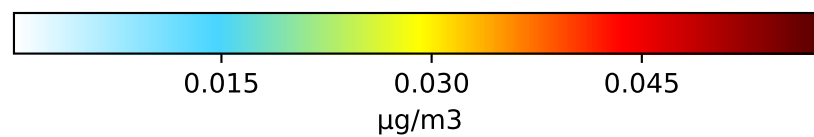
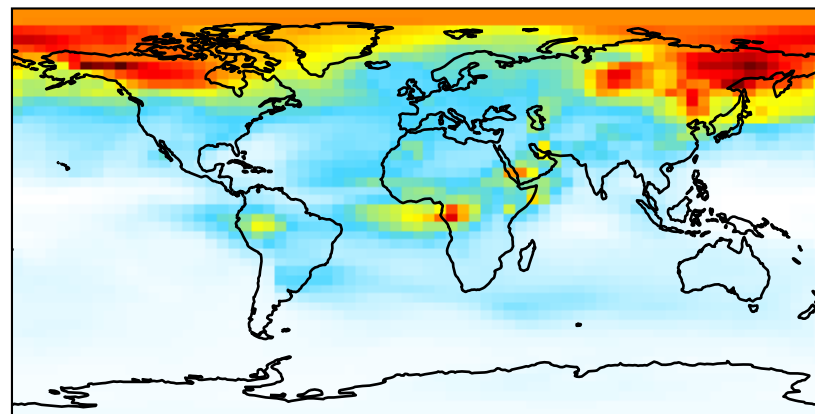


# SpeciesConcVV\_Complex\_SOA

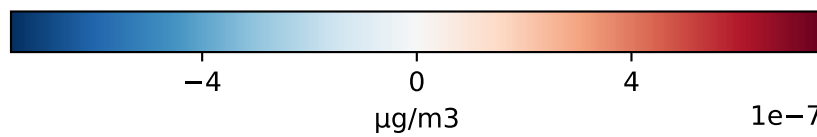
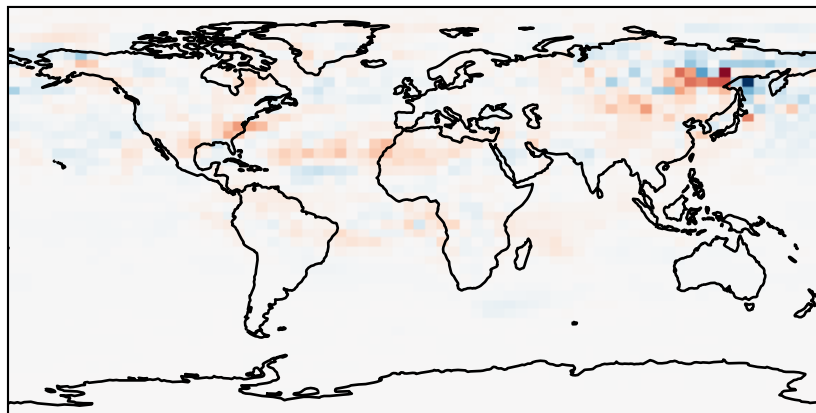
GCC\_ref (Ref)  
4.0x5.0



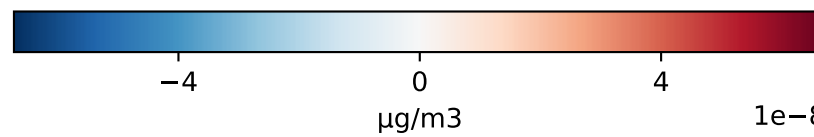
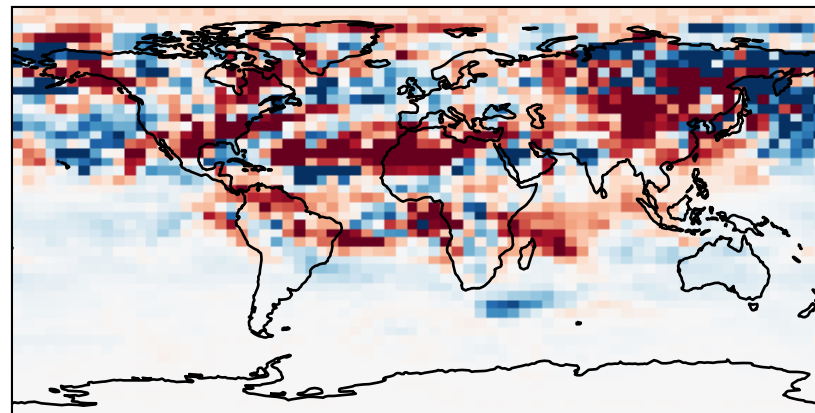
GCC\_dev (Dev)  
4.0x5.0



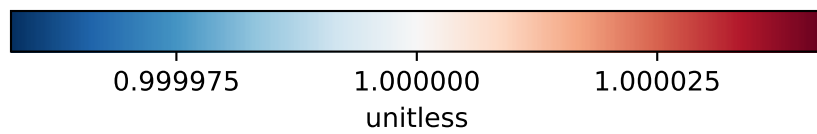
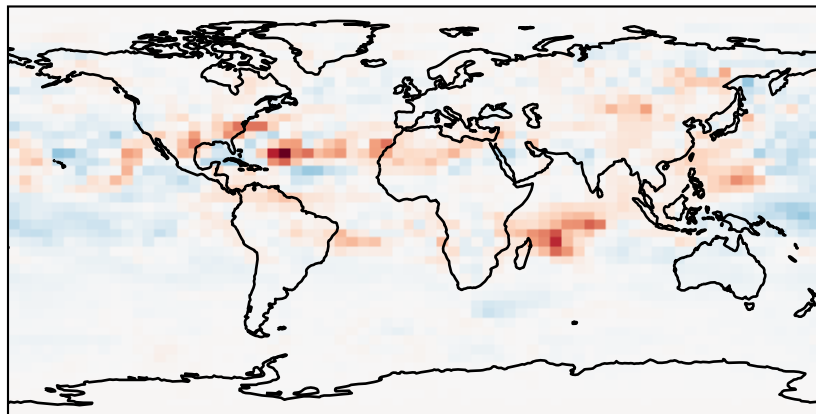
Difference  
Dev - Ref, Dynamic Range



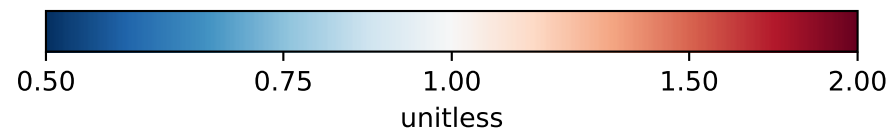
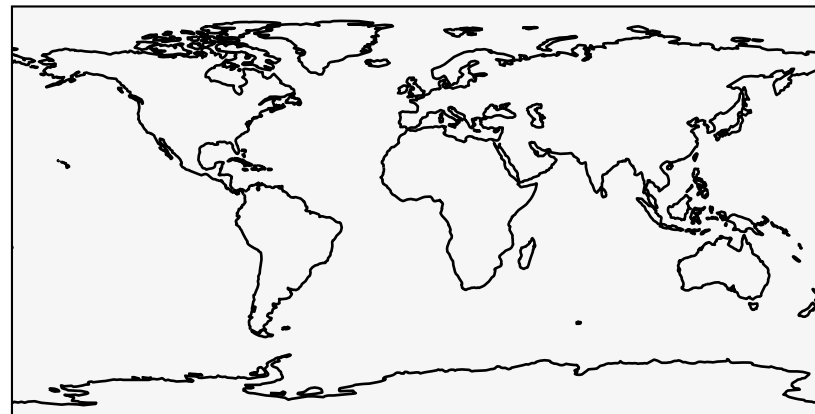
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

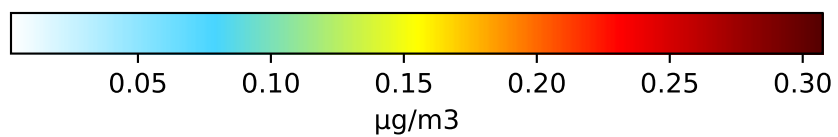
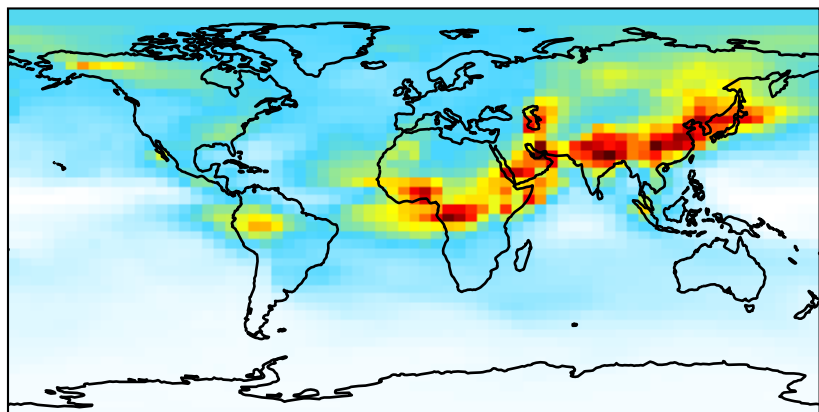


Ratio  
Dev/Ref, Fixed Range

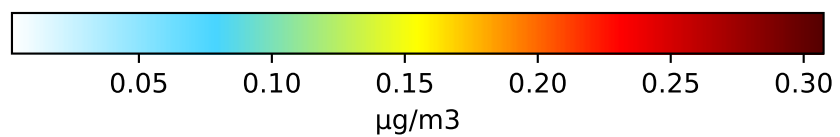
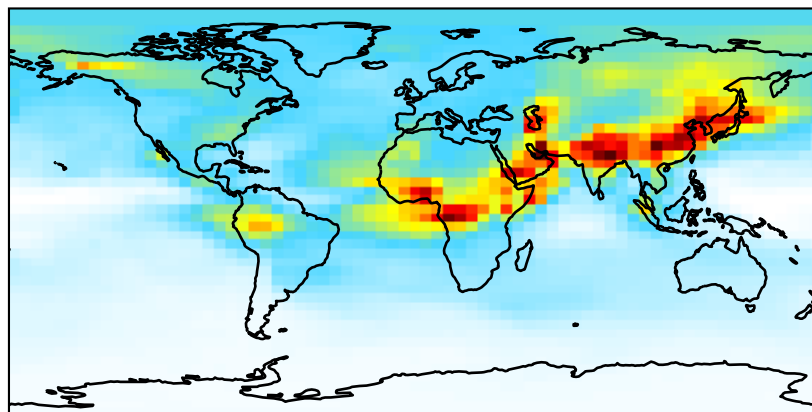


# SpeciesConcVV\_Simple\_SOA

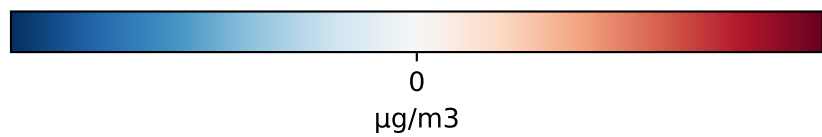
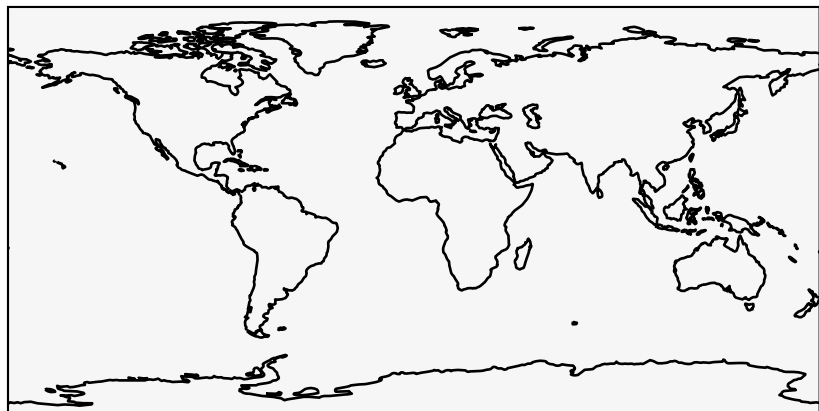
GCC\_ref (Ref)  
4.0x5.0



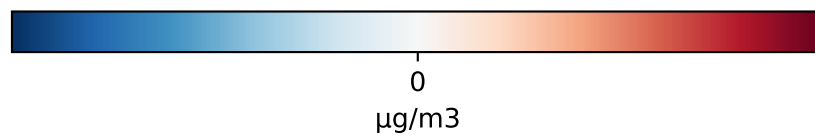
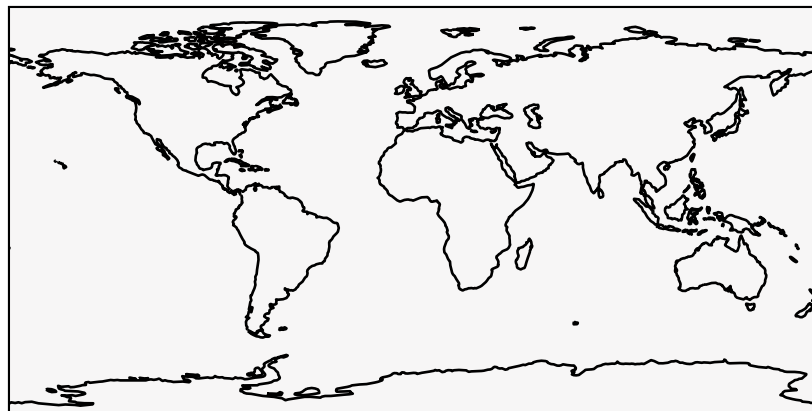
GCC\_dev (Dev)  
4.0x5.0



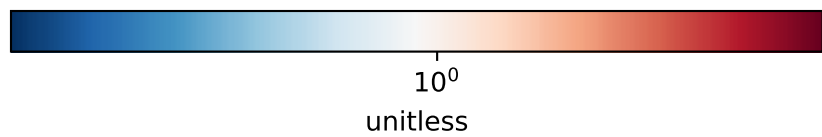
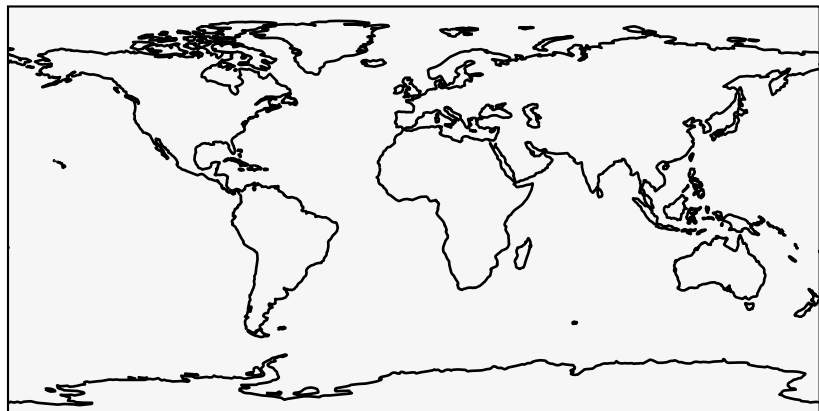
Difference  
Dev - Ref, Dynamic Range



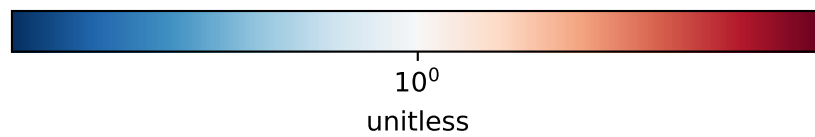
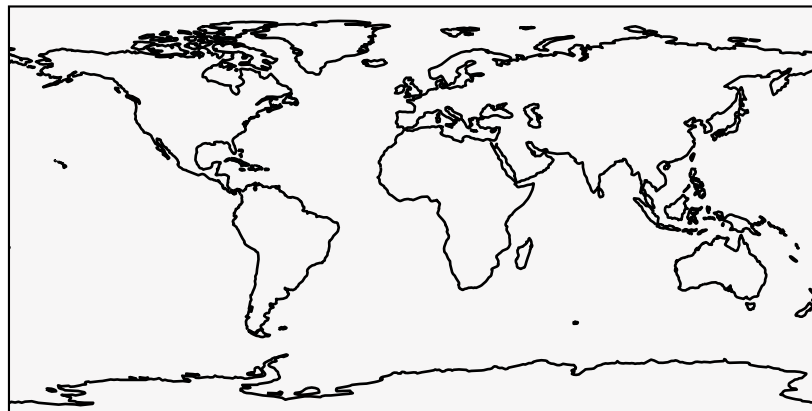
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

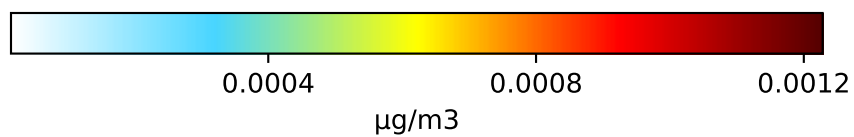
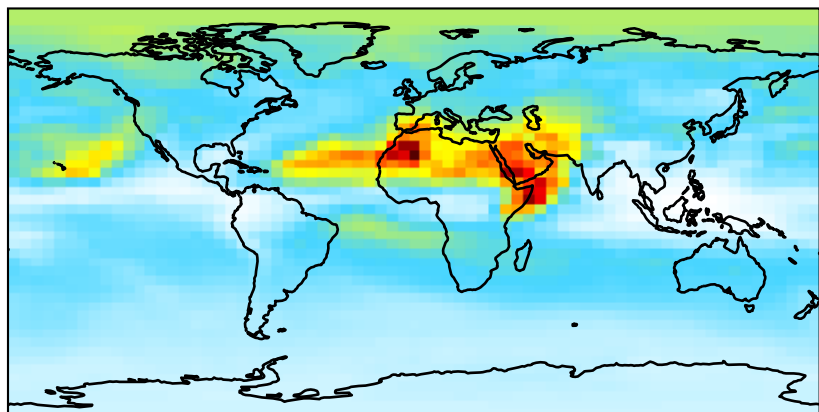


Ratio  
Dev/Ref, Fixed Range

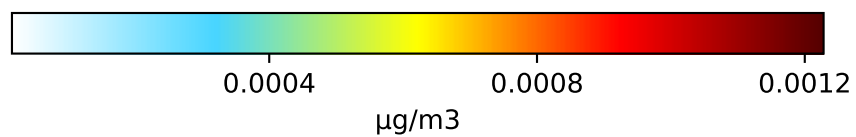
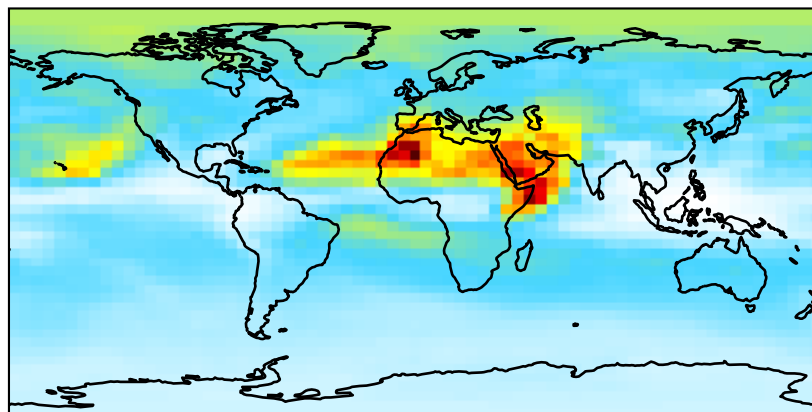


# SpeciesConcVW\_AERI

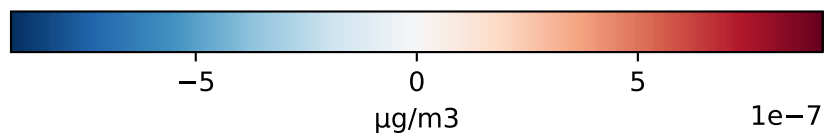
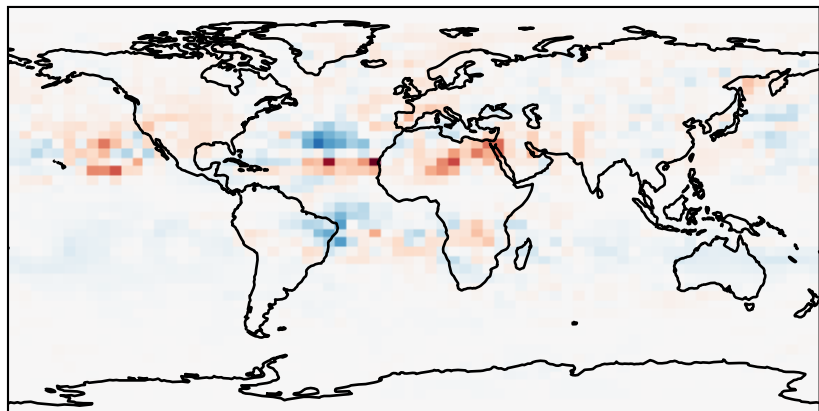
GCC\_ref (Ref)  
4.0x5.0



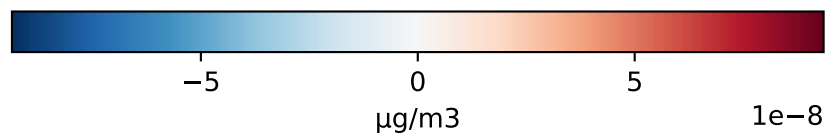
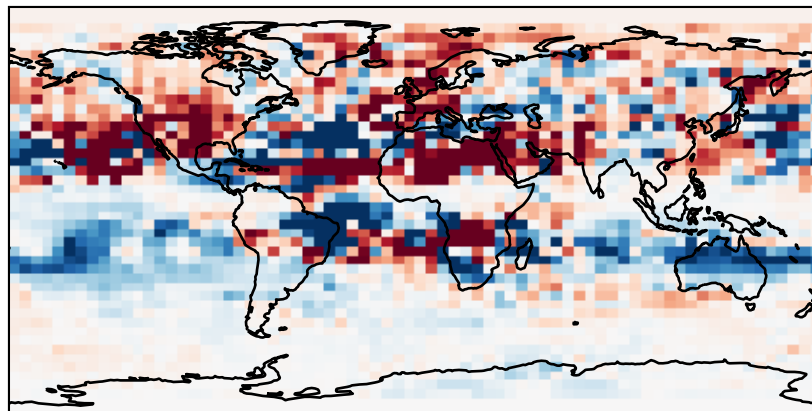
GCC\_dev (Dev)  
4.0x5.0



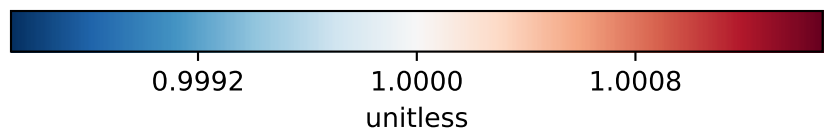
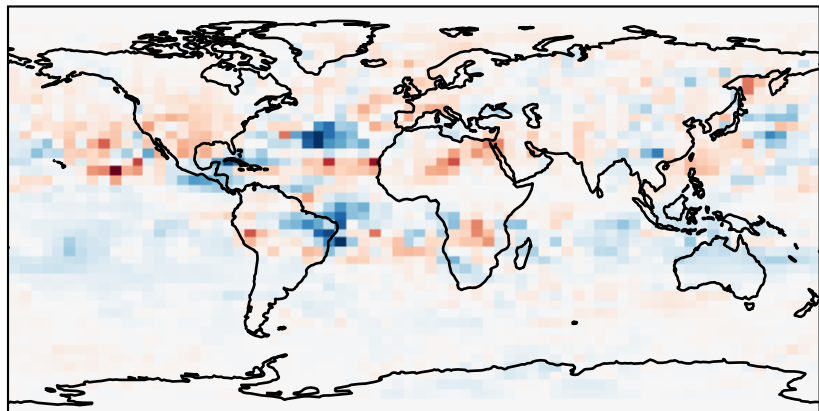
Difference  
Dev - Ref, Dynamic Range



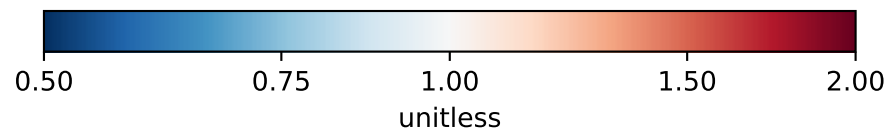
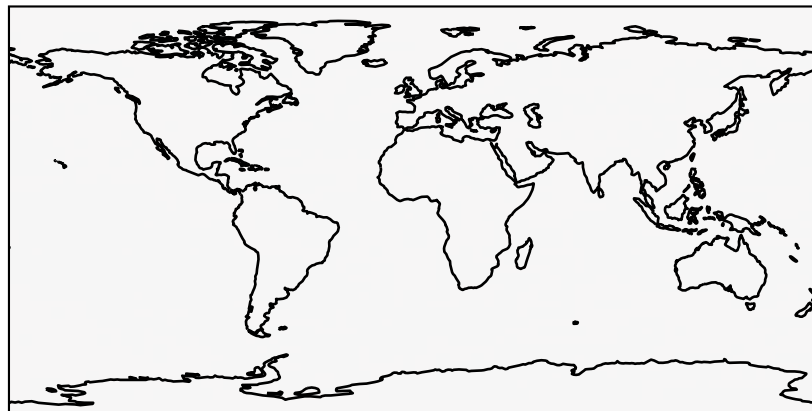
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

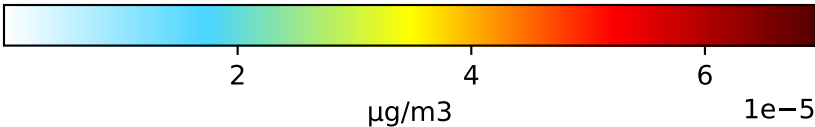
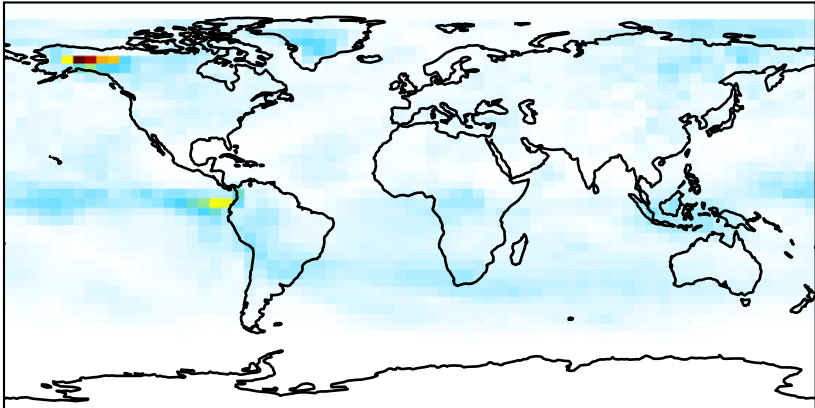


Ratio  
Dev/Ref, Fixed Range

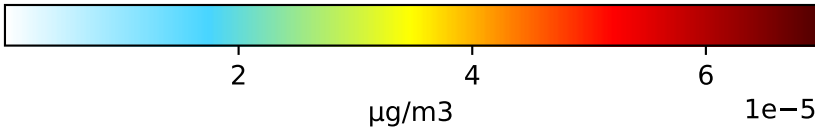
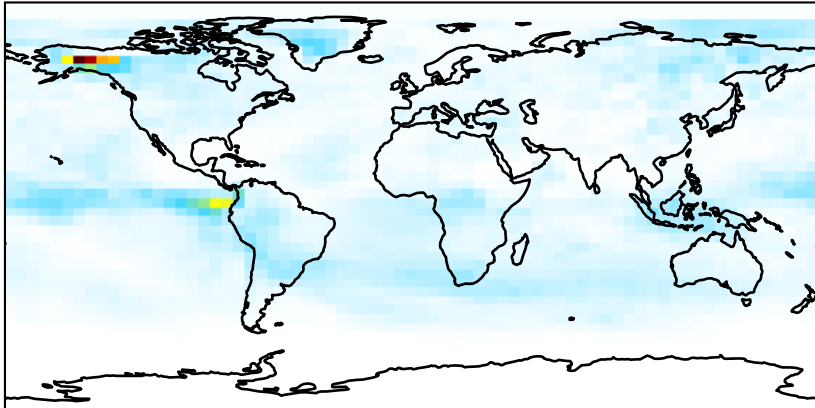


# SpeciesConcVW\_BrSALA

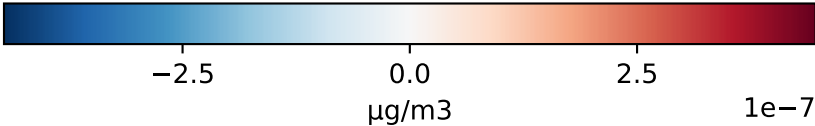
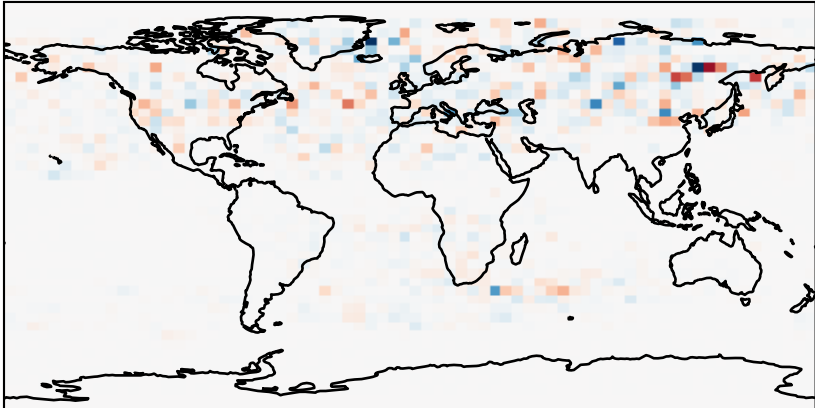
GCC\_ref (Ref)  
4.0x5.0



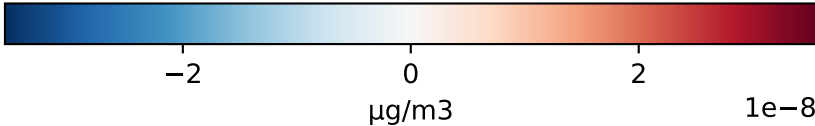
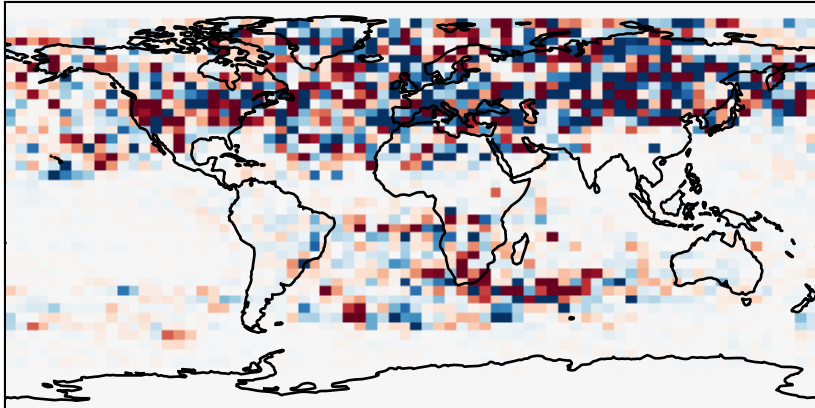
GCC\_dev (Dev)  
4.0x5.0



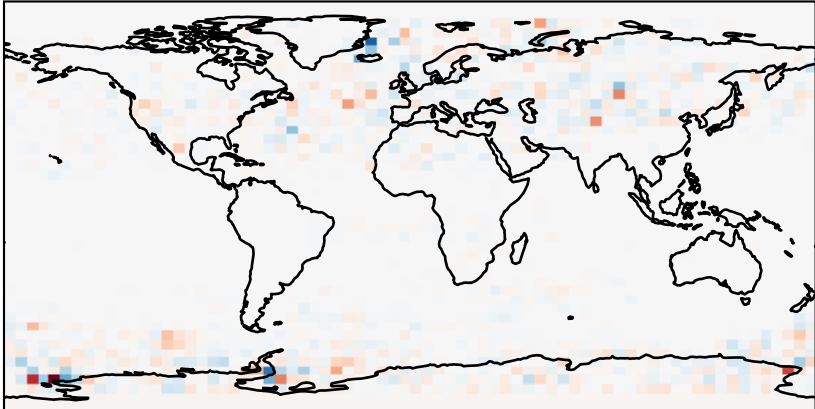
Difference  
Dev - Ref, Dynamic Range



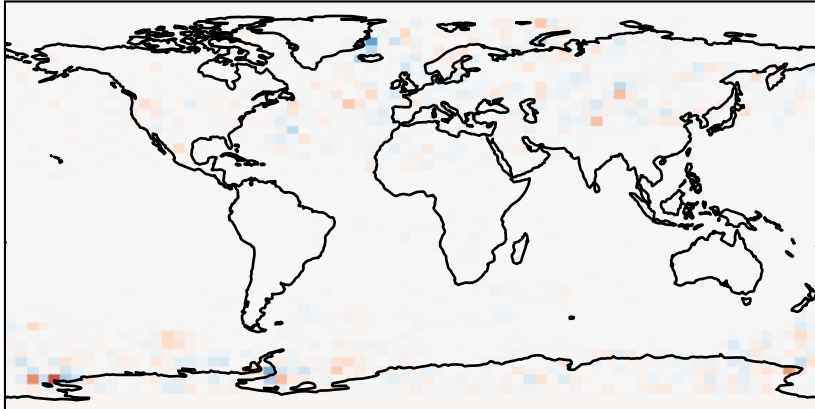
Difference  
Dev - Ref, Restricted Range [5%,95%]



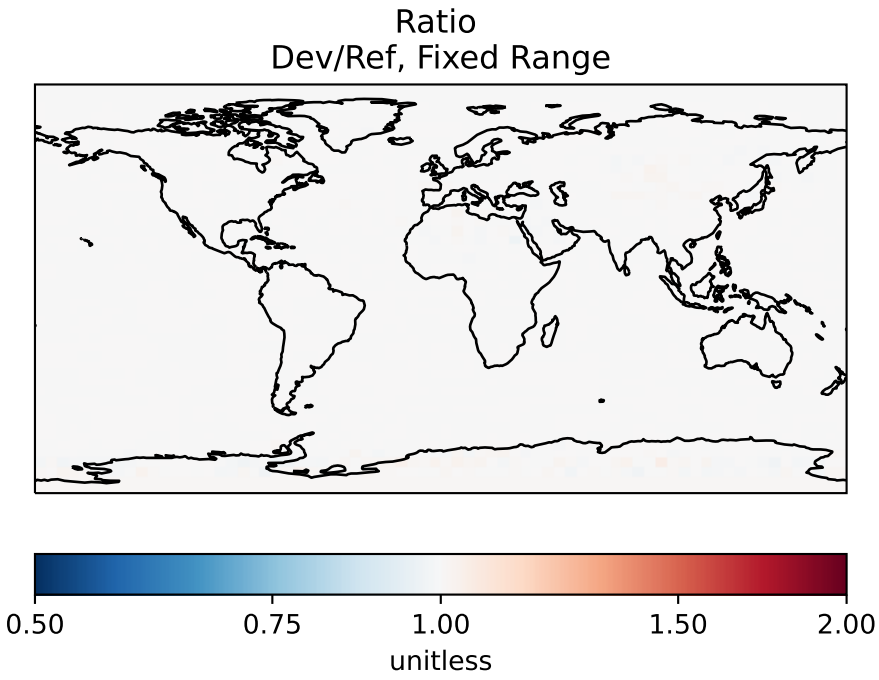
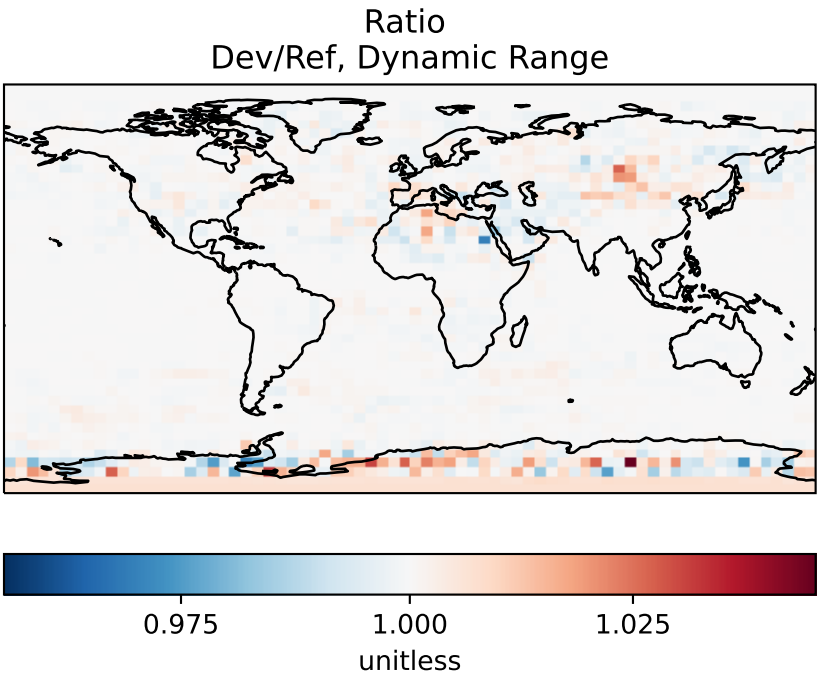
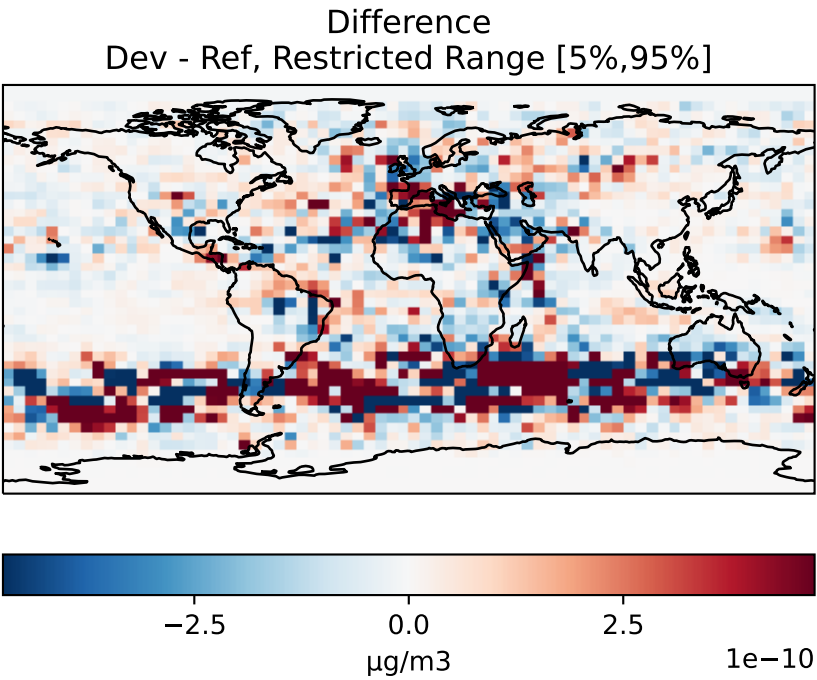
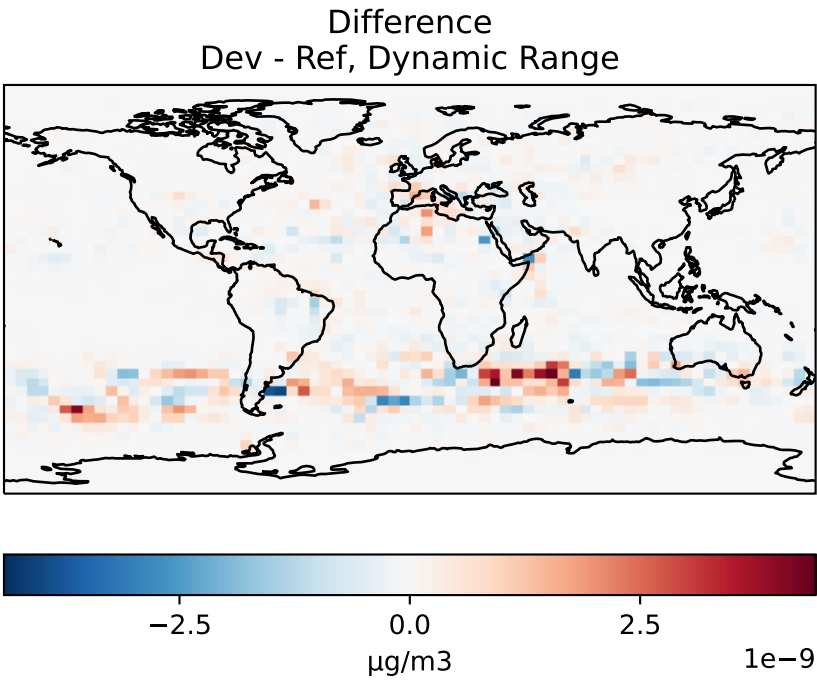
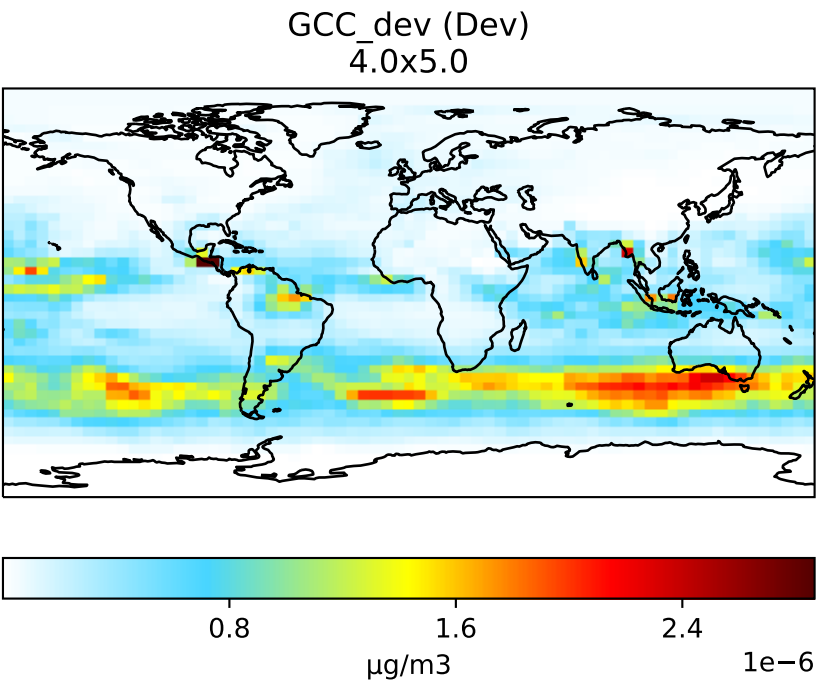
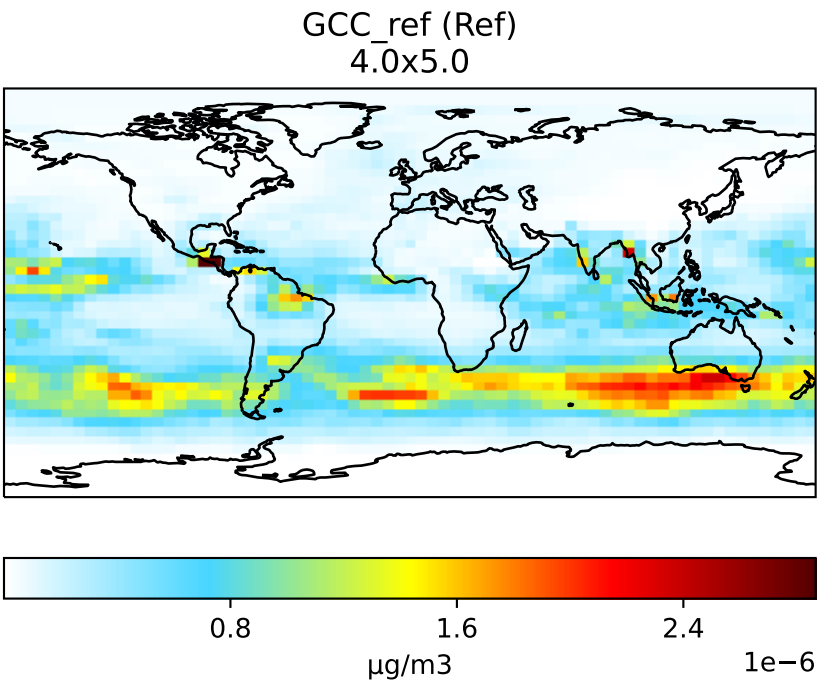
Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

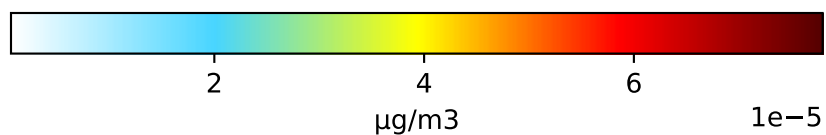
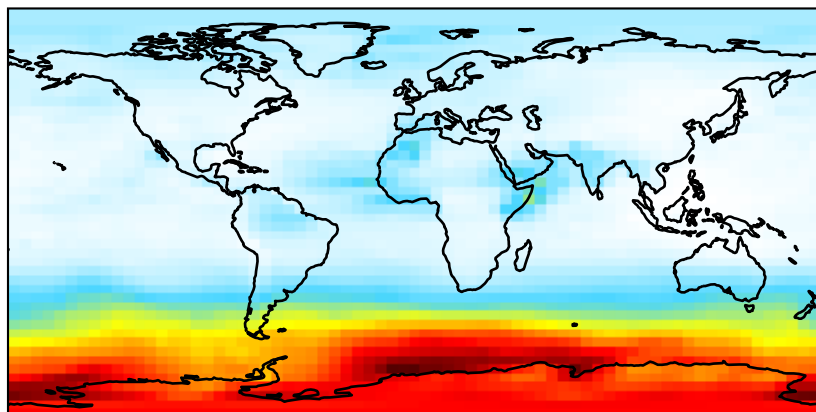


# SpeciesConcVW\_BrSALC

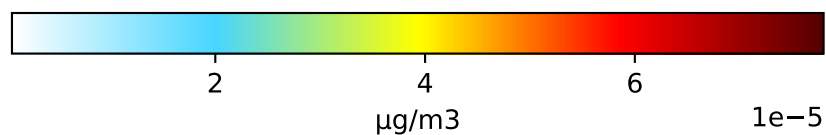
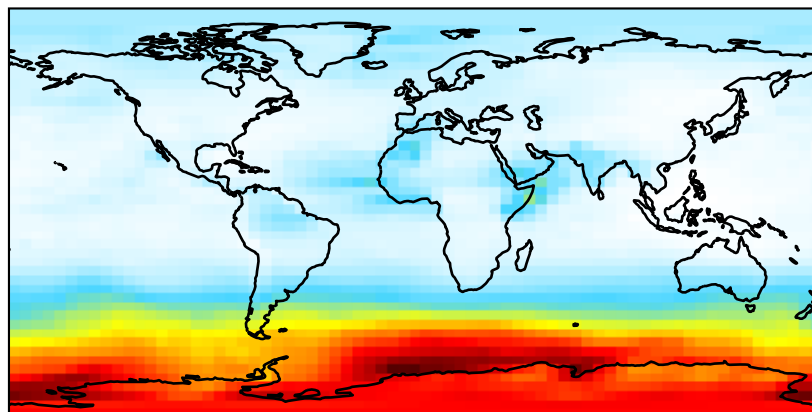


# SpeciesConcVV\_ISALA

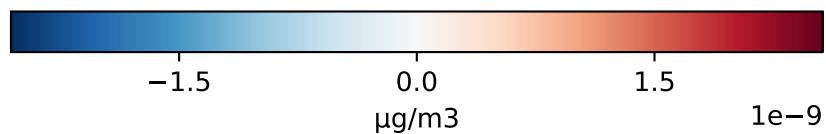
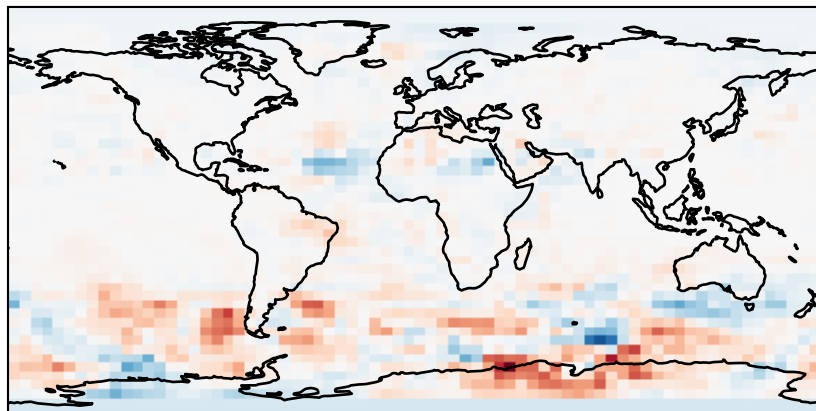
GCC\_ref (Ref)  
4.0x5.0



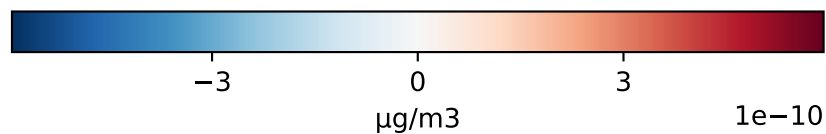
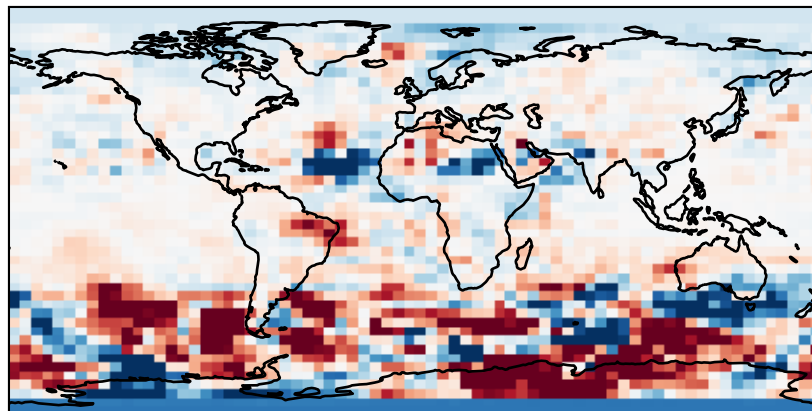
GCC\_dev (Dev)  
4.0x5.0



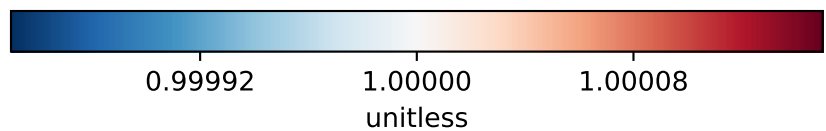
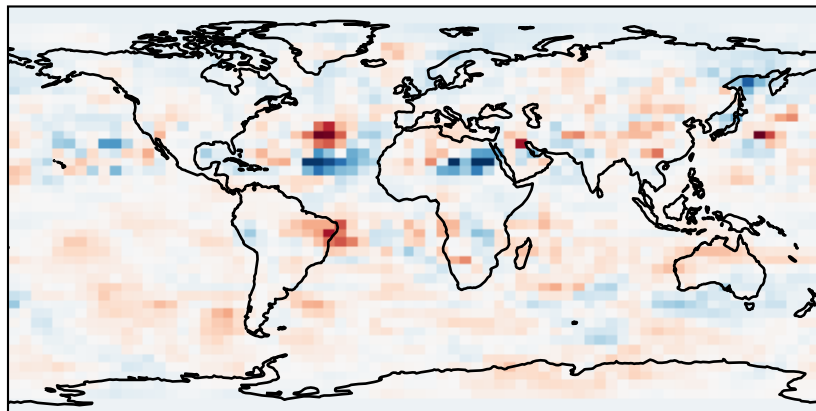
Difference  
Dev - Ref, Dynamic Range



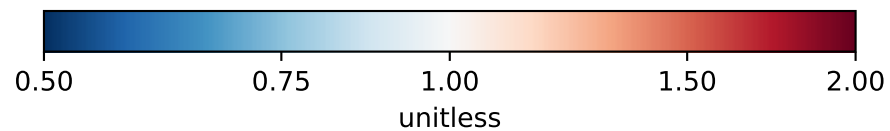
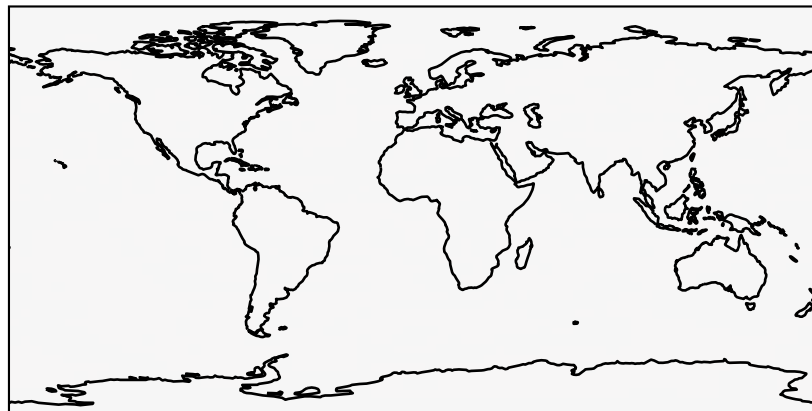
Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range

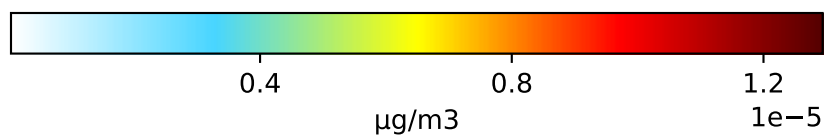
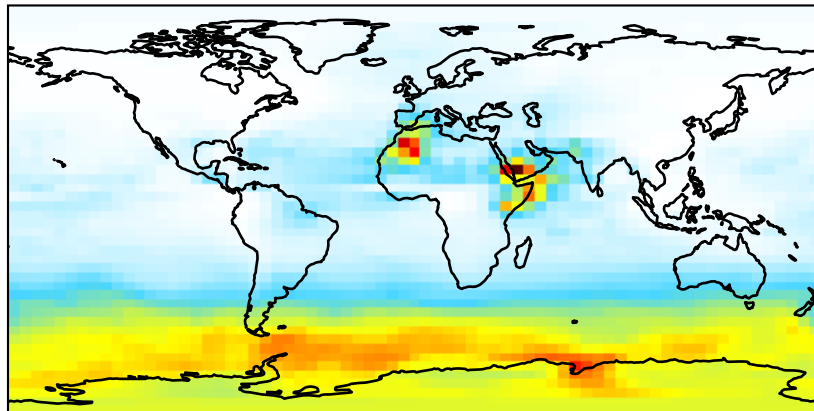


Ratio  
Dev/Ref, Fixed Range

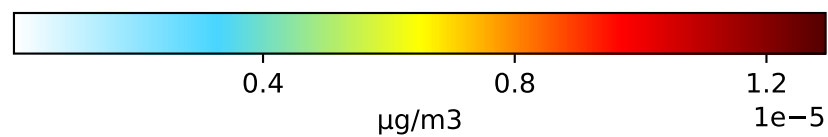
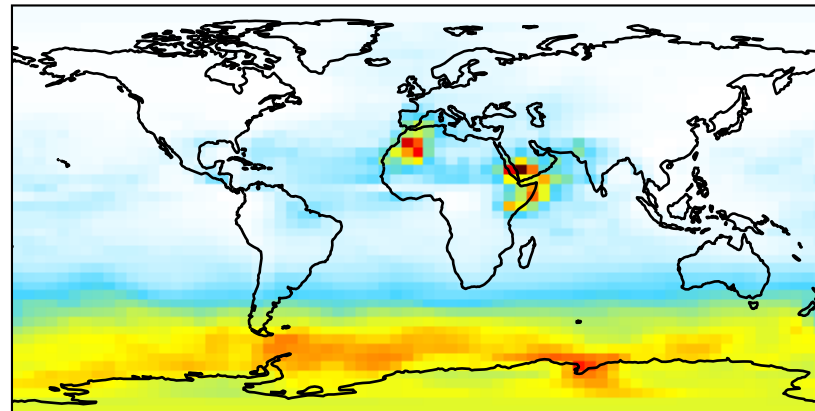


# SpeciesConcVV\_ISALC

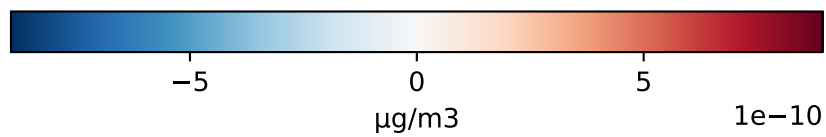
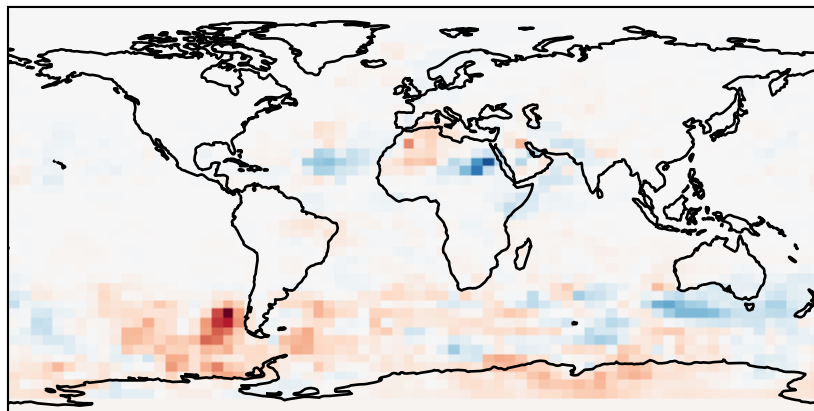
GCC\_ref (Ref)  
4.0x5.0



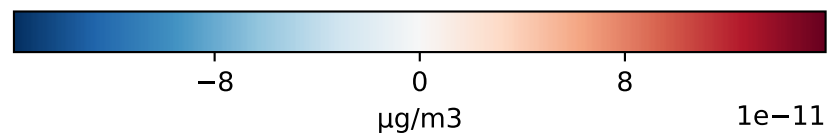
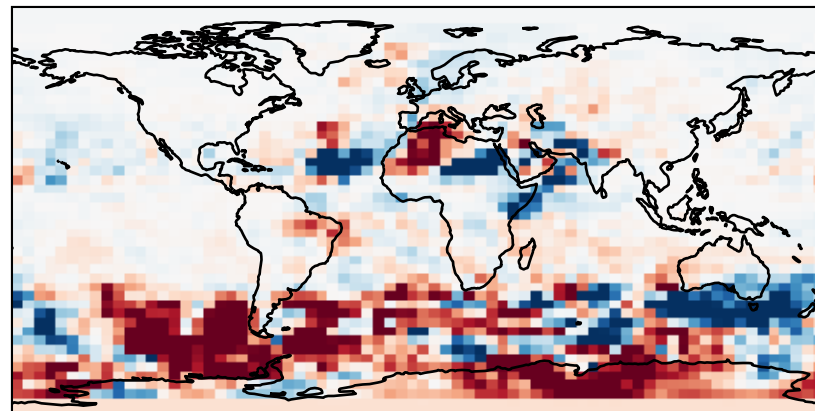
GCC\_dev (Dev)  
4.0x5.0



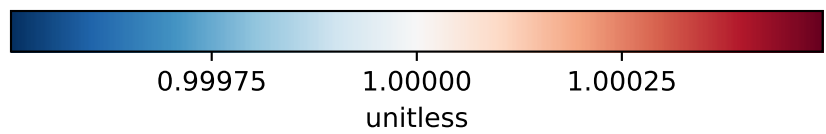
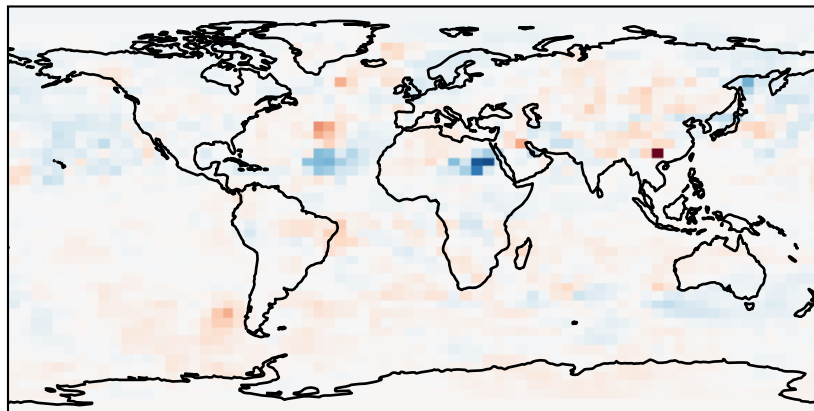
Difference  
Dev - Ref, Dynamic Range



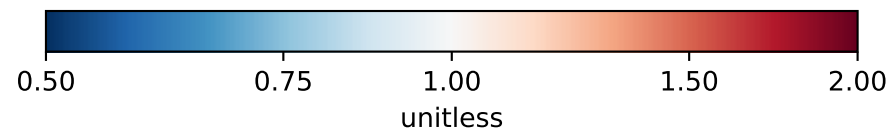
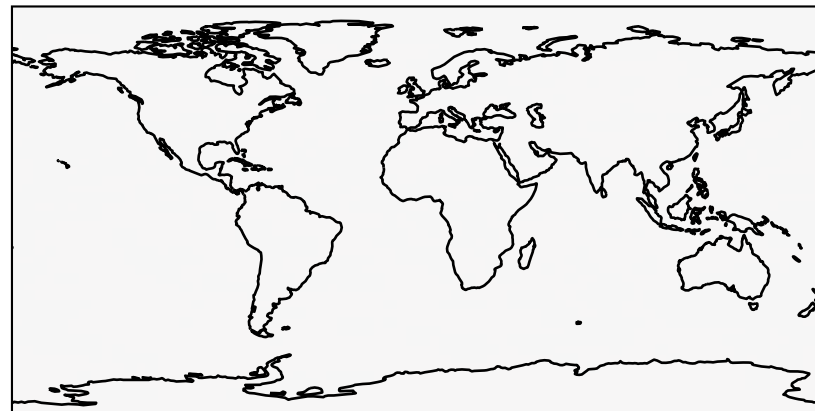
Difference  
Dev - Ref, Restricted Range [5%,95%]



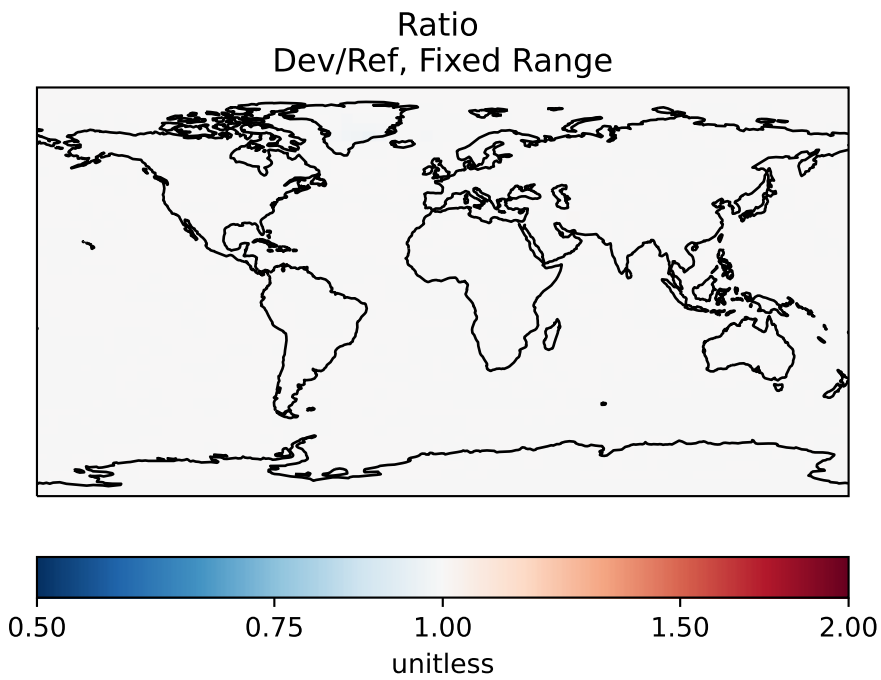
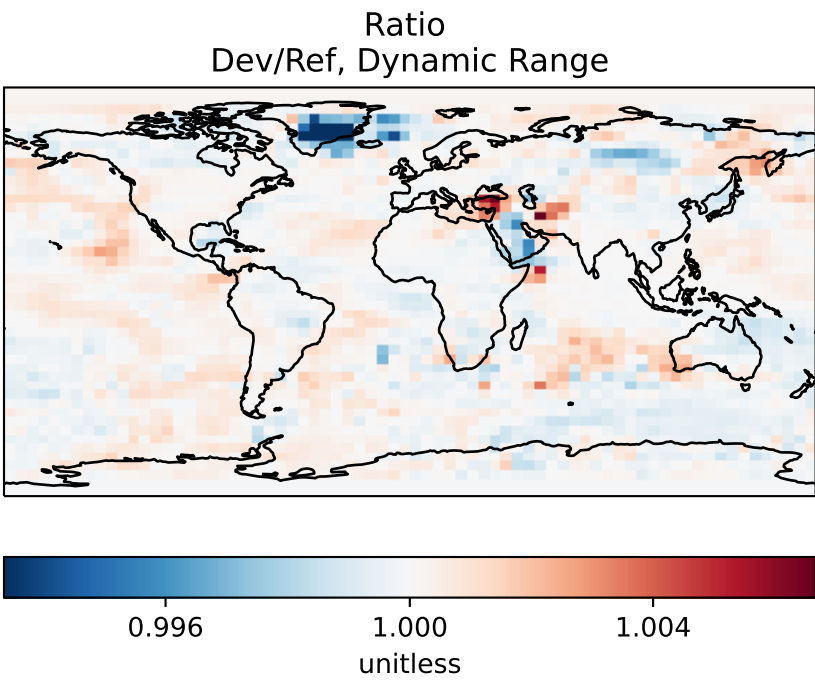
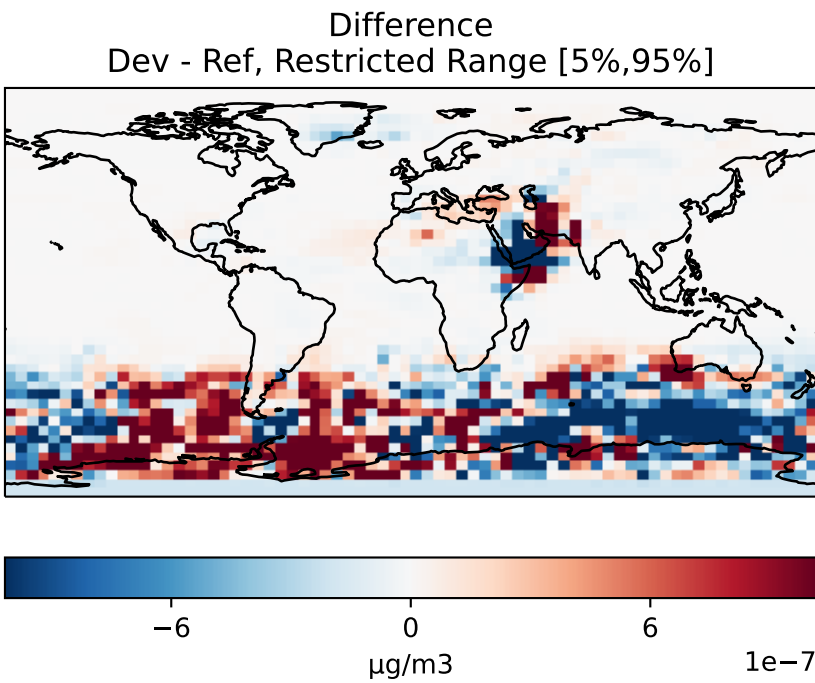
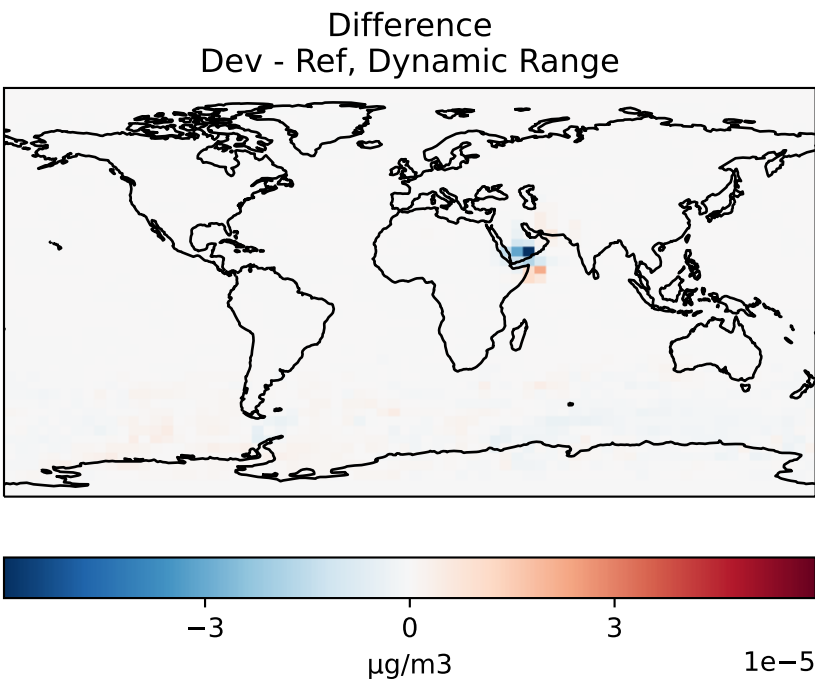
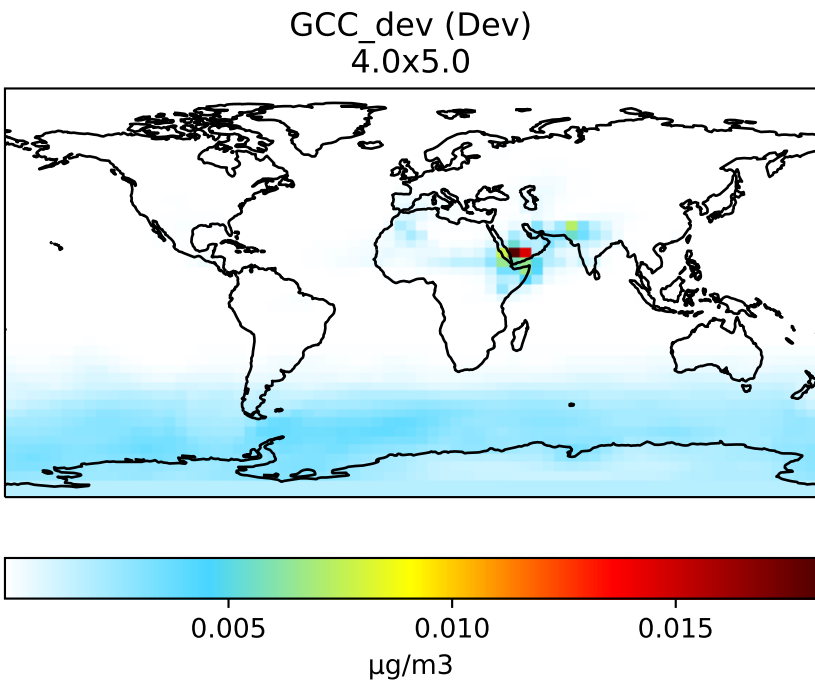
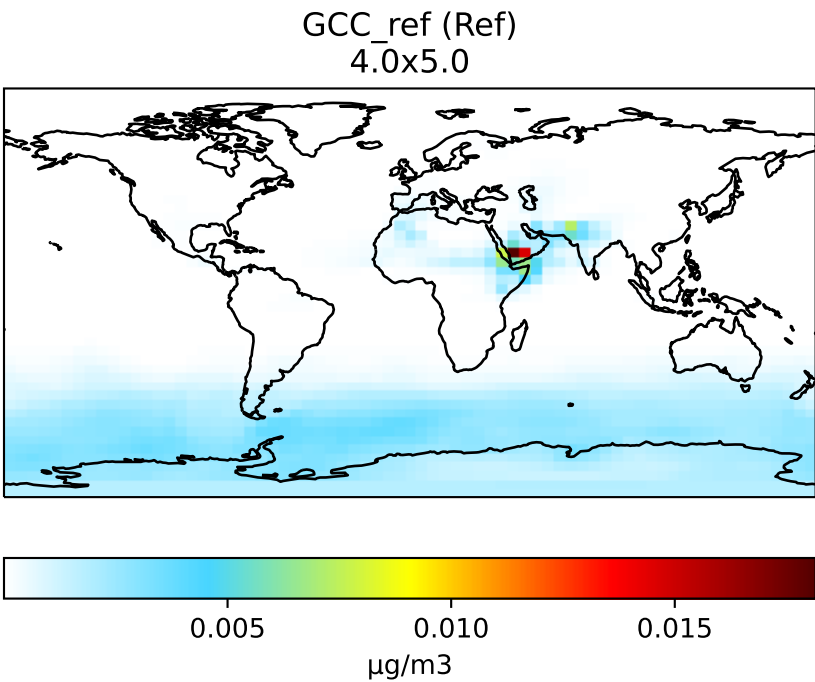
Ratio  
Dev/Ref, Dynamic Range



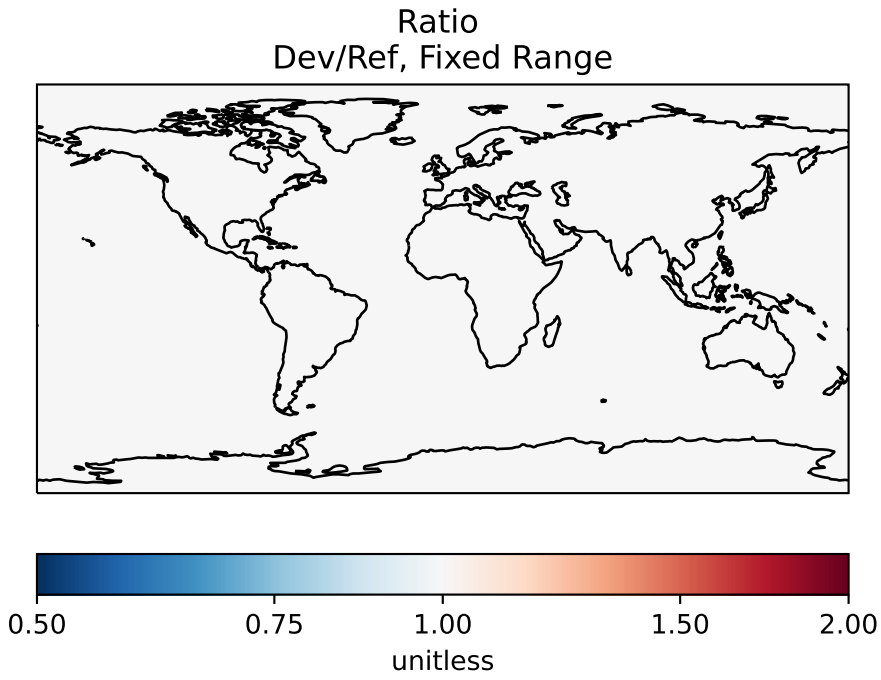
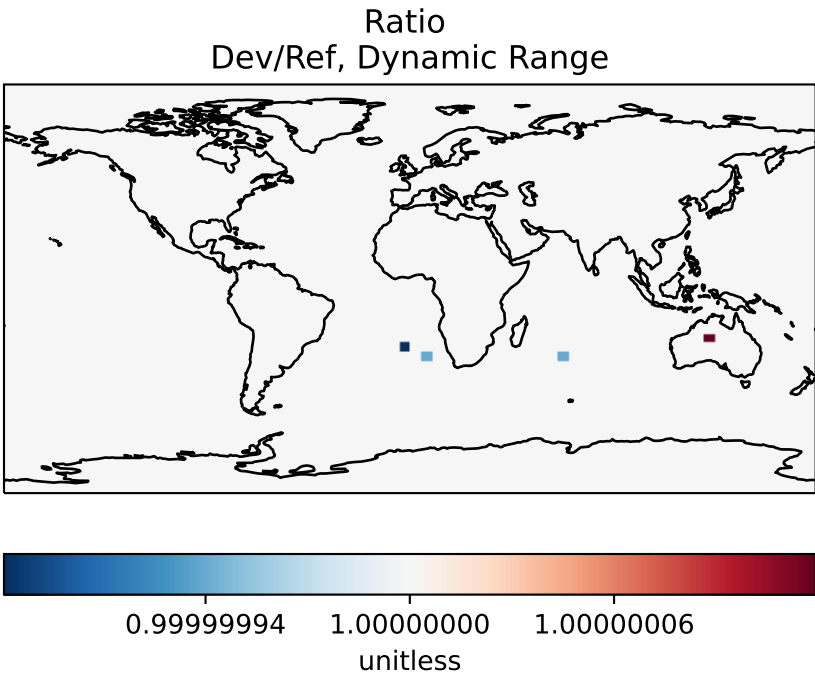
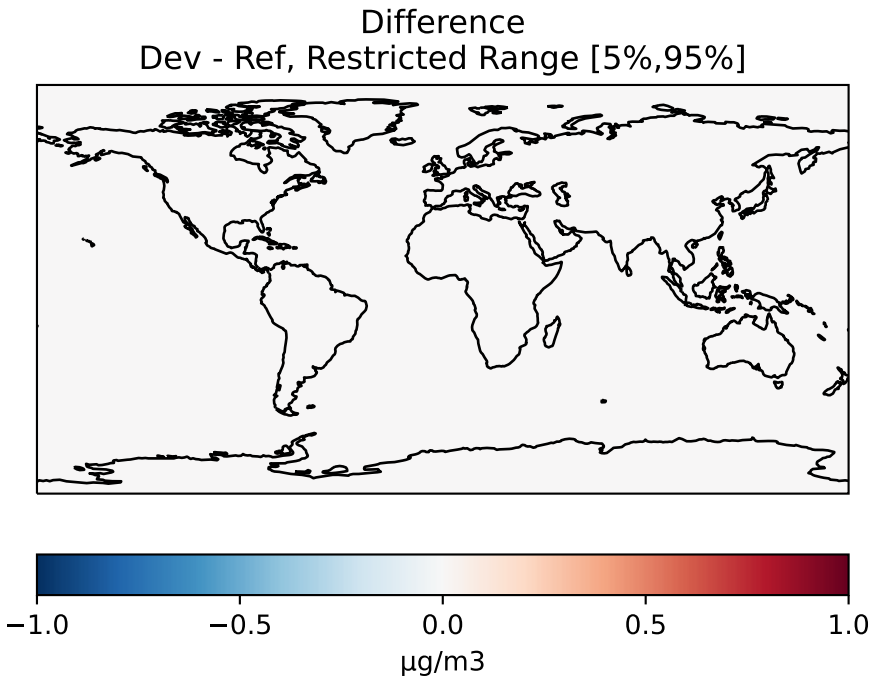
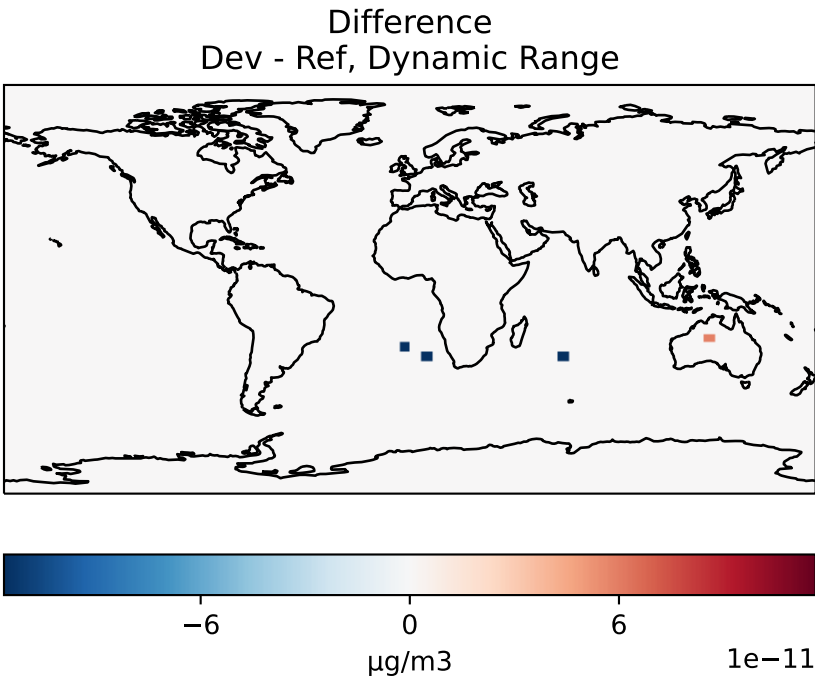
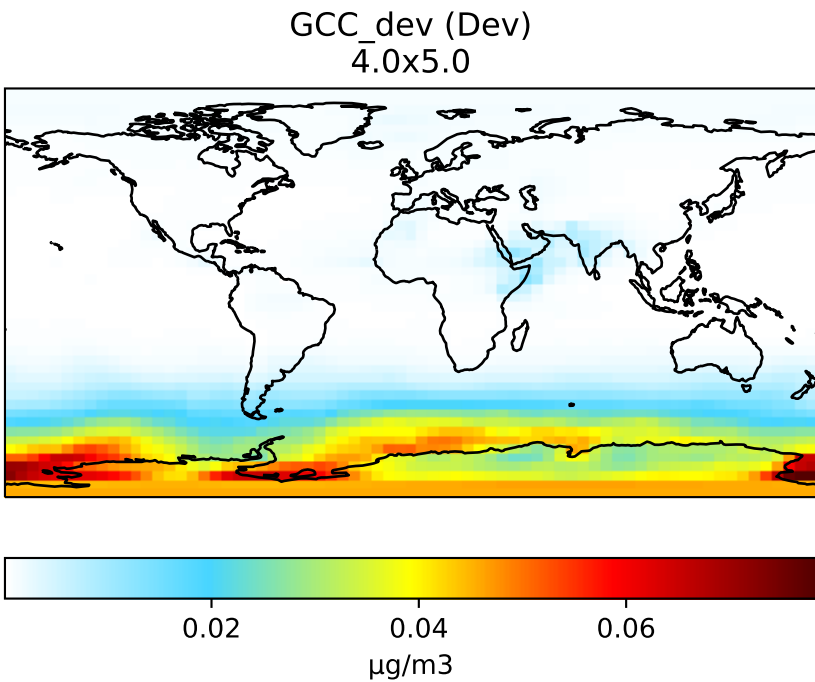
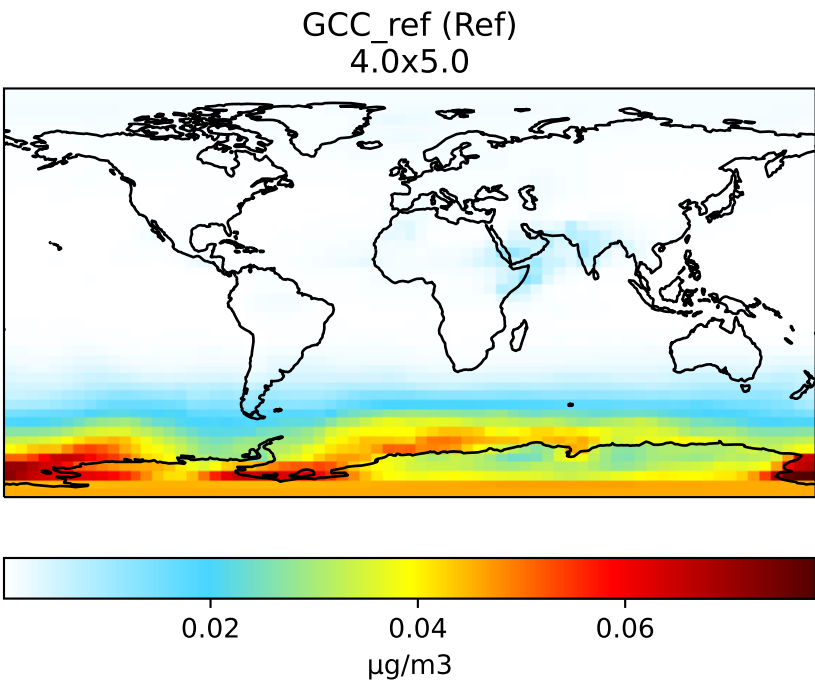
Ratio  
Dev/Ref, Fixed Range



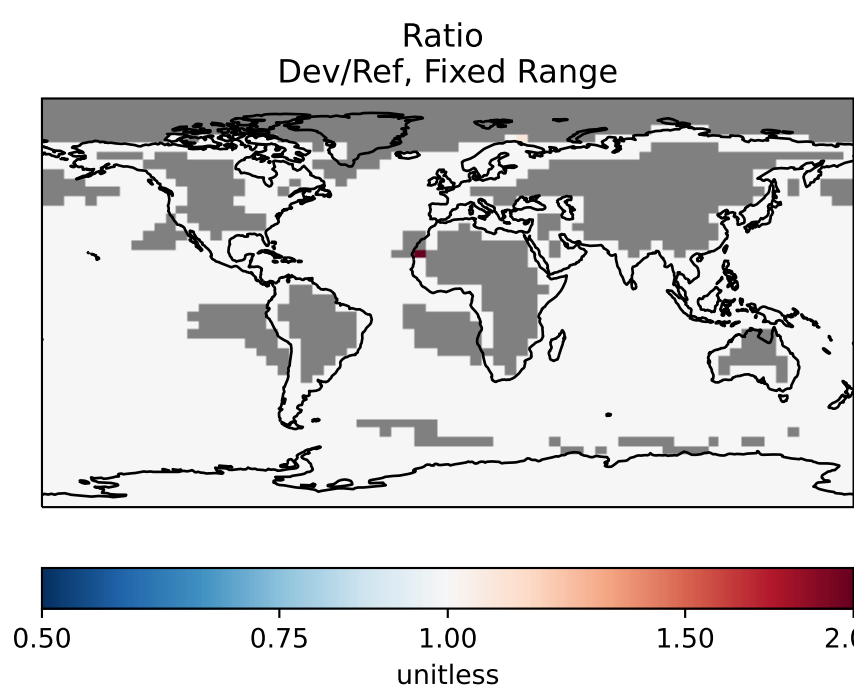
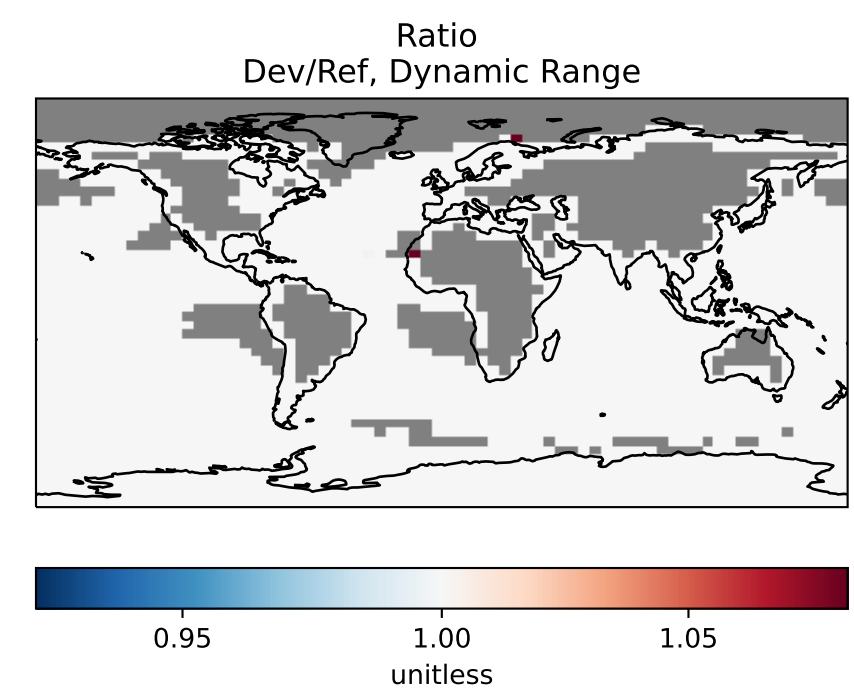
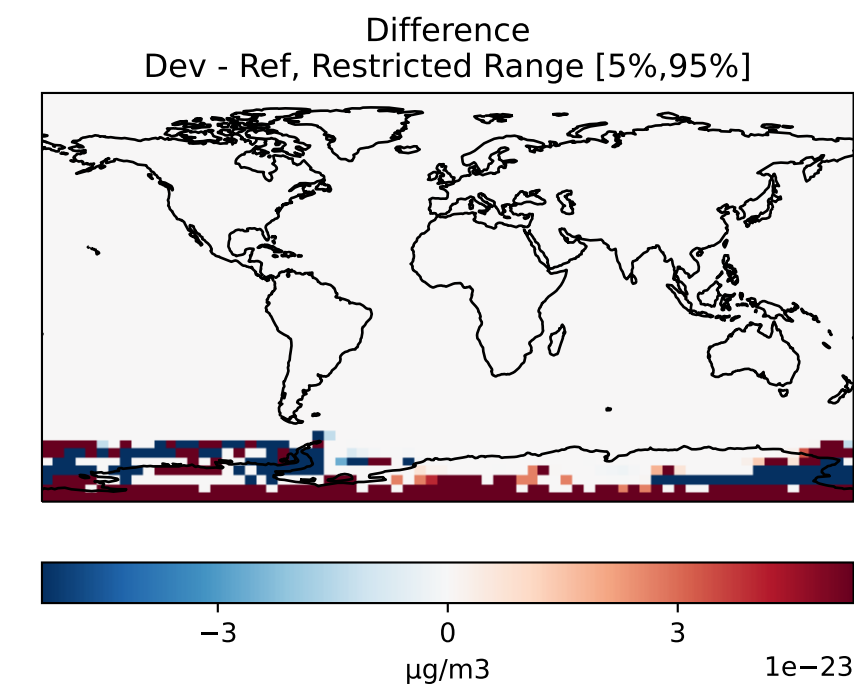
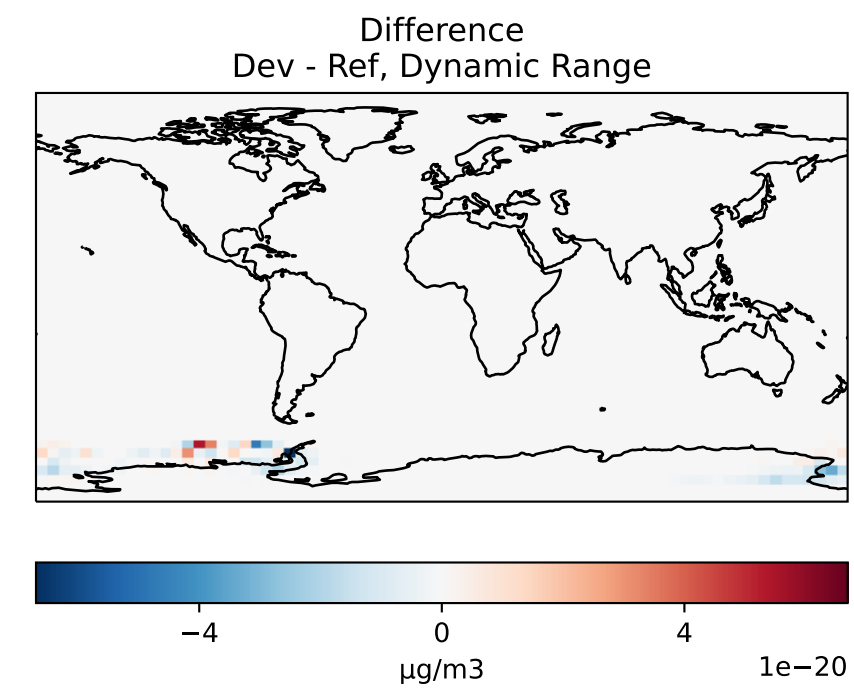
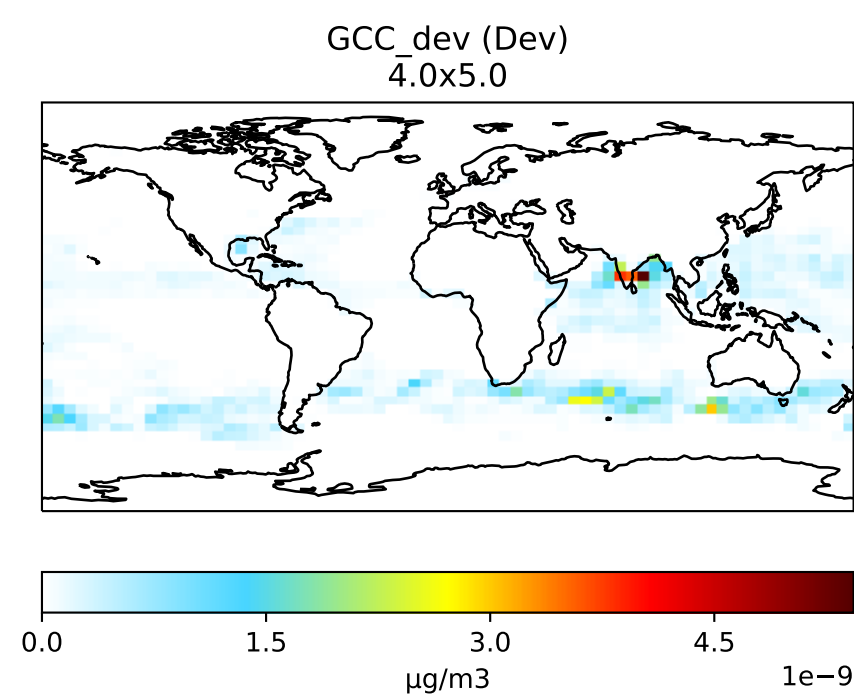
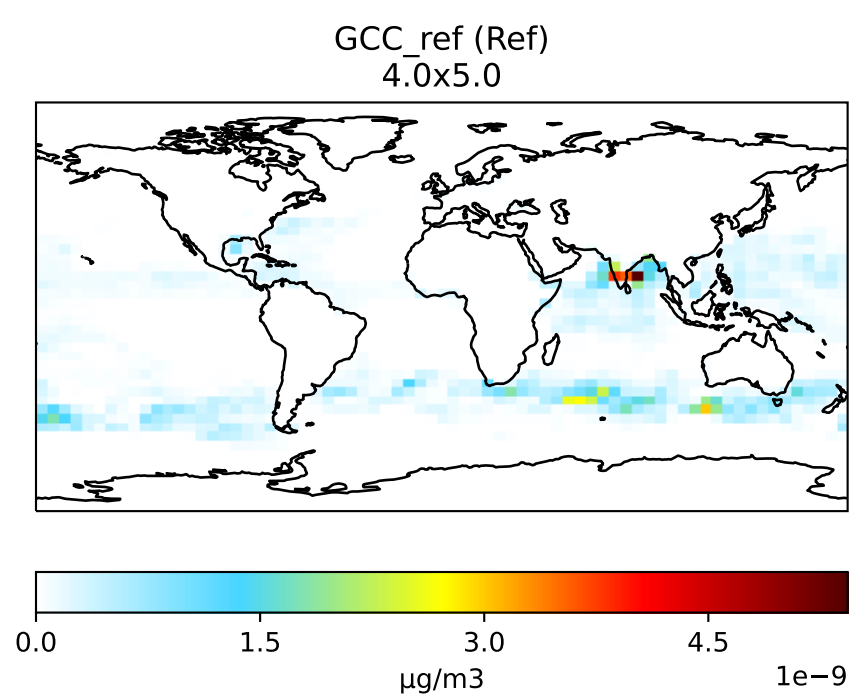
# SpeciesConcVV\_NITs



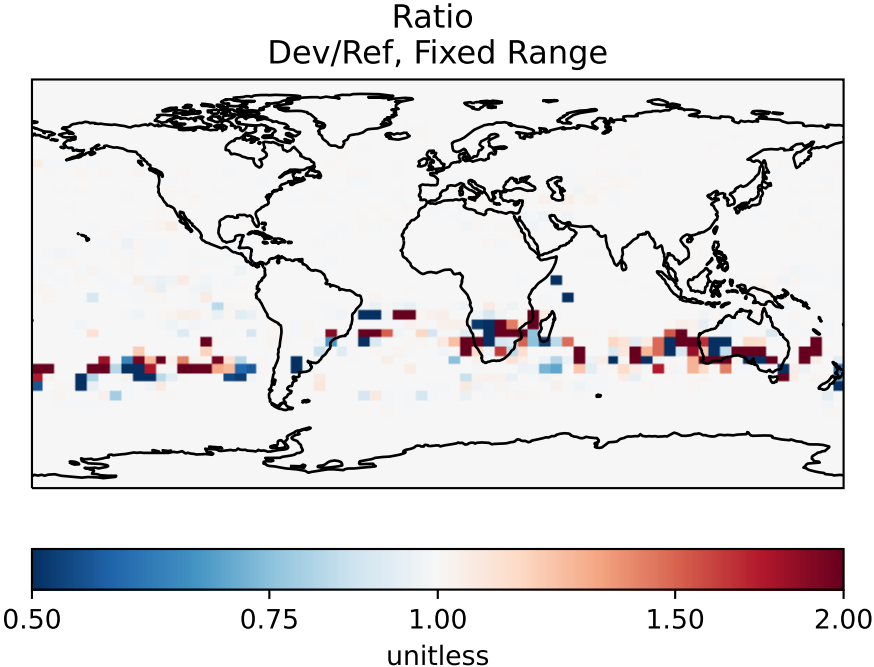
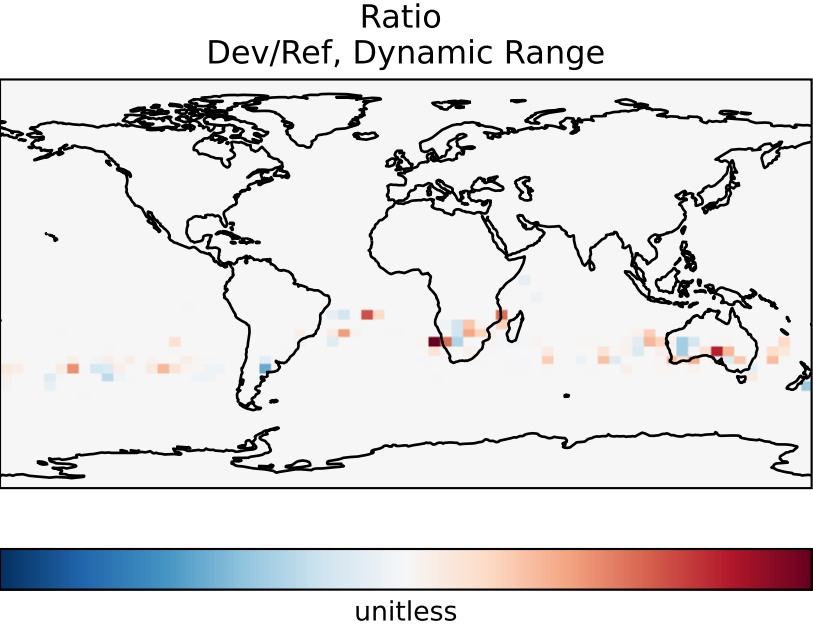
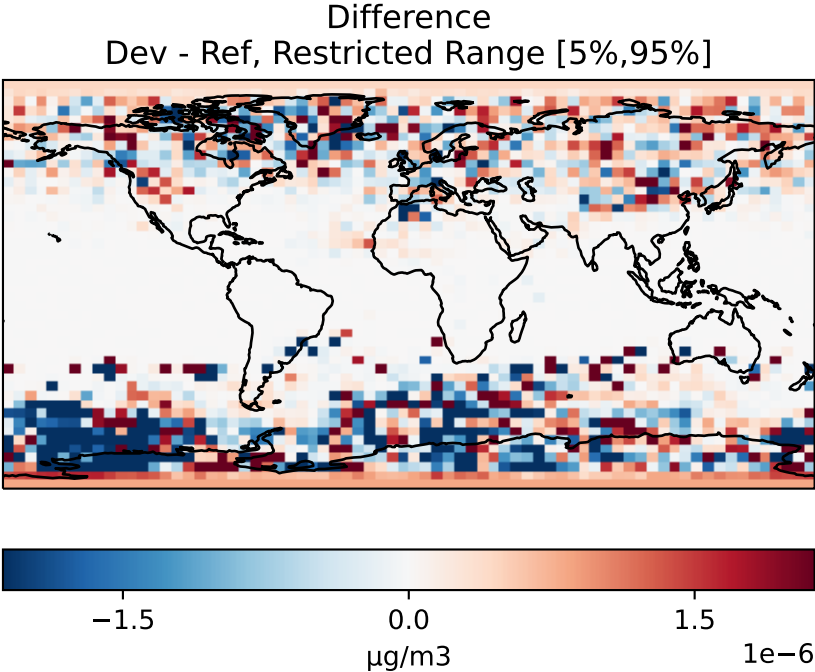
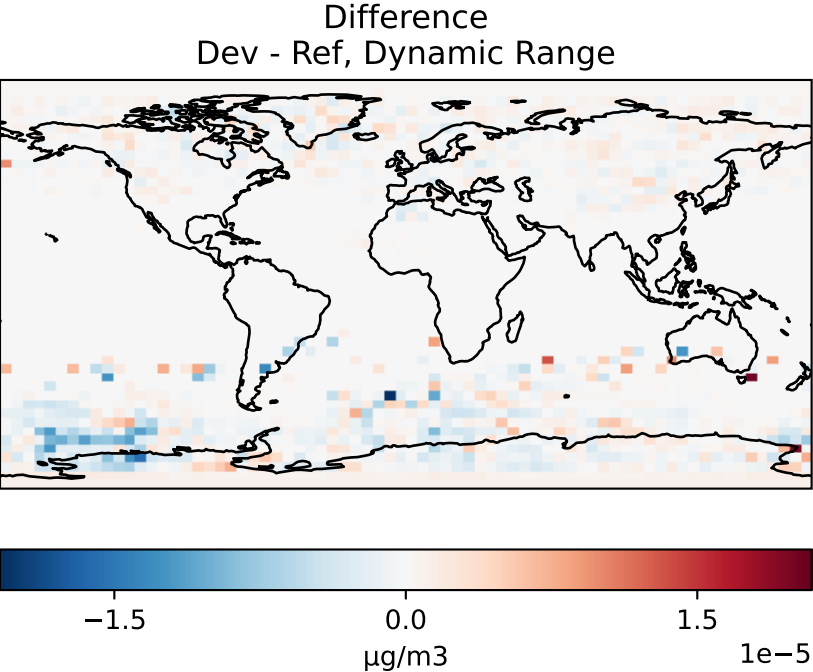
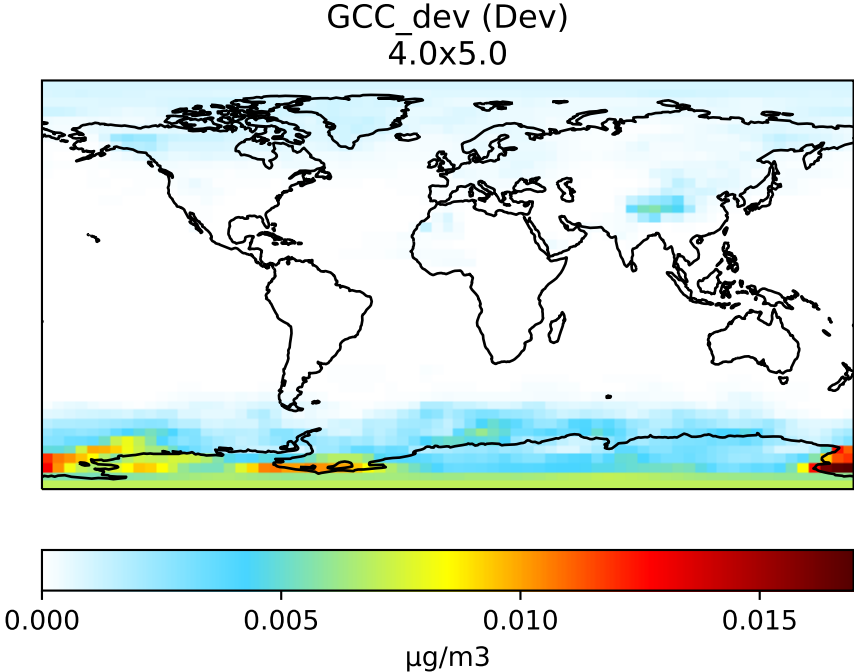
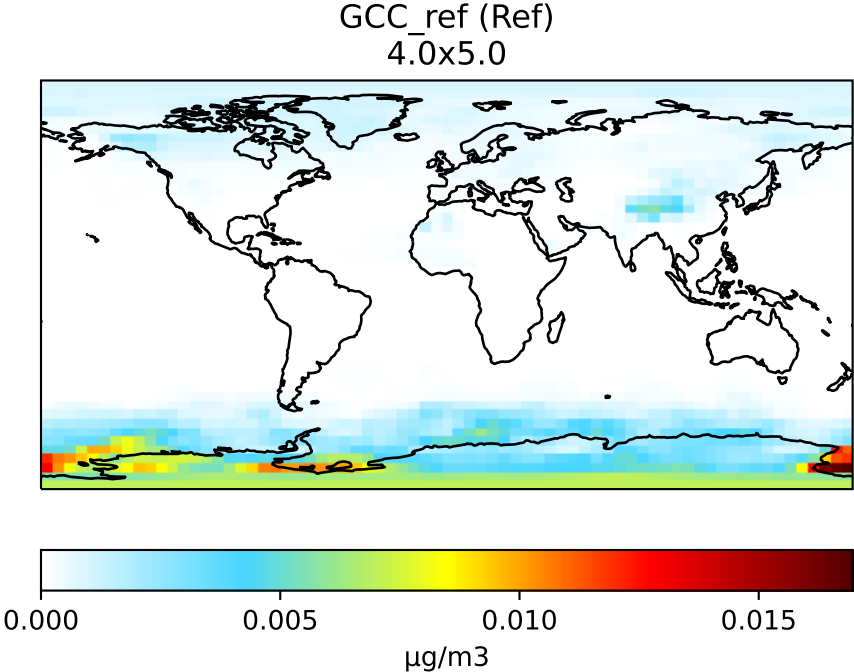
# SpeciesConcVV\_SALA



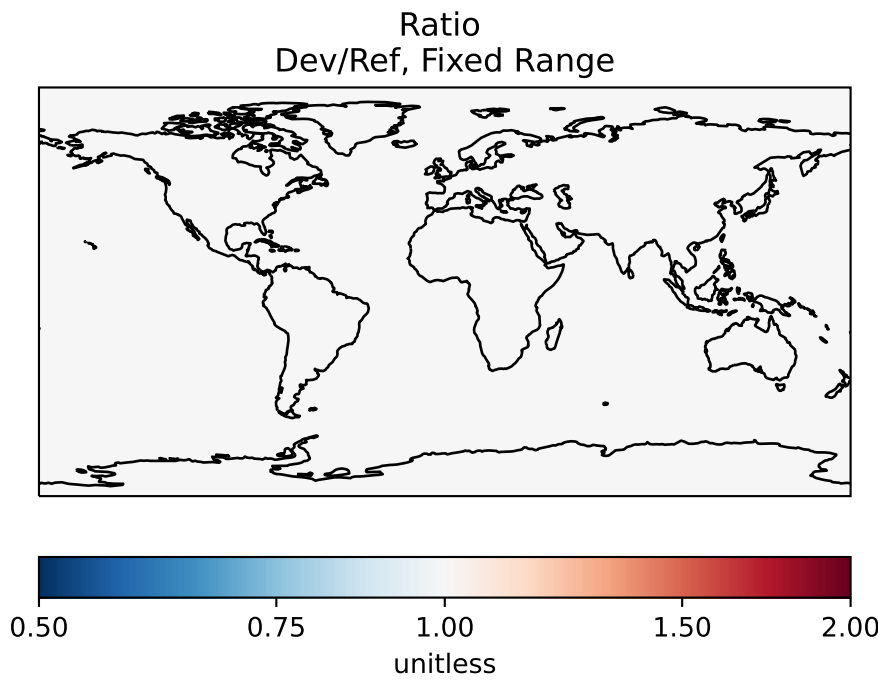
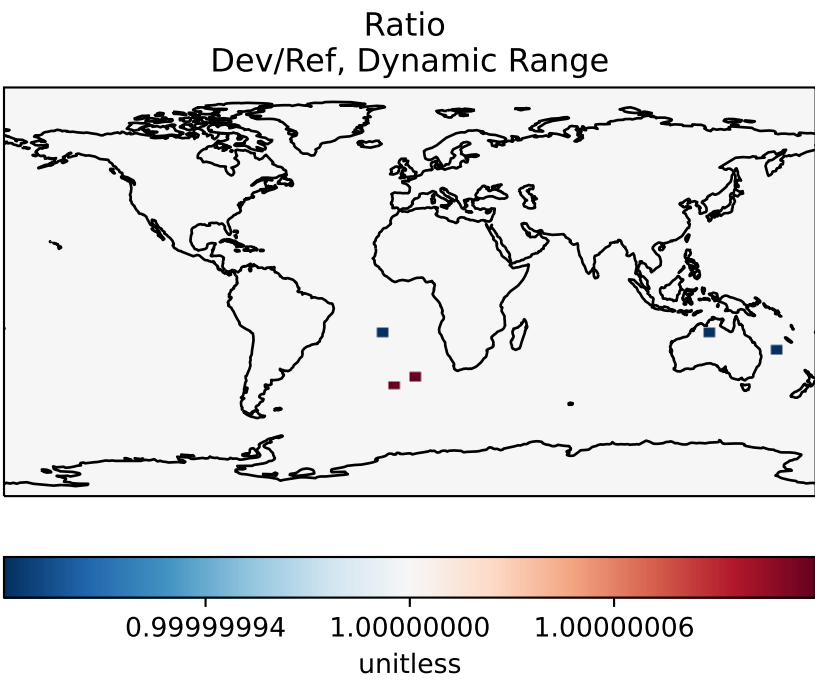
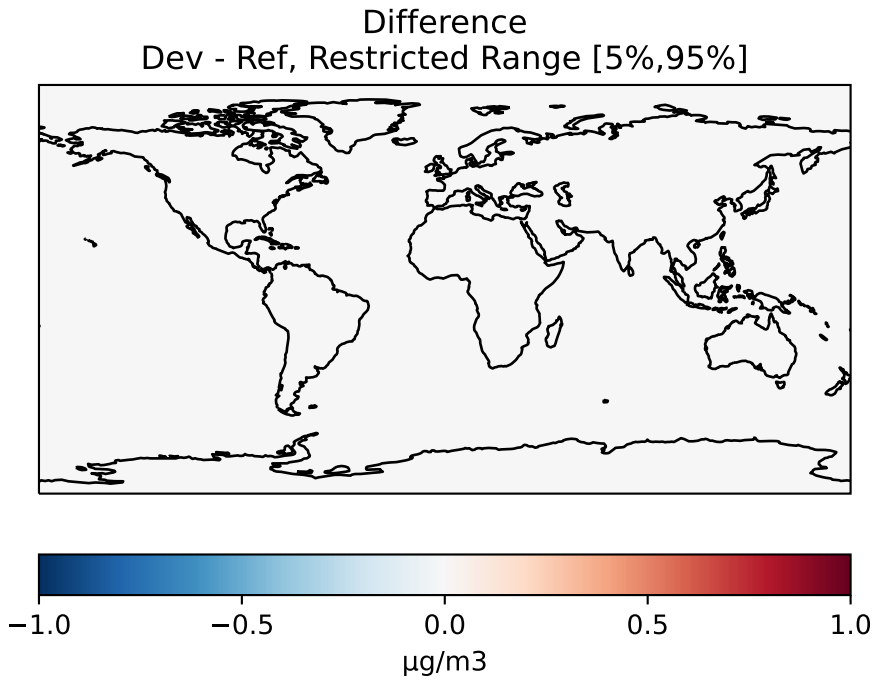
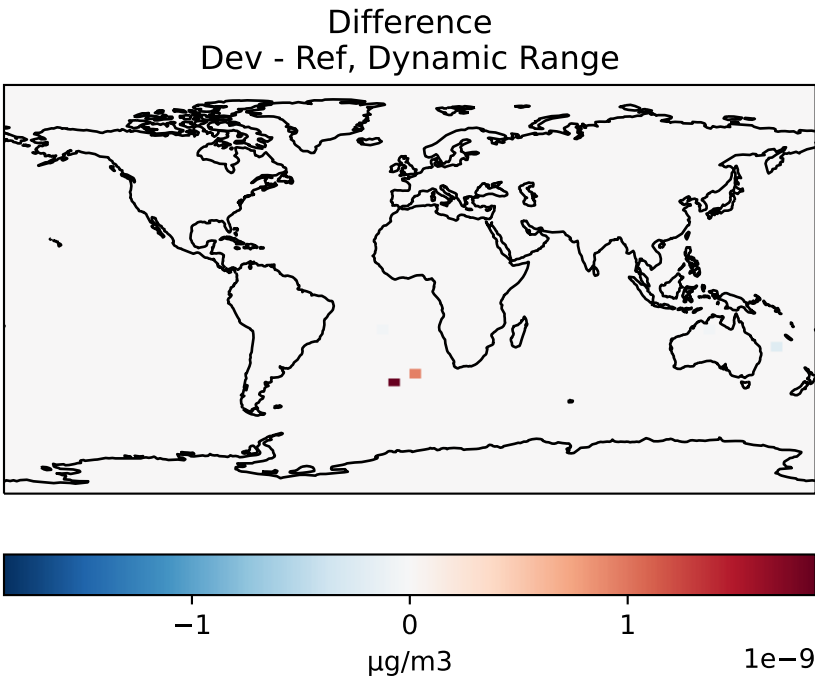
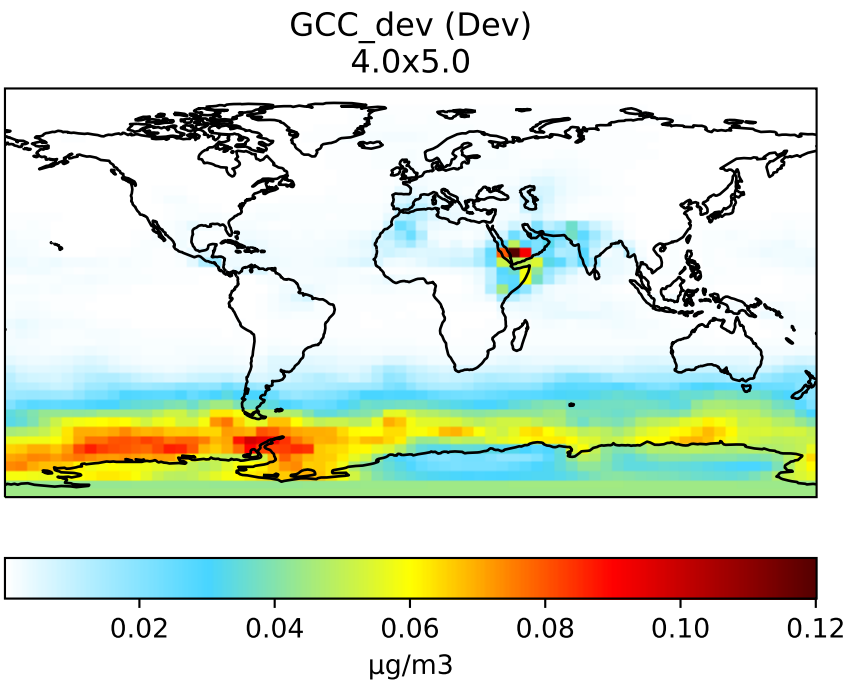
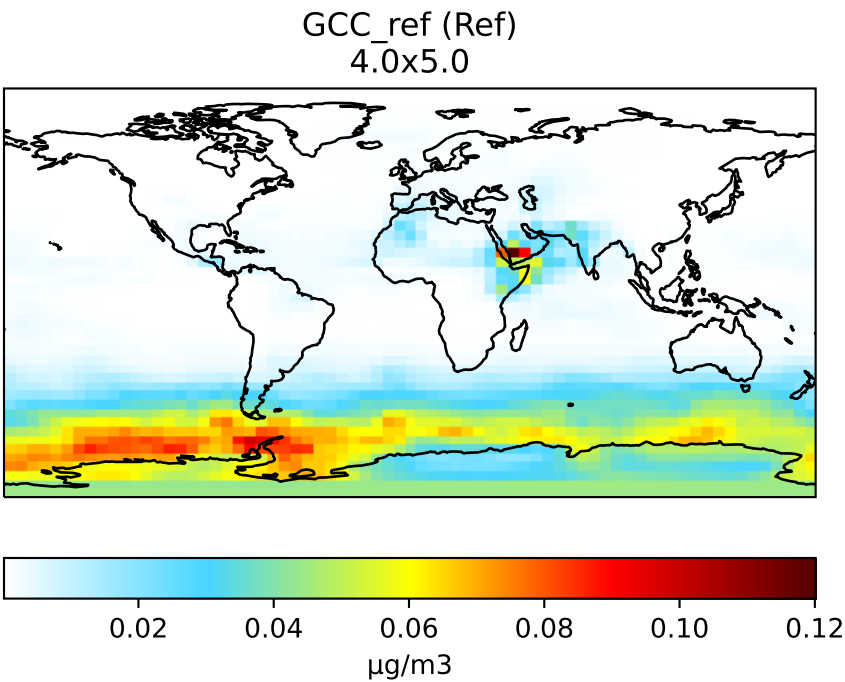
# SpeciesConcVV\_SALAAL



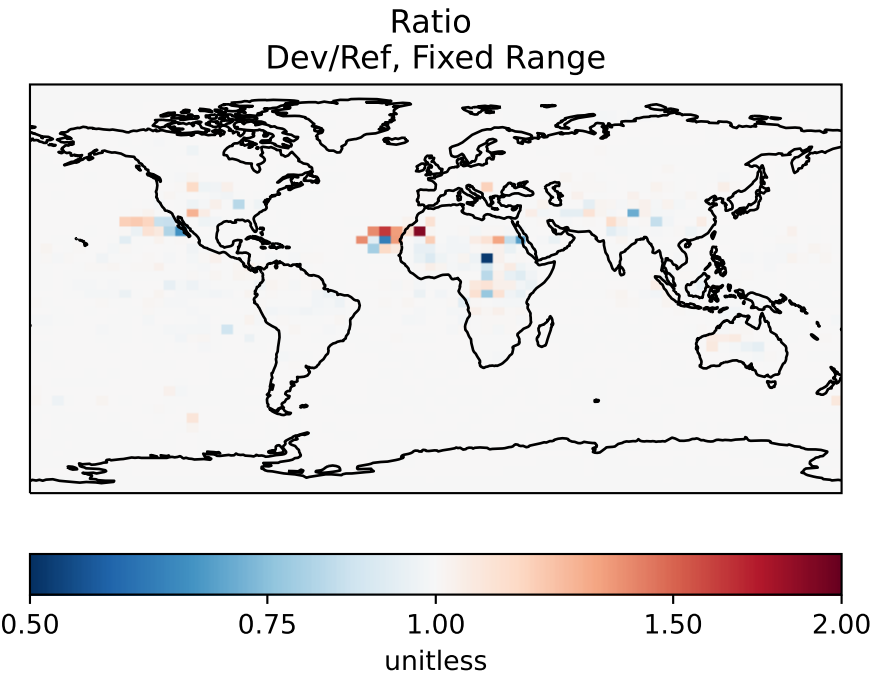
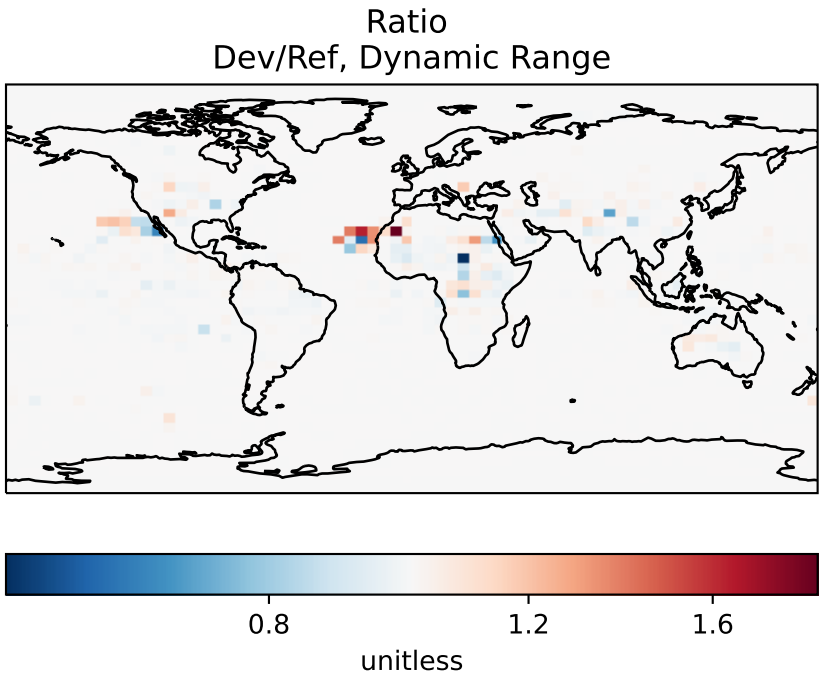
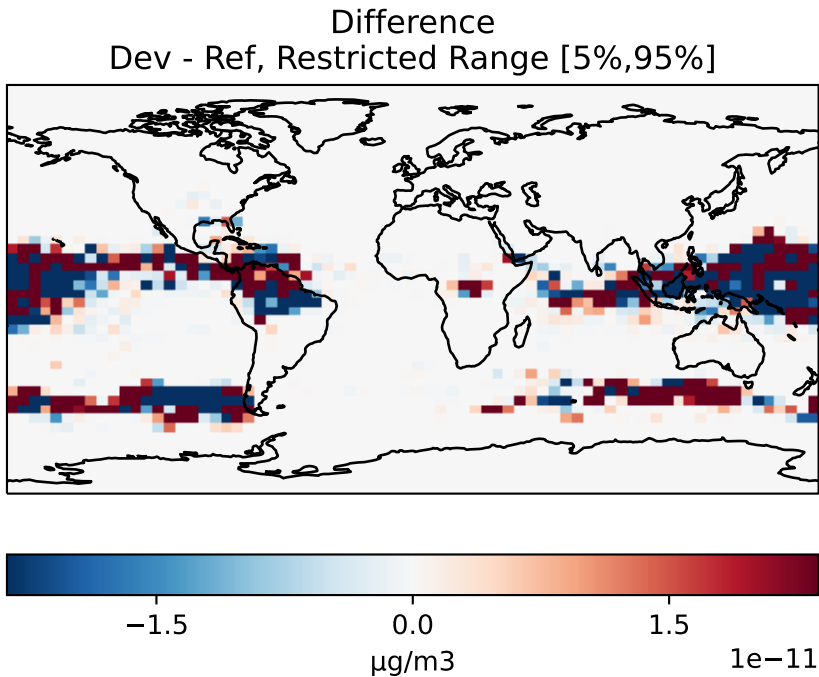
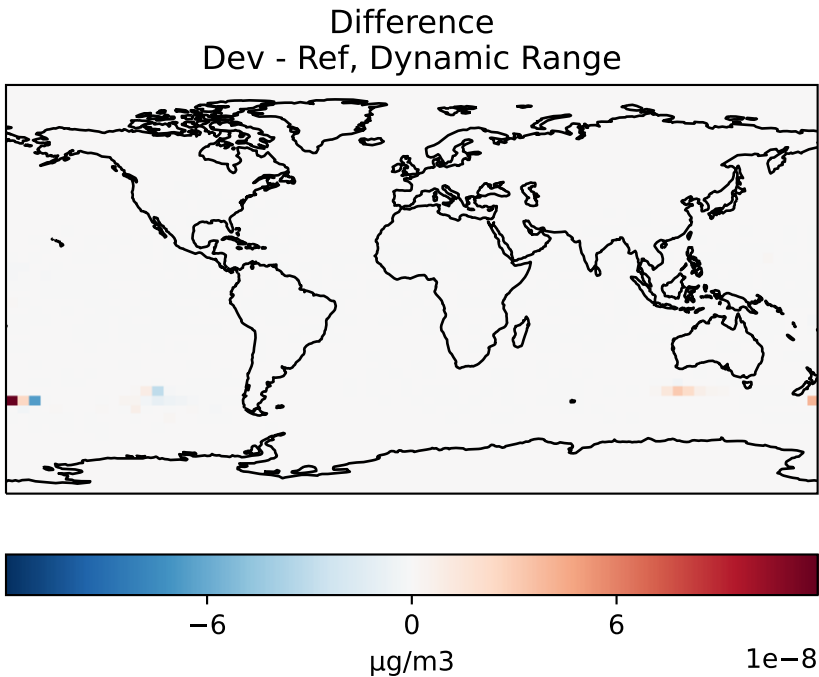
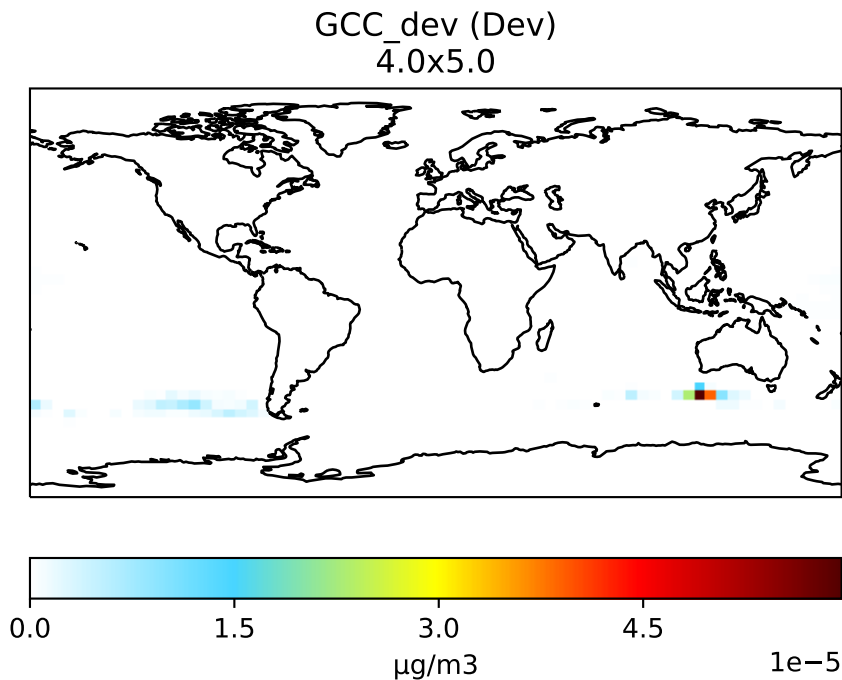
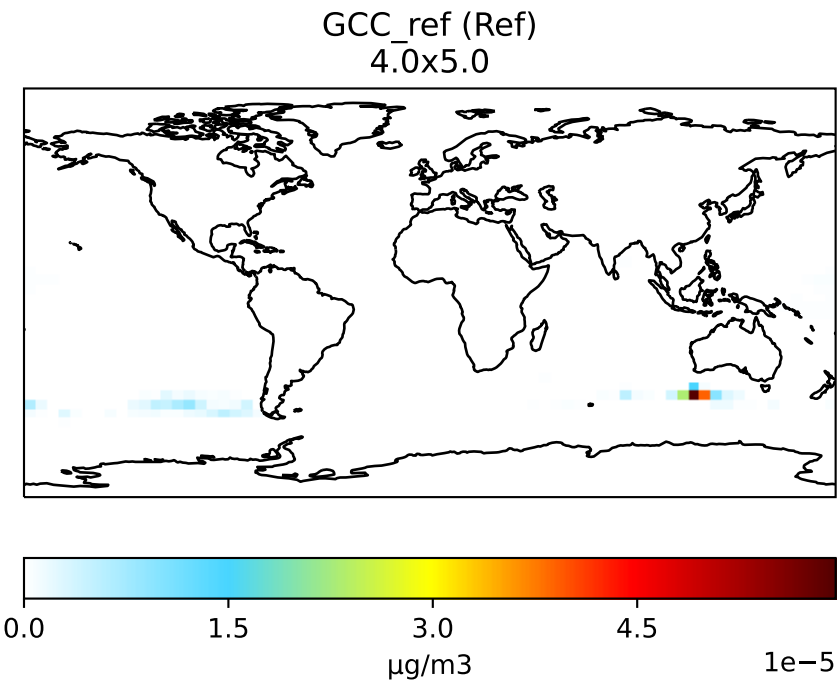
# SpeciesConcVV\_SALACL



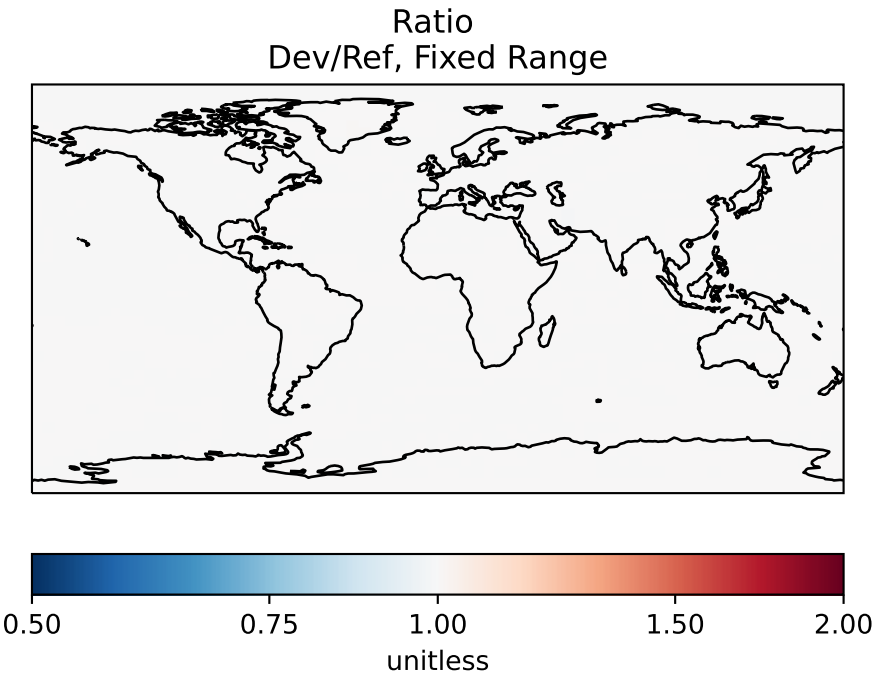
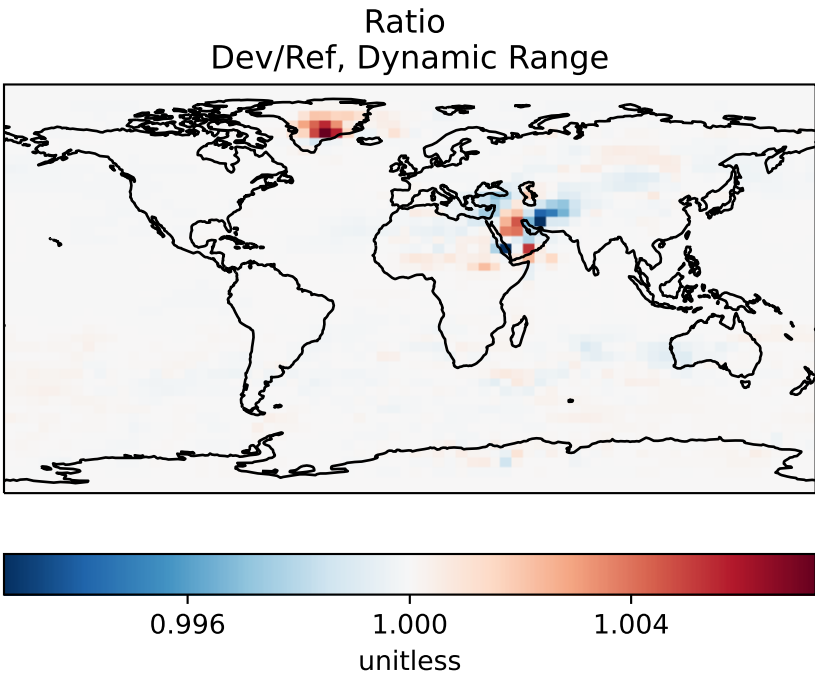
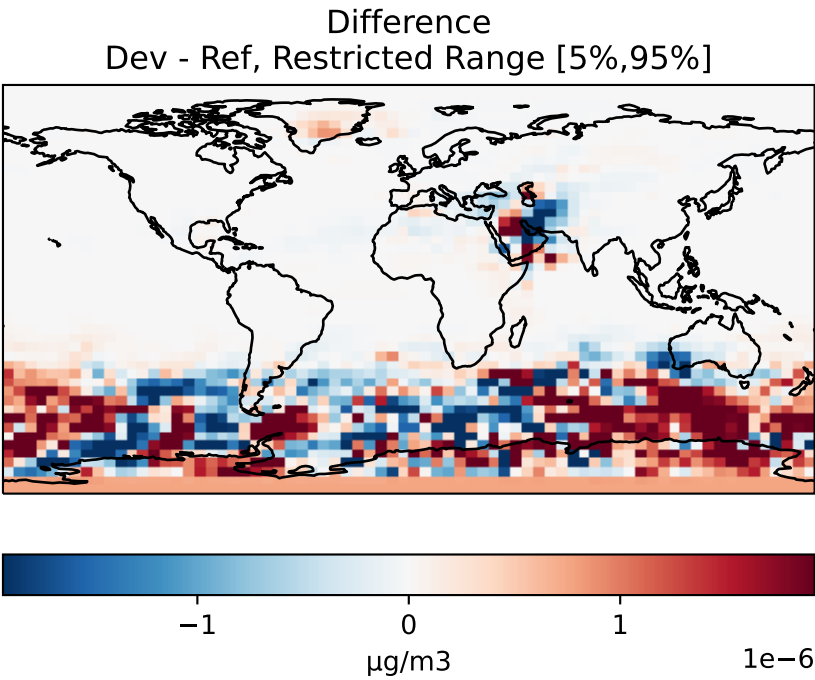
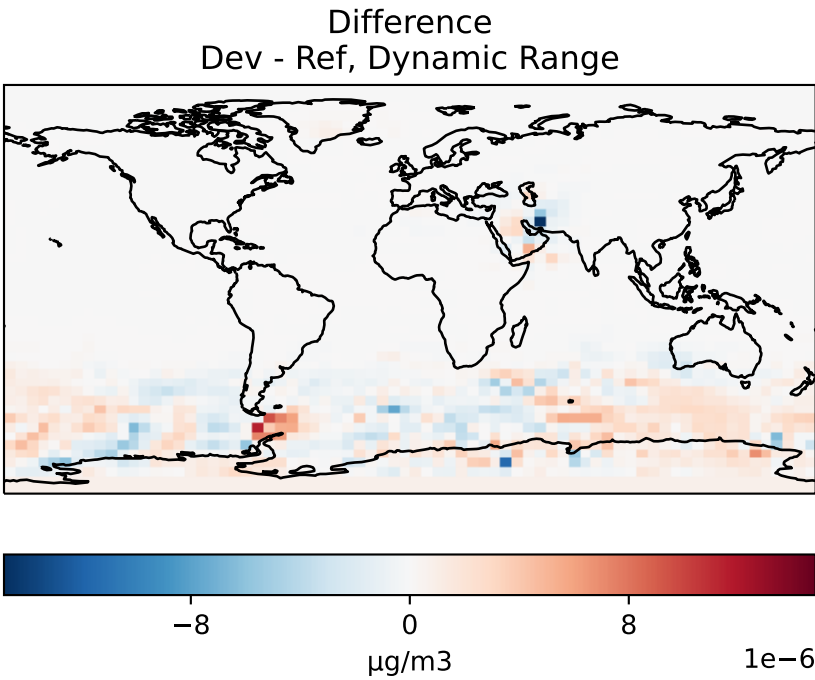
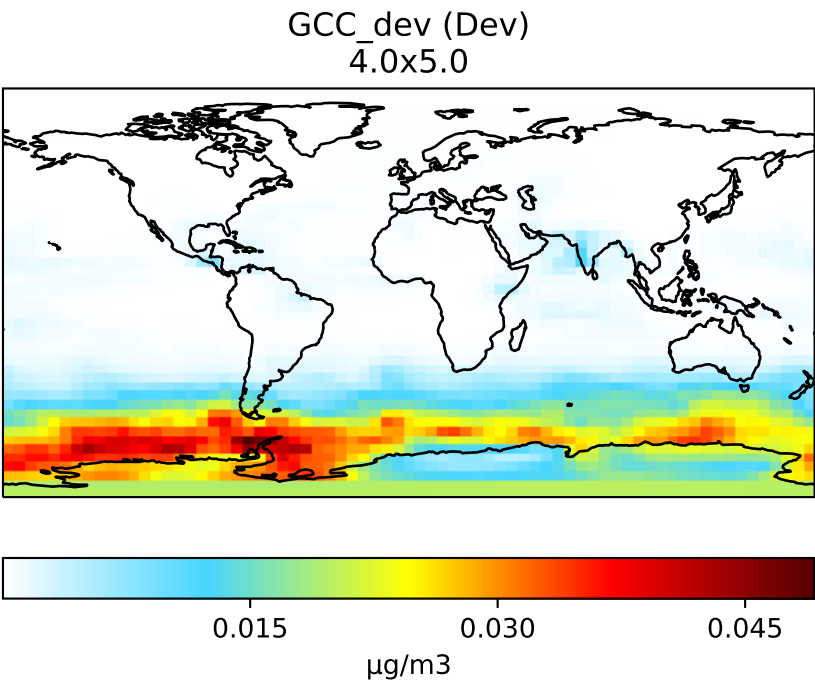
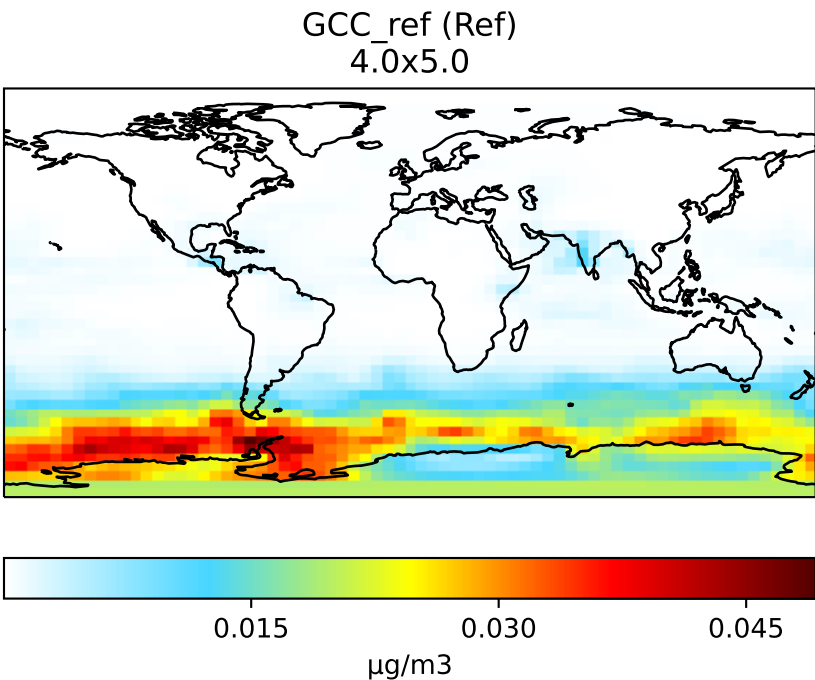
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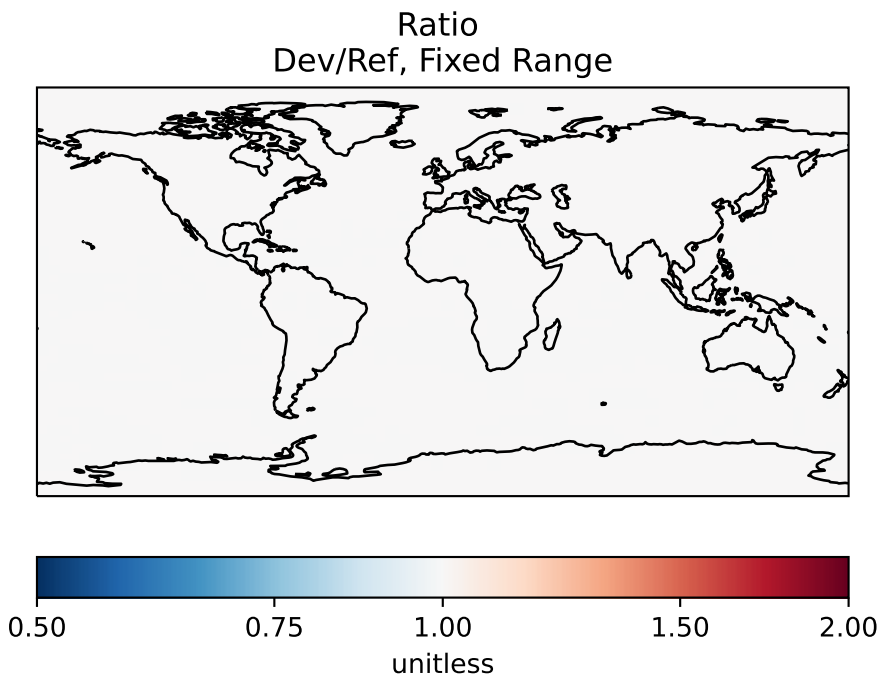
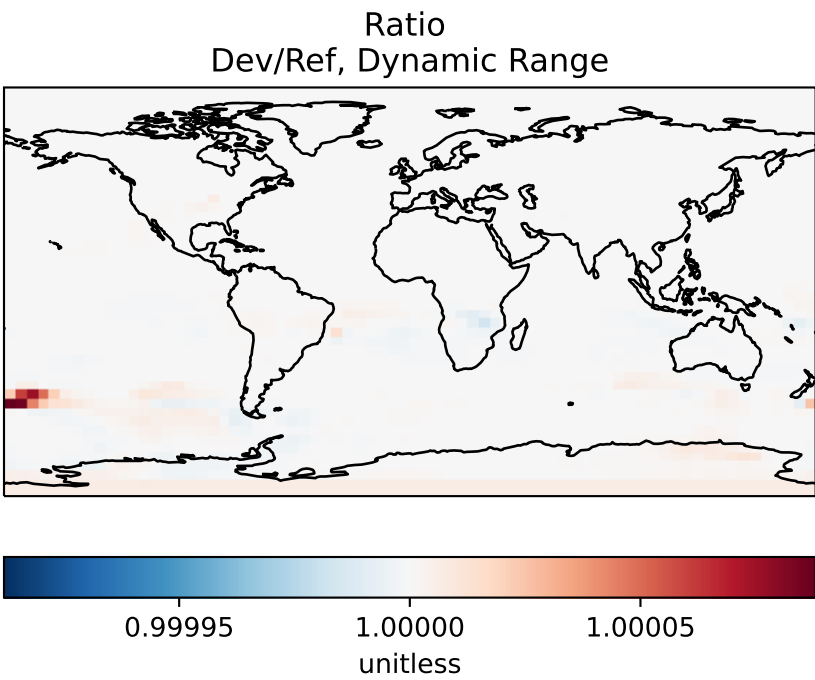
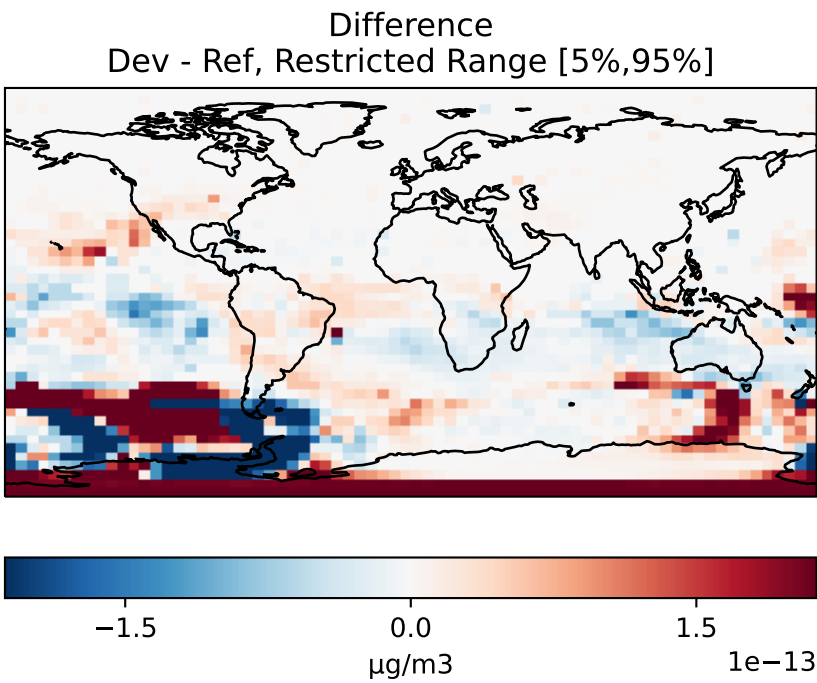
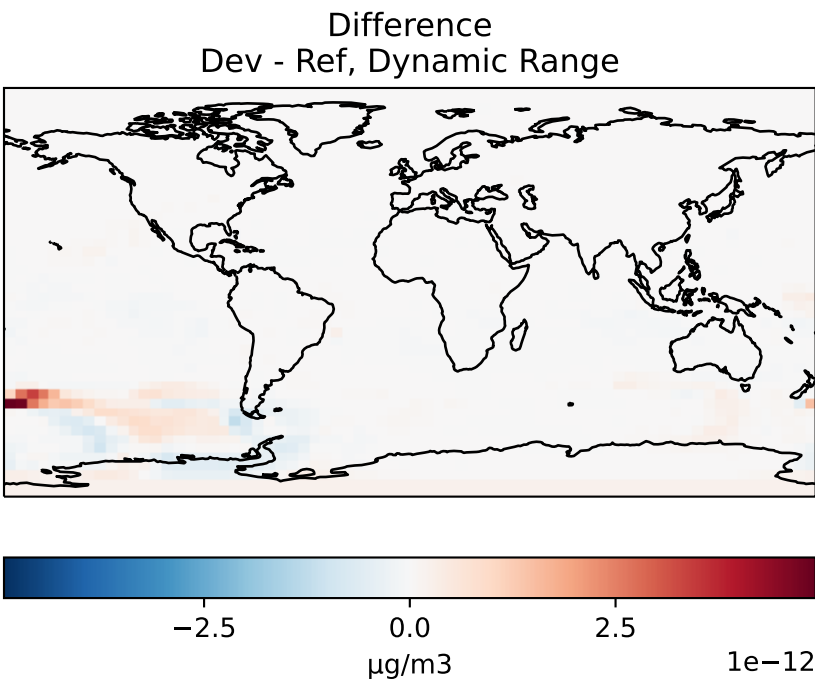
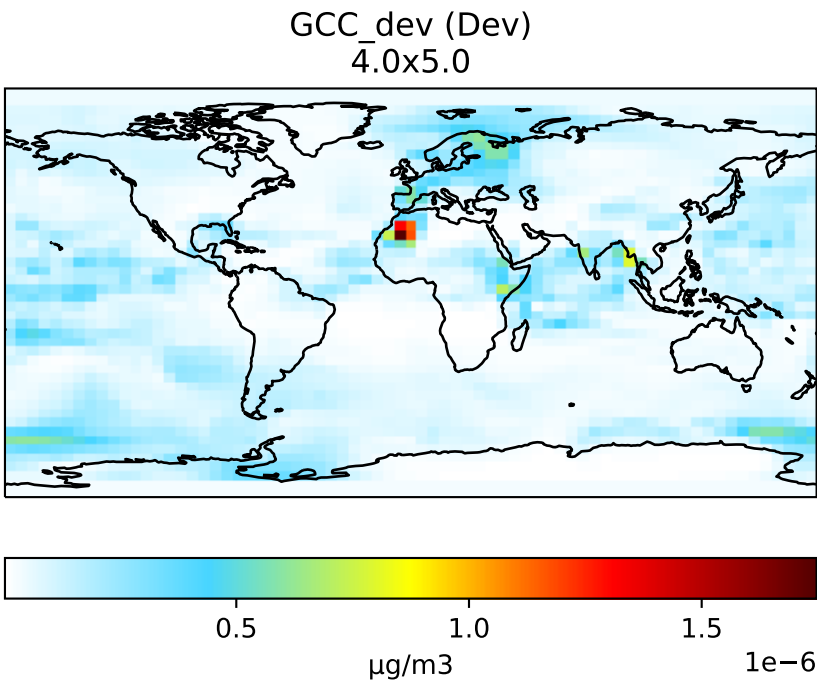
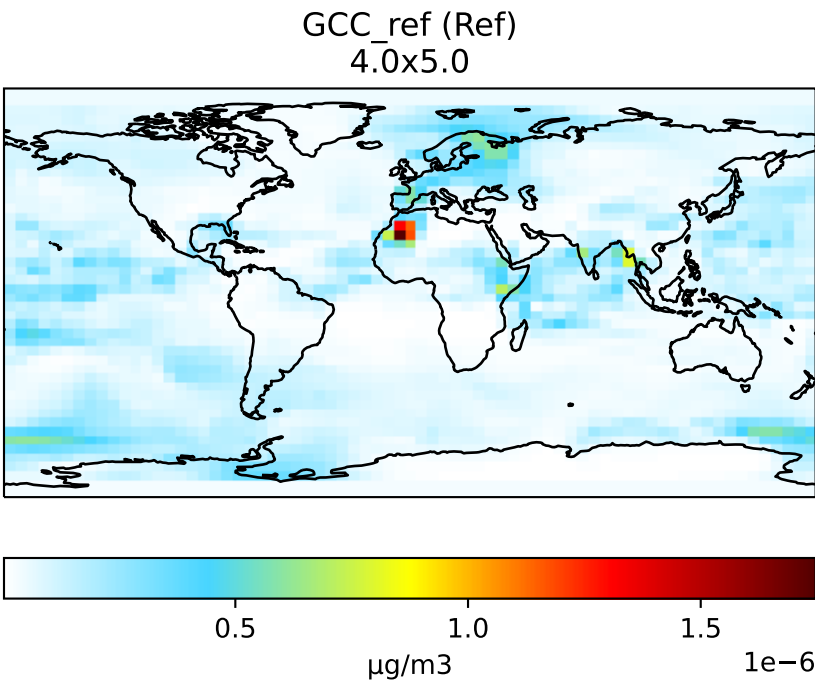
# SpeciesConcVV\_SALCAL



# SpeciesConcVV\_SALCCL

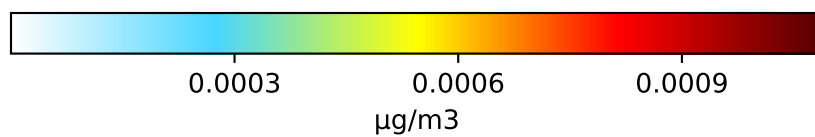
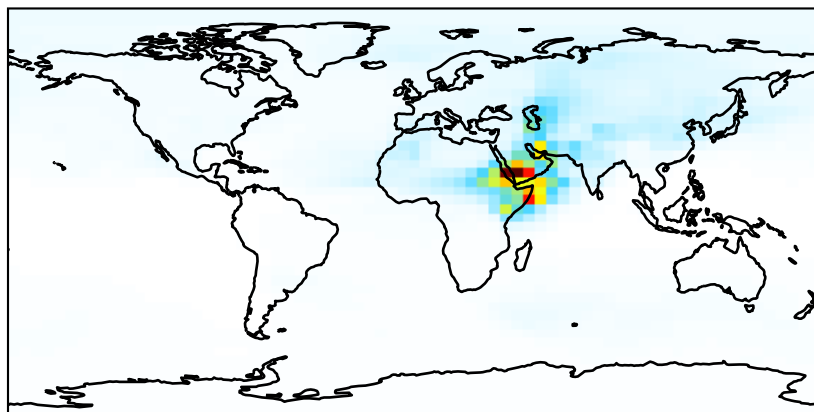


# SpeciesConcVV\_S04s

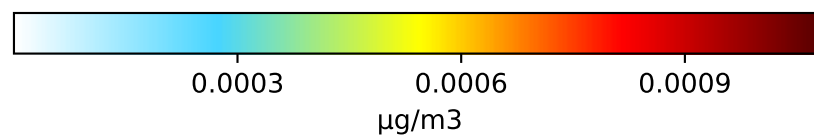
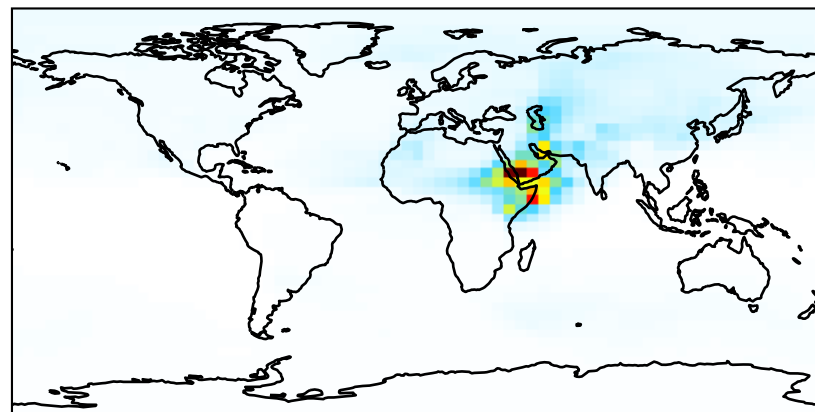


# SpeciesConcVV\_pFe

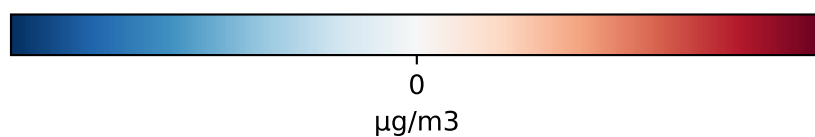
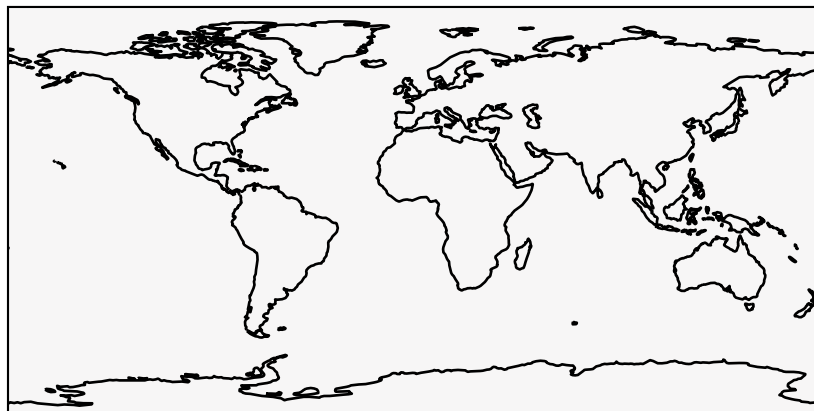
GCC\_ref (Ref)  
4.0x5.0



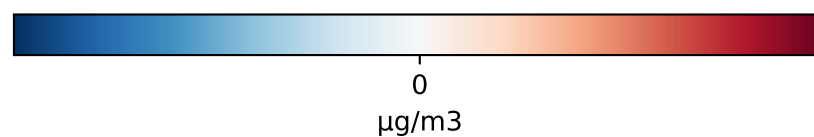
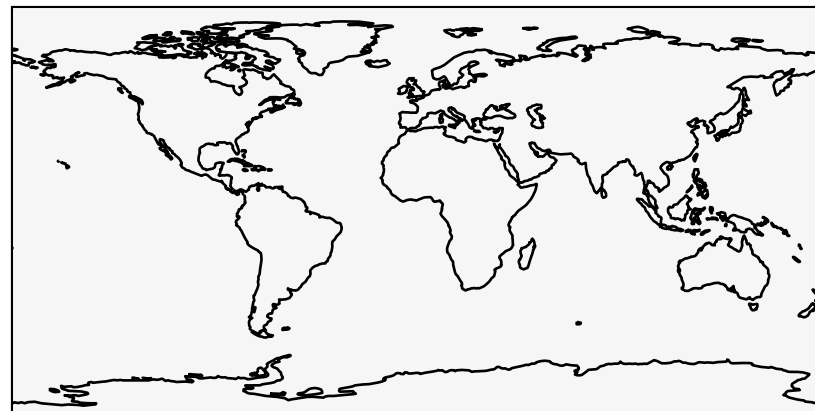
GCC\_dev (Dev)  
4.0x5.0



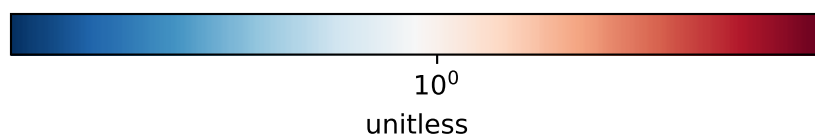
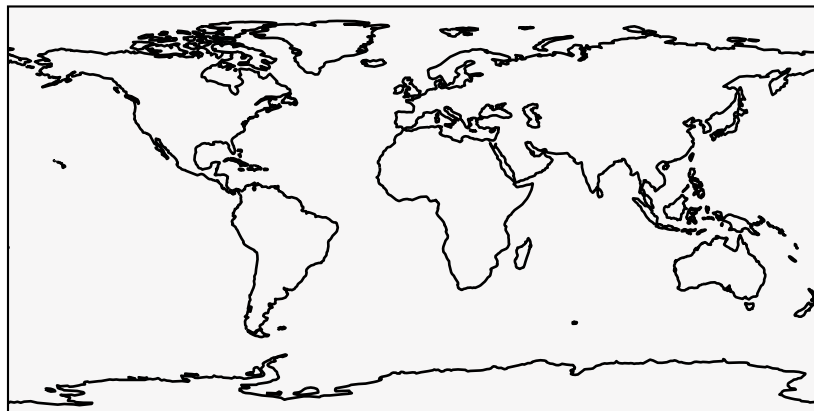
Difference  
Dev - Ref, Dynamic Range



Difference  
Dev - Ref, Restricted Range [5%,95%]



Ratio  
Dev/Ref, Dynamic Range



Ratio  
Dev/Ref, Fixed Range

